IC529
XC2VP20FF896

ROCKET_LDVS / ROCKET_LVDS / T / DP_RX_2A — ROCKET_LDVS / ROCKET_LVDS / T / DP_RX_2A — ROCKET_LDVS / ROCKET_LVDS / S / DP_TX_2A — ROCKET_LDVS / ROCKET_LVDS / S / DP_TX_2A
ROCKET_LDVS / ROCKET_LVDS / T / DP_RX_2B — ROCKET_LDVS / ROCKET_LVDS / T / DP_RX_2B — ROCKET_LDVS / ROCKET_LVDS / S / DP_TX_2B — ROCKET_LDVS / ROCKET_LVDS / S / DP_TX_2B
ROCKET_LDVS / ROCKET_LVDS / T / DP_RX_1A — ROCKET_LDVS / ROCKET_LVDS / T / DP_RX_1A — ROCKET_LDVS / ROCKET_LVDS / S / DP_TX_1A — ROCKET_LDVS / ROCKET_LVDS / S / DP_TX_1A
ROCKET_LDVS / ROCKET_LVDS / T / DP_RX_1B — ROCKET_LDVS / ROCKET_LVDS / T / DP_RX_1B — ROCKET_LDVS / ROCKET_LVDS / S / DP_TX_1B — ROCKET_LDVS / ROCKET_LVDS / S / DP_TX_1B

	A2 VCCAUX	A3 VRP_2	A4 RXN_2A	A5 RXP_2A	A6 TXP_2A	A7 TXN_2A	A8 Busy_2A	A9 GND	A10
B1 VCCAUX	B2 GND	B3 VRN_2	B4 AVCCAUXRX9	B5 VTRXPAD9	B6 AVCCAUXTX9	B7 VTTXPAD9	B8 LocalBAR(0)	B9	B10
C1 Module_En	C2 SYSFAIL_En	C3 GND	C4 LEDs(0)	C5 LEDs(1)	C6 GNDA9	C7	C8 LocalBAR(1)	C9	C10
D1	D2	D3 LEDs(2)	D4 GND	D5 LEDs(3)	D6	D7	D8 LocalBAR(2)	D9 GND	D10 IRQ0a_n
E1	E2	E3	E4	E5 GND	E6 VRN_1	E7 VRP_1	E8 LocalBAR(3)	E9 Spare_2A(0)	E10 Spare_2B(4)
F1	F2	F3	F4	F5 TDO	F6 GND	F7	F8 LocalBAR(4)	F9 Spare_2A(1)	F10
G1	G2	G3	G4	G5 ASP_Con_n	G6 DS2401	G7 TCK	G8 Spare_2A(4)	G9 Spare_2A(2)	G10
H1 GND	H2 Spare_3B(0)	H3	H4	H5	H6	H7 VBATT	H8 TMS	H9 Spare_2A(3)	H10
J1	J2	J3 Spare_3B(4)	J4 Spare_3B(3)	J5	J6	J7 Spare_3B(2)	J8 Spare_3B(1)	J9 VCCO_2	J10 VCCO_1
K1 Spare_3A(3)	K2 Spare_3A(2)	K3 Spare_3A(1)	K4 Spare_3A(0)	K5	K6	K7	K8	K9 VCCO_2	K10 VCCO_1
L1 HBR_n	L2 HBG_n	L3 GND	L4	L5 Spare_3A(4)	L6 GND	L7	L8	L9 VCCO_2	L10 VCCO_2
M1 Ack	M2 MS0_n	M3 MS1_n	M4 MS2_n	M5 MS3_n	M6 DMAR1_n	M7 DMAR2_n	M8	M9 VCCO_2	M10 VCCO_2
N1 RstSharc_n	N2 SharcAdr(0)	N3 SharcAdr(1)	N4 SharcAdr(2)	N5 SharcAdr(3)	N6 SharcAdr(4)	N7 SharcAdr(5)	N8	N9 VCCO_2	N10 VCCO_2
P1 SharcAdr(6)	P2 SharcAdr(7)	P3 SharcAdr(8)	P4 SharcAdr(9)	P5 SharcAdr(10)	P6 GND	P7 SharcAdr(11)	P8 SharcAdr(12)	P9 SharcAdr(13)	P10 VCCO_2
R1 VCCAUX	R2 SharcAdr(14)	R3 SharcAdr(15)	R4 SharcAdr(16)	R5 SharcAdr(17)	R6 SharcAdr(18)	R7 SharcAdr(19)	R8 SharcAdr(20)	R9 SharcAdr(21)	R10 VCCO_2
T1 VCCAUX	T2 SharcAdr(22)	T3 LRL(3)	T4 LRL(2)	T5 LRL(1)	T6 LRL(0)	T7 LDOWN_n	T8 LFF_n	T9 UWEN_n	T10 VCCO_3
U1 URESET_n	U2 UTEST_n	U3 UCTRL_n	U4 UD(31)	U5 UD(30)	U6 GND	U7 UD(29)	U8 UD(28)	U9 UD(27)	U10 VCCO_3
V1 UD(26)	V2 UD(25)	V3 UD(24)	V4 UD(23)	V5 UD(22)	V6 UD(21)	V7 UD(20)	V8 UD(19)	V9 VCCO_3	V10 VCCO_3
W1 UD(18)	W2 UD(17)	W3 UD(16)	W4 UD(15)	W5 UD(14)	W6 UD(13)	W7 UD(12)	W8 UD(11)	W9 VCCO_3	W10 VCCO_3
Y1 UD(10)	Y2 UD(9)	Y3 GND	Y4 UD(8)	Y5 UD(7)	Y6 GND	Y7	Y8	Y9 VCCO_3	Y10 VCCO_3
AA1 UD(6)	AA2 UD(5)	AA3 UD(4)	AA4 UD(3)	AA5 UD(2)	AA6 UD(1)	AA7	AA8	AA9 VCCO_3	AA10 VCCO_4
AB1 Spare_4B(2)	AB2 Spare_4B(1)	AB3 Spare_4B(0)	AB4 UD(0)	AB5	AB6	AB7	AB8	AB9 VCCO_3	AB10 VCCO_4
AC1 GND	AC2 Spare_4B(3)	AC3 Spare_4B(4)	AC4	AC5	AC6	AC7 CCLK	AC8 DONE	AC9 D0	AC10 Spare_4A(3)
AD1	AD2	AD3	AD4	AD5	AD6	AD7 PWRDWN_B	AD8	AD9 D1	AD10 Spare_4A(4)
AE1	AE2	AE3	AE4	AE5	AE6 GND	AE7	AE8	AE9	AE10
AF1	AF2	AF3	AF4	AF5 GND	AF6	AF7 INIT_B	AF8 IRQ2b_n	AF9 IRQ1b_n	AF10
AG1	AG2	AG3	AG4 GND	AG5	AG6 DOUT	AG7 D2	AG8 VRP_4	AG9 GND	AG10 IRQ0b_n
AH1	AH2	AH3 GND	AH4	AH5	AH6 GNDA16	AH7 D3	AH8 VRN_4	AH9	AH10
AJ1 VCCAUX	AJ2 GND	AJ3 VRP_3	AJ4 AVCCAUXRX16	AJ5 VTRXPAD16	AJ6 AVCCAUXTX16	AJ7 VTTXPAD16	AJ8	AJ9	AJ10
	AK2 VCCAUX	AK3 VRN_3	AK4 RXN_3B	AK5 RXP_3B	AK6 TXP_3B	AK7 TXN_3B	AK8 Busy_3B	AK9 GND	AK10

A11 RXN_2B	A12 RXP_2B	A13 TXP_2B	A14 TXN_2B	A15 VCCAUX	A16 VCCAUX	A17 RXN_1A	A18 RXP_1A	A19 TXP_1A	A20 TXN_1A
B11 AVCCAUXRX7	B12 VTRXPAD7	B13 AVCCAUXTX7	B14 VTTXPAD7	B15 Rst_n	B16 Rocket_XClk	B17 AVCCAUXRX6	B18 VTRXPAD6	B19 AVCCAUXTX6	B20 VTTXPAD6
C11	C12 GNDA7	C13 Busy_2B	C14 GND	C15	C16	C17 GND	C18 Busy_1A	C19 GNDA6	C20
D11 IRQ1a_n	D12 GND	D13 IRQ2a_n	D14 CSa_n	D15 FLAG0a	D16 VME_D(4)	D17 VME_D(12)	D18 VME_D(3)	D19 GND	D20 VME_D(11)
E11 Spare_2B(1)	E12	E13	E14 Redy_a	E15 FLAG1a	E16 VME_D(7)	E17 VME_D(15)	E18	E19	E20 VME_D(6)
F11	F12	F13 GND	F14	F15 SharcRd_n	F16	F17	F18 GND	F19	F20
G11 Spare_2B(2)	G12 RstSharcD_n	G13	G14	G15 SharcWr_n	G16 AS_n	G17 SMBData	G18 T_Alert_n	G19 RstSharcC_n	G20 AM10_Rst_n
H11 Spare_2B(3)	H12 Spare_2B(0)	H13	H14	H15 FLAG2a	H16 SWDEN_n	H17	H18 SMBClk	H19 LEAB_n	H20 DENO_n
J11 VCCO_1	J12 VCCO_1	J13 VCCO_1	J14	J15 FLAG3a	J16 R_W_n	J17	J18 VCCO_0	J19 VCCO_0	J20 VCCO_0
K11 VCCO_1	K12 VCCO_1	K13 VCCO_1	K14 VCCO_1	K15 VCCO_1	K16 VCCO_0	K17 VCCO_0	K18 VCCO_0	K19 VCCO_0	K20 VCCO_0
L11 GND	L12 VCCINT	L13 VCCINT	L14 VCCINT	L15 VCCINT	L16 VCCINT	L17 VCCINT	L18 VCCINT	L19 VCCINT	L20 GND
M11 VCCINT	M12 GND	M13 GND	M14 GND	M15 GND	M16 GND	M17 GND	M18 GND	M19 GND	M20 VCCINT
N11 VCCINT	N12 GND	N13 GND	N14 GND	N15 GND	N16 GND	N17 GND	N18 GND	N19 GND	N20 VCCINT
P11 VCCINT	P12 GND	P13 GND	P14 GND	P15 GND	P16 GND	P17 GND	P18 GND	P19 GND	P20 VCCINT
R11 VCCINT	R12 GND	R13 GND	R14 GND	R15 GND	R16 GND	R17 GND	R18 GND	R19 GND	R20 VCCINT
T11 VCCINT	T12 GND	T13 GND	T14 GND	T15 GND	T16 GND	T17 GND	T18 GND	T19 GND	T20 VCCINT
U11 VCCINT	U12 GND	U13 GND	U14 GND	U15 GND	U16 GND	U17 GND	U18 GND	U19 GND	U20 VCCINT
V11 VCCINT	V12 GND	V13 GND	V14 GND	V15 GND	V16 GND	V17 GND	V18 GND	V19 GND	V20 VCCINT
W11 VCCINT	W12 GND	W13 GND	W14 GND	W15 GND	W16 GND	W17 GND	W18 GND	W19 GND	W20 VCCINT
Y11 GND	Y12 VCCINT	Y13 VCCINT	Y14 VCCINT	Y15 VCCINT	Y16 VCCINT	Y17 VCCINT	Y18 VCCINT	Y19 VCCINT	Y20 GND
AA11 VCCO_4	AA12 VCCO_4	AA13 VCCO_4	AA14 VCCO_4	AA15 VCCO_4	AA16 VCCO_5	AA17 VCCO_5	AA18 VCCO_5	AA19 VCCO_5	AA20 VCCO_5
AB11 VCCO_4	AB12 VCCO_4	AB13 VCCO_4	AB14	AB15 FLAG3b	AB16 TestCon(11)	AB17 TestCon(12)	AB18 VCCO_5	AB19 VCCO_5	AB20 VCCO_5
AC11 Spare_4A(1)	AC12 Spare_4A(0)	AC13	AC14	AC15 FLAG2b	AC16 TestCon(10)	AC17 TestCon(13)	AC18 TestCon(14)	AC19 TestCon(4)	AC20 TestCon(3)
AD11 Spare_4A(2)	AD12 RstSharcE_n	AD13	AD14	AD15 FLAG1b	AD16 TestCon(9)	AD17 TestCon(7)	AD18 TestCon(15)	AD19 RstSharcF_n	AD20 TestCon(2)
AE11	AE12	AE13 GND	AE14 Redy_b	AE15 FLAG0b	AE16 TestCon(8)	AE17 TestCon(6)	AE18 GND	AE19	AE20
AF11	AF12	AF13	AF14 CSb_n	AF15 SDRAM_CLKin	AF16 SDRAM_CLK	AF17 TestCon(5)	AF18	AF19	AF20
AG11	AG12 GND	AG13	AG14 Busy_3A	AG15 SLINK_CLK	AG16 LHC_Clk	AG17 Busy_4B	AG18	AG19 GND	AG20
AH11	AH12 GNDA18	AH13	AH14 GND	AH15 SLINK_CLKin	AH16 Clkx2	AH17 GND	AH18	AH19 GNDA19	AH20
AJ11 AVCCAUXRX18	AJ12 VTRXPAD18	AJ13 AVCCAUXTX18	AJ14 VTTXPAD18	AJ15	AJ16 Clk	AJ17 AVCCAUXRX19	AJ18 VTRXPAD19	AJ19 AVCCAUXTX19	AJ20 VTTXPAD19
AK11 RXN_3A	AK12 RXP_3A	AK13 TXP_3A	AK14 TXN_3A	AK15 VCCAUX	AK16 VCCAUX	AK17 RXN_4B	AK18 RXP_4B	AK19 TXP_4B	AK20 TXN_4B

A21	A22 GND	A23 Busy_1B	A24 RXN_1B	A25 RXP_1B	A26 TXP_1B	A27 TXN_1B	A28 VRP_7	A29 VCCAUX	
B21	B22	B23 VME_D(8)	B24 AVCCAUXRX4	B25 VTRXPAD4	B26 AVCCAUXTX4	B27 VTTXPAD4	B28 VRN_7	B29 GND	B30 VCCAUX
C21	C22	C23 VME_D(9)	C24 VME_D(0)	C25 GNDA4	C26 Spare_1A(3)	C27 Spare_1A(2)	C28 GND	C29 Spare_1A(1)	C30 Spare_1A(0)
D21 VME_D(2)	D22 GND	D23 VME_D(10)	D24 VME_D(1)	D25 DXN	D26	D27 GND	D28 Spare_1A(4)	D29	D30
E21 VME_D(14)	E22 VME_D(5)	E23 VME_D(13)	E24 VRN_0	E25 VRP_0	E26 GND	E27	E28	E29	E30
F21 LWORD_n	F22 DS0_n	F23 DS1_n	F24 BERR_n	F25 GND	F26 TDI	F27	F28	F29	F30
G21 DTACK_OE_n	G22 DTACK_n	G23 WRITE_n	G24 PROG_B	G25	G26	G27	G28	G29	G30
H21 DENIN1_n	H22 DENIN0_n	H23 HSWAP_EN	H24 DXP	H25	H26	H27	H28	H29	H30 GND
J21 VCCO_0	J22 VCCO_7	J23 Spare_1B(1)	J24 Spare_1B(2)	J25	J26	J27 Spare_1B(3)	J28 Spare_1B(4)	J29	J30
K21 VCCO_0	K22 VCCO_7	K23 IACKIN_n	K24 GA_n(4)	K25 GA_n(3)	K26 GA_n(2)	K27 GA_n(1)	K28 GA_n(0)	K29 GAP_n	K30 Spare_1B(0)
L21 VCCO_7	L22 VCCO_7	L23 AM(2)	L24 AM(3)	L25 GND	L26 AM(4)	L27 AM(5)	L28 GND	L29 IACKOUT_n	L30 IACK_n
M21 VCCO_7	M22 VCCO_7	M23 VME_IRQ_n(2)	M24 VME_IRQ_n(3)	M25 VME_IRQ_n(4)	M26 VME_IRQ_n(5)	M27 VME_IRQ_n(6)	M28 VME_IRQ_n(7)	M29 AM(0)	M30 AM(1)
N21 VCCO_7	N22 VCCO_7	N23 VME_A(17)	N24 VME_A(18)	N25 VME_A(19)	N26 VME_A(20)	N27 VME_A(21)	N28 VME_A(22)	N29 VME_A(23)	N30 VME_IRQ_n(1)
P21 VCCO_7	P22 VME_A(5)	P23 VME_A(12)	P24 VME_A(6)	P25 GND	P26 VME_A(13)	P27 VME_A(7)	P28 VME_A(14)	P29 VME_A(15)	P30 VME_A(16)
R21 VCCO_7	R22 VME_A(1)	R23 VME_A(8)	R24 VME_A(2)	R25 VME_A(9)	R26 VME_A(3)	R27 VME_A(10)	R28 VME_A(4)	R29 VME_A(11)	R30 VCCAUX
T21 VCCO_6	T22 VME_A(31)	T23 VME_A(30)	T24 VME_A(29)	T25 VME_A(28)	T26 VME_A(27)	T27 VME_A(26)	T28 VME_A(25)	T29 VME_A(24)	T30 VCCAUX
U21 VCCO_6	U22 VME_D(23)	U23 VME_D(22)	U24 VME_D(21)	U25 GND	U26 VME_D(20)	U27 VME_D(19)	U28 VME_D(18)	U29 VME_D(17)	U30 VME_D(16)
V21 VCCO_6	V22 VCCO_6	V23 SDRAM_DQ(3)	V24 SDRAM_DQ(2)	V25 SDRAM_DQ(1)	V26 SDRAM_DQ(0)	V27 VME_D(27)	V28 VME_D(26)	V29 VME_D(25)	V30 VME_D(24)
W21 VCCO_6	W22 VCCO_6	W23 SDRAM_DQ(7)	W24 SDRAM_DQ(6)	W25 SDRAM_DQ(5)	W26 SDRAM_DQ(4)	W27 VME_D(31)	W28 VME_D(30)	W29 VME_D(29)	W30 VME_D(28)
Y21 VCCO_6	Y22 VCCO_6	Y23 SDRAM_A(11)	Y24 SDRAM_CS_n	Y25 GND	Y26 SDRAM_RAS_n	Y27 SDRAM_CAS_n	Y28 GND	Y29 SDRAM_WE_n	Y30 SDRAM_DQM(0)
AA21 VCCO_5	AA22 VCCO_6	AA23 SDRAM_DQ(16)	AA24 SDRAM_DQM(2)	AA25 SDRAM_A(2)	AA26 SDRAM_A(1)	AA27 SDRAM_A(0)	AA28 SDRAM_A(10)	AA29 SDRAM_BA(1)	AA30 SDRAM_BA(0)
AB21 VCCO_5	AB22 VCCO_6	AB23 SDRAM_DQ(24)	AB24 SDRAM_DQ(23)	AB25 SDRAM_DQ(22)	AB26 SDRAM_DQ(21)	AB27 SDRAM_DQ(20)	AB28 SDRAM_DQ(19)	AB29 SDRAM_DQ(18)	AB30 SDRAM_DQ(17)
AC21 TestCon(1)	AC22 D7	AC23 M2	AC24 M1	AC25 SDRAM_DQ(29)	AC26 SDRAM_DQ(28)	AC27 SDRAM_DQ(27)	AC28 SDRAM_DQ(26)	AC29 SDRAM_DQ(25)	AC30 GND
AD21 TestCon(0)	AD22 D6	AD23	AD24 M0	AD25 SDRAM_A(5)	AD26 SDRAM_A(4)	AD27 SDRAM_A(3)	AD28 SDRAM_DQM(3)	AD29 SDRAM_DQ(31)	AD30 SDRAM_DQ(30)
AE21 Z50_NET_TYPE TTC_n(7)	AE22 ROD_Busy	AE23	AE24	AE25 GND	AE26 SDRAM_CKE	AE27 SDRAM_A(9)	AE28 SDRAM_A(8)	AE29 SDRAM_A(7)	AE30 SDRAM_A(6)
AF21 Z50_NET_TYPE TTC_n(6)	AF22 Z50_NET_TYPE TTC_n(5)	AF23 Z50_NET_TYPE TTC_n(4)	AF24 RDWR_B	AF25 SDRAM_DQ(10)	AF26 GND	AF27 SDRAM_DQ(9)	AF28 SDRAM_DQ(8)	AF29 SDRAM_DQM(1)	AF30 SDRAM_A(12)
AG21 Z50_NET_TYPE TTC_n(2)	AG22 GND	AG23 VRN_5	AG24 D5	AG25 CS_B	AG26 SDRAM_DQ(12)	AG27 GND	AG28 SDRAM_DQ(11)	AG29 SDRAM_DQ(14)	AG30 SDRAM_DQ(13)
AH21	AH22 Z50_NET_TYPE TTC_n(3)	AH23 VRP_5	AH24 D4	AH25 GNDA21	AH26	AH27	AH28 GND	AH29	AH30 SDRAM_DQ(15)
AJ21	AJ22 Z50_NET_TYPE TTC_n(1)	AJ23 Z50_NET_TYPE TTC_n(0)	AJ24 AVCCAUXRX21	AJ25 VTRXPAD21	AJ26 AVCCAUXTX21	AJ27 VTTXPAD21	AJ28 VRP_6	AJ29 GND	AJ30 VCCAUX
AK21	AK22 GND	AK23 Busy_4A	AK24 RXN_4A	AK25 RXP_4A	AK26 TXP_4A	AK27 TXN_4A	AK28 VRN_6	AK29 VCCAUX	

ROCKET_LDVS / ROCKET_LVDS / T / DP_RX_3B — ROCKET_LDVS / ROCKET_LVDS / T / DP_RX_3B — ROCKET_LDVS / ROCKET_LVDS / S / DP_TX_3B — ROCKET_LDVS / ROCKET_LVDS / S / DP_TX_3B
ROCKET_LDVS / ROCKET_LVDS / T / DP_RX_3A — ROCKET_LDVS / ROCKET_LVDS / T / DP_RX_3A — ROCKET_LDVS / ROCKET_LVDS / S / DP_TX_3A — ROCKET_LDVS / ROCKET_LVDS / S / DP_TX_3A
ROCKET_LDVS / ROCKET_LVDS / T / DP_RX_4B — ROCKET_LDVS / ROCKET_LVDS / T / DP_RX_4B — ROCKET_LDVS / ROCKET_LVDS / S / DP_TX_4B — ROCKET_LDVS / ROCKET_LVDS / S / DP_TX_4B
ROCKET_LDVS / ROCKET_LVDS / T / DP_RX_4A — ROCKET_LDVS / ROCKET_LVDS / T / DP_RX_4A — ROCKET_LDVS / ROCKET_LVDS / S / DP_TX_4A — ROCKET_LDVS / ROCKET_LVDS / S / DP_TX_4A

Input FPGA Power pins:
VCCAUX (2V5) 16 pins
VCCINT (1V5) 32 pins
VCCO_# (3V3) 10 pins each
GND 124 pins

MROD-Out

Output FPGA

Proj: MROD-X | Proj.No: 38405

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Dim 420 x 297 mm