

IC501
ADSP21160N

A1	DATA(14)	A2	DATA(13)	A3	DATA(10)	A4	DATA(8)	A5	DATA(4)	A6	DATA(2)	A7	TDI	A8	TRST_n	A9	RESET_n	A10	RPBA	A11	RPBA	A12	IRQ0_n	A13	FLAG1	A14	TIMEP	A15	NC_A14	A16	NC_A15	A17	TPS1	A18	RPB1	A19	RCLK0	A20	DPO	A21	LOGAT(4)
B1	DATA(22)	B2	DATA(16)	B3	DATA(15)	B4	DATA(9)	B5	DATA(6)	B6	DATA(3)	B7	DATA(0)	B8	TRCK	B9	EMC_n	B10	IRQ2_n	B11	FLAG2	B12	FLAG3	B13	NC_C13	B14	NC_C14	B15	DTL	B16	TPS1a	B17	RCLK2	B18	RPB0	B19	TCLK0	B20	LOGAT(5)	B21	LOGAT(2)
C1	DATA(24)	C2	DATA(18)	C3	DATA(17)	C4	DATA(11)	C5	DATA(7)	C6	DATA(5)	C7	DATA(1)	C8	TMS	C9	TDO	C10	IRQ1_n	C11	FLAG2	C12	NC_C12	C13	NC_C13	C14	TCLK1	C15	DB1	C16	DB0	C17	LOGAT(7)	C18	LOGAT(6)	C19	LOGACK	C20	LOGAT(6)		
D1	DATA(28)	D2	DATA(25)	D3	DATA(20)	D4	DATA(19)	D5	DATA(12)	D6	VDDEXT_04	D7	VDDINT_07	D8	VDDEXT_08	D9	VDDEXT_09	D10	VDDEXT_010	D11	VDDEXT_011	D12	VDDEXT_012	D13	VDDINT_013	D14	VDDEXT_014	D15	TPS0a	D16	L1DATA(7)	D17	LOGCLK	D18	LOGAT(3)	D19	LOGAT(1)	D20	L1CLK		
E1	DATA(30)	E2	DATA(29)	E3	DATA(23)	E4	DATA(21)	E5	VDDEXT_05	E6	VDDINT_06	E7	VDDINT_07	E8	VDDINT_08	E9	VDDINT_09	E10	VDDINT_010	E11	GND	E12	VDDINT_012	E13	VDDINT_013	E14	VDDINT_014	E15	VDDINT_015	E16	VDDEXT_016	E17	L1DATA(6)	E18	L1DATA(5)	E19	L1ACK	E20	L1DATA(1)		
F1	DATA(34)	F2	DATA(33)	F3	DATA(27)	F4	DATA(26)	F5	VDDEXT_04	F6	VDDINT_04	F7	GND	F8	GND	F9	GND	F10	GND	F11	GND	F12	GND	F13	GND	F14	GND	F15	VDDINT_015	F16	VDDEXT_016	F17	L1DATA(4)	F18	L1DATA(3)	F19	L1DATA(0)	F20	L2DATA(7)		
G1	DATA(38)	G2	DATA(35)	G3	DATA(32)	G4	DATA(31)	G5	VDDEXT_05	G6	VDDINT_06	G7	GND	G8	GND	G9	GND	G10	GND	G11	GND	G12	GND	G13	GND	G14	GND	G15	VDDINT_015	G16	VDDEXT_016	G17	L1DATA(2)	G18	L2DATA(6)	G19	L2DATA(4)	G20	L2CLK		
H1	DATA(40)	H2	DATA(39)	H3	DATA(37)	H4	DATA(36)	H5	VDDEXT_05	H6	VDDINT_06	H7	GND	H8	GND	H9	GND	H10	GND	H11	GND	H12	GND	H13	GND	H14	GND	H15	VDDINT_015	H16	VDDEXT_016	H17	L2DATA(5)	H18	L2ACK	H19	L2DATA(3)	H20	L2DATA(1)		
J1	DATA(44)	J2	DATA(43)	J3	DATA(42)	J4	DATA(41)	J5	VDDEXT_05	J6	VDDINT_06	J7	GND	J8	GND	J9	GND	J10	GND	J11	GND	J12	GND	J13	GND	J14	GND	J15	VDDINT_015	J16	VDDEXT_016	J17	L2DATA(2)	J18	L2DATA(0)	J19	HRB_n	J20	HRB_n		
K1	CLK_CFG_0	K2	DATA(46)	K3	DATA(45)	K4	DATA(47)	K5	VDDEXT_05	K6	VDDINT_06	K7	GND	K8	GND	K9	GND	K10	GND	K11	GND	K12	GND	K13	GND	K14	GND	K15	VDDINT_015	K16	VDDEXT_016	K17	BR4_n	K18	BR4_n	K19	BR4_n	K20	BR4_n		
L1	CLKIN	L2	CLK_CFG_1	L3	AGND	L4	CLK_CFG_2	L5	VDDEXT_05	L6	VDDINT_06	L7	GND	L8	GND	L9	GND	L10	GND	L11	GND	L12	GND	L13	GND	L14	GND	L15	VDDINT_015	L16	VDDEXT_016	L17	BR2_n	L18	BR2_n	L19	BR2_n	L20	BR2_n		
M1	AVDD	M2	CLK_CFG_3	M3	CLKOUT	M4	NC_M4	M5	VDDEXT_05	M6	VDDINT_06	M7	GND	M8	GND	M9	GND	M10	GND	M11	GND	M12	GND	M13	GND	M14	GND	M15	VDDINT_015	M16	VDDEXT_016	M17	PAGE	M18	PAGE	M19	SBT_n	M20	PA_n		
N1	NC_N1	N2	NC_N2	N3	DATA(48)	N4	DATA(51)	N5	VDDEXT_05	N6	VDDINT_06	N7	GND	N8	GND	N9	GND	N10	GND	N11	GND	N12	GND	N13	GND	N14	GND	N15	VDDINT_015	N16	VDDEXT_016	N17	L3DATA(5)	N18	L3DATA(6)	N19	L3DATA(4)	N20	L3CLK		
P1	DATA(49)	P2	DATA(50)	P3	DATA(52)	P4	DATA(55)	P5	VDDEXT_05	P6	VDDINT_06	P7	GND	P8	GND	P9	GND	P10	GND	P11	GND	P12	GND	P13	GND	P14	GND	P15	VDDINT_015	P16	VDDEXT_016	P17	L3DATA(2)	P18	L3DATA(1)	P19	L3DATA(3)	P20	L3ACK		
Q1	DATA(53)	Q2	DATA(54)	Q3	DATA(57)	Q4	DATA(60)	Q5	VDDEXT_05	Q6	VDDINT_06	Q7	GND	Q8	GND	Q9	GND	Q10	GND	Q11	GND	Q12	GND	Q13	GND	Q14	GND	Q15	VDDINT_015	Q16	VDDEXT_016	Q17	L4DATA(5)	Q18	L4DATA(6)	Q19	L4DATA(7)	Q20	L3DATA(0)		
R1	DATA(56)	R2	DATA(58)	R3	DATA(59)	R4	DATA(63)	R5	VDDEXT_05	R6	VDDINT_06	R7	GND	R8	GND	R9	GND	R10	GND	R11	VDDINT_011	R12	VDDINT_012	R13	VDDINT_013	R14	VDDINT_014	R15	VDDINT_015	R16	VDDEXT_016	R17	L4DATA(3)	R18	L4ACK	R19	L4CLK	R20	L4DATA(4)		
S1	DATA(61)	S2	DATA(62)	S3	ADDR(3)	S4	ADDR(2)	S5	VDDEXT_05	S6	VDDEXT_06	S7	VDDEXT_07	S8	VDDEXT_08	S9	VDDEXT_09	S10	VDDEXT_010	S11	VDDEXT_011	S12	VDDEXT_012	S13	VDDEXT_013	S14	VDDEXT_014	S15	VDDEXT_015	S16	VDDEXT_016	S17	LOGAT(7)	S18	L4DATA(0)	S19	L4DATA(1)	S20	L4DATA(2)		
T1	ADDR(4)	T2	ADDR(6)	T3	ADDR(7)	T4	ADDR(10)	T5	ADDR(14)	T6	ADDR(18)	T7	ADDR(22)	T8	ADDR(25)	T9	ADDR(28)	T10	ID0	T11	ADDR(1)	T12	MS1_n	T13	CS_n	T14	RD_n	T15	RD_n	T16	DMAR2_n	T17	LOGAT(0)	T18	LOGAT(2)	T19	L5ACK	T20	L5DATA(6)		
U1	ADDR(5)	U2	ADDR(9)	U3	ADDR(12)	U4	ADDR(15)	U5	ADDR(17)	U6	ADDR(20)	U7	ADDR(23)	U8	ADDR(26)	U9	ADDR(29)	U10	ID1	U11	ADDR(0)	U12	BMS_n	U13	MS2_n	U14	CIF_n	U15	RDH_n	U16	DMAG2_n	U17	GND	U18	L5DATA(1)	U19	L5DATA(3)	U20	L5DATA(5)		
V1	ADDR(8)	V2	ADDR(11)	V3	ADDR(13)	V4	ADDR(16)	V5	ADDR(19)	V6	ADDR(21)	V7	ADDR(24)	V8	ADDR(27)	V9	ADDR(30)	V10	ADDR(31)	V11	ID2	V12	BRST	V13	MS0_n	V14	MS3_n	V15	WRH_n	V16	WRL_n	V17	DMAG1_n	V18	DMAR1_n	V19	EBOOT	V20	L5CLK		
W1	ADDR(9)	W2	ADDR(11)	W3	ADDR(13)	W4	ADDR(16)	W5	ADDR(19)	W6	ADDR(21)	W7	ADDR(24)	W8	ADDR(27)	W9	ADDR(30)	W10	ADDR(31)	W11	GND	W12	BRST	W13	MS0_n	W14	MS3_n	W15	WRH_n	W16	WRL_n	W17	DMAG1_n	W18	DMAR1_n	W19	GND	W20	L5CLK		

Clock Configuration:
CLK_CFG(3:0) = "0010"
=> Core / CLKIN Ration 2:1

SHARC A
ID = "001"

Booting Mode:
EBOOT = '0', LBOOT = '0', BMS_n = '1' (Input)
=> Host Port Booting

SHARC Power pins:
VDDINT (1V9) 40 pins
VDDEXT (3V3) 43 pins
GND 82 pins
NC 9 pins

MROD-Out		Rev	V2	3
		Date	7 Feb 2006	
SHARC-A		Time	1:25:29 pm	
Proj: MROD-X	Proj.No: 38405	Name	tonvr	
Peter Jansweijer	peterj@nikhef.nl	Size	A3	4 1 4 A
NIKHEF <small>© ET-Nikhef Amsterdam</small>		<small>NATIONAAL INSTITUUT VOOR KERN-FYSICA EN HOGE ENERGIE-FYSICA KRUISLAAN 409, 020-592 2000 1098 SJ AMSTERDAM NEDERLAND</small>		
		Dim	420 x 297 mm	
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