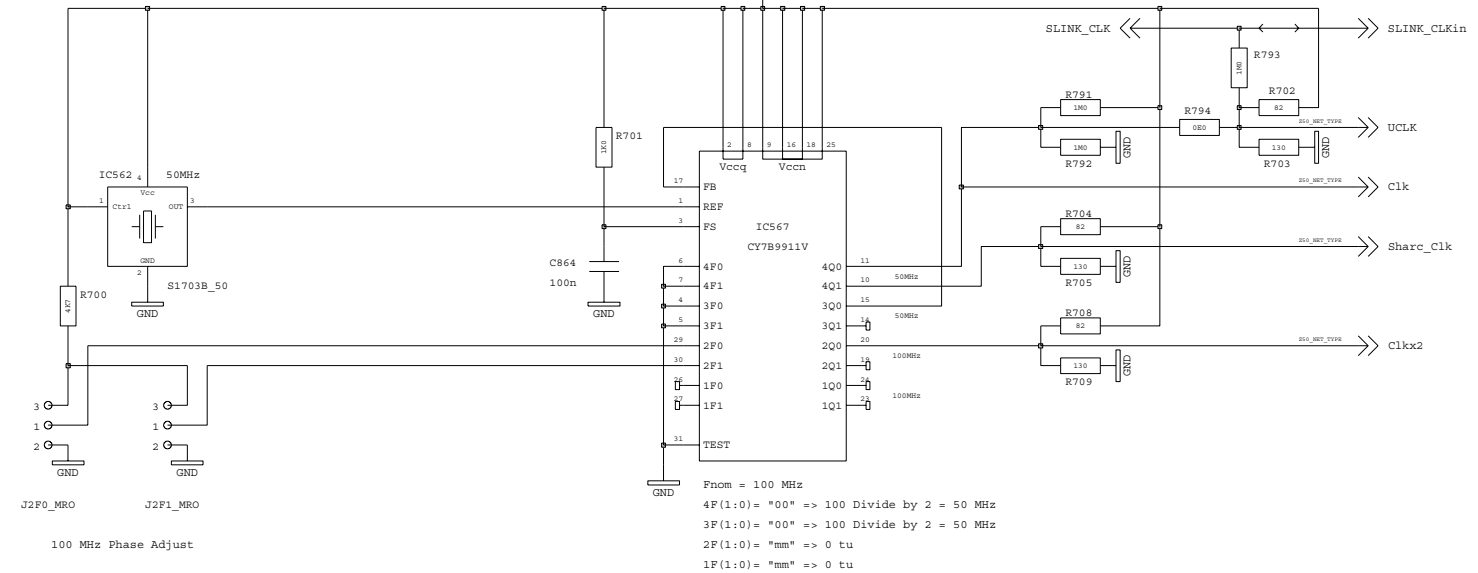
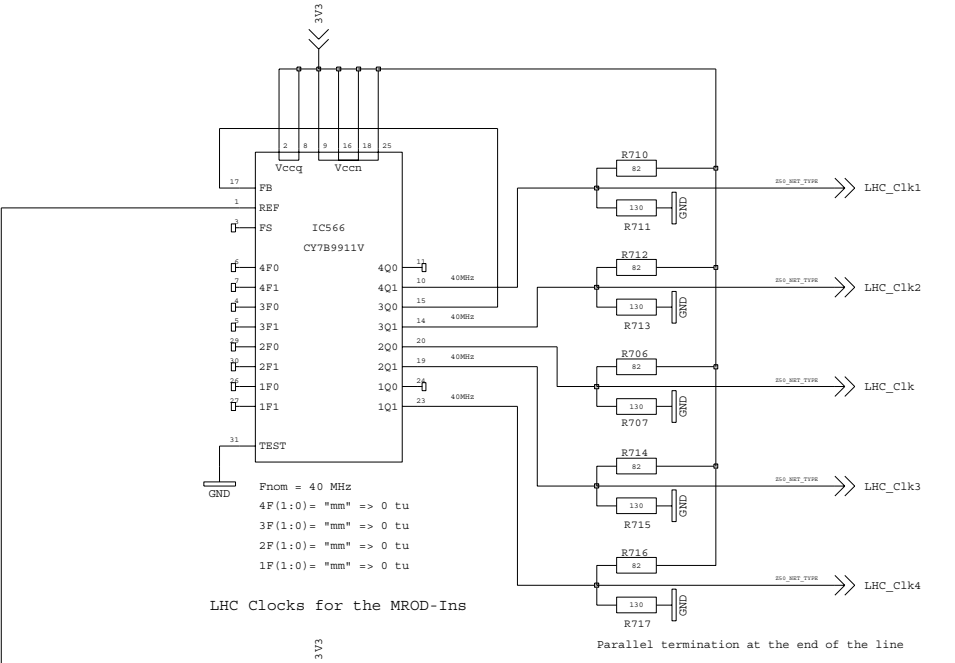


Rocket-IO Inter-FPGA Link Frequency  
 100 MHz 16 bit = 50 MHz 32 bit  
 = 200 MB/s



Clocks for the MROD-Out



LHC Clocks for the MROD-Ins

MROD-Out		Rev	V2	12
		Date	7 Feb 2006	
Clocks		Time	1:34:07 pm	
Proj: MROD-X	Proj.No: 38405	Name	Ton van Reen	
Peter Jansweijer	peterj@nikhef.nl	Size	A3	4 1 4 A
<b>NIKHEF</b> NATIONAL INSTITUUT VOOR KERN- FYSICA EN HOGE ENERGIE-FYSICA KRUISLAAN 409, 020-592 2000 1098 SJ AMSTERDAM NEDERLAND		Dim	420 x 297 mm	
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