IC6004
XC2VP7FF896

	A2 VCCAUX	A3 VRP_2	A4 I/O	A5 I/O	A6 I/O	A7 I/O	A8 I/O	A9 GND	A10 I/O	A11 I/O	A12 I/O	A13 I/O	A14 I/O	A15 VCCAUX	A16 VCCAUX	A17 GOL_LVDS GOL_LVDS T GOL_RXN DP_GOL_RX3B	A18 GOL_LVDS GOL_LVDS T GOL_RXP DP_GOL_RX3B	A19 GOL_LVDS GOL_LVDS S GOL_TXP DP_GOL_TX3B	A20 GOL_LVDS GOL_LVDS S GOL_TXN DP_GOL_TX3B	A21 I/O	A22 GND	A23 LEDs(0)	A24 I/O	A25 I/O	A26 I/O	A27 I/O	A28 VRP_7	A29 VCCAUX	
B1 VCCAUX	B2 GND	B3 VRN_2	B4 AVCCAUXRX9	B5 VTRXPAD9	B6 AVCCAUXTX9	B7 VTTXPAD9	B8 I/O	B9 I/O	B10 I/O	B11 AVCCAUXRX7	B12 VTRXPAD7	B13 AVCCAUXTX7	B14 VTTXPAD7	B15 I/O	B16 GOL_XClk	B17 AVCCAUXRX6	B18 VTRXPAD6	B19 AVCCAUXTX6	B20 VTTXPAD6	B21 I/O	B22 GND	B23 LEDs(1)	B24 AVCCAUXRX4	B25 VTRXPAD4	B26 AVCCAUXTX4	B27 VTTXPAD4	B28 VRN_7	B29 GND	B30 VCCAUX
C1 I/O	C2 I/O	C3 GND	C4 I/O	C5 I/O	C6 GNDA9	C7 I/O	C8 I/O	C9 I/O	C10 I/O	C11 I/O	C12 GNDA7	C13 I/O	C14 GND	C15 I/O	C16 I/O	C17 GND	C18 UpLED	C19 GNDA6	C20 I/O	C21 I/O	C22 I/O	C23 LEDs(2)	C24 LEDs(3)	C25 GNDA4	C26 BufA(6)	C27 BufA(7)	C28 GND	C29 BufCE_n	C30 BufA(19)
D1 I/O	D2 I/O	D3 I/O	D4 GND	D5 I/O	D6 I/O	D7 I/O	D8 I/O	D9 GND	D10 I/O	D11 I/O	D12 GND	D13 I/O	D14 I/O	D15 I/O	D16 GOL_RX_LOS	D17 GOL_TX_Disable	D18 ErrLED	D19 GND	D20 GOL_MOD_DEF1	D21 GOL_MOD_DEF0	D22 GND	D23 ChaID(1)	D24 ChaID(0)	D25 DXN	D26 BufD(16)	D27 GND	D28 BufD(33)	D29 I/O	D30 I/O
E1 I/O	E2 I/O	E3 I/O	E4 I/O	E5 GND	E6 VRN_1	E7 VRP_1	E8 I/O	E9 I/O	E10 I/O	E11 I/O	E12 I/O	E13 I/O	E14 I/O	E15 I/O	E16 I/O	R17 GOL_TX_Fault	E18 I/O	E19 I/O	E20 GOL_MOD_DEF2	E21 GOL_RATE_SEL	E22 I/O	E23 ChaID(2)	E24 VRN_0	E25 VRP_0	E26 GND	E27 I/O	E28 I/O	E29 I/O	E30 I/O
F1 I/O	F2 I/O	F3 I/O	F4 I/O	F5 TDO	F6 GND	F7 I/O	F8 I/O	F9 I/O	F10 I/O	F11 I/O	F12 I/O	F13 GND	F14 I/O	F15 I/O	F16 I/O	F17 I/O	F18 GND	F19 I/O	F20 I/O	F21 TestCon(1)	F22 TestCon(0)	F23 I/O	F24 I/O	F25 GND	F26 TDI	F27 I/O	F28 I/O	F29 I/O	F30 I/O
G1 I/O	G2 I/O	G3 I/O	G4 I/O	G5 I/O	G6 I/O	G7 TCK	G8 I/O	G9 I/O	G10 I/O	G11 I/O	G12 I/O	G13 I/O	G14 I/O	G15 I/O	G16 Rst_n	G17 I/O	G18 I/O	G19 TestCon(6)	G20 TestCon(5)	G21 TestCon(4)	G22 TestCon(3)	G23 TestCon(2)	G24 PROG_B	G25 BufD(18)	G26 BufD(17)	G27 I/O	G28 I/O	G29 I/O	G30 I/O
H1 GND	H2 I/O	H3 I/O	H4 I/O	H5 I/O	H6 I/O	H7 VBATT	H8 TMS	H9 I/O	H10 I/O	H11 I/O	H12 TestCon(15)	H13 I/O	H14 I/O	H15 I/O	H16 I/O	H17 I/O	H18 I/O	H19 TestCon(7)	H20 T_Alert_n	H21 SMBData	H22 SMBClk	H23 HSWAP_EN	H24 DXP	H25 I/O	H26 I/O	H27 I/O	H28 I/O	H29 BufD(19)	H30 GND
J1 I/O	J2 I/O	J3 I/O	J4 I/O	J5 I/O	J6 I/O	J7 I/O	J8 TestCon(14)	J9 VCCO_2	J10 VCCO_1	J11 VCCO_1	J12 VCCO_1	J13 VCCO_1	J14 I/O	J15 I/O	J16 I/O	J17 I/O	J18 VCCO_0	J19 VCCO_0	J20 VCCO_0	J21 VCCO_0	J22 VCCO_7	J23 BufD(25)	J24 BufD(24)	J25 I/O	J26 I/O	J27 BufD(23)	J28 BufD(22)	J29 BufD(21)	J30 BufD(20)
K1 I/O	K2 I/O	K3 I/O	K4 I/O	K5 I/O	K6 I/O	K7 I/O	K8 I/O	K9 VCCO_2	K10 VCCO_1	K11 VCCO_1	K12 VCCO_1	K13 VCCO_1	K14 VCCO_1	K15 VCCO_1	K16 VCCO_0	K17 VCCO_0	K18 VCCO_0	K19 VCCO_0	K20 VCCO_0	K21 VCCO_0	K22 VCCO_7	K23 I/O	K24 I/O	K25 BufD(31)	K26 BufD(30)	K27 BufD(29)	K28 BufD(28)	K29 BufD(27)	K30 BufD(26)
L1 I/O	L2 I/O	L3 GND	L4 I/O	L5 I/O	L6 GND	L7 I/O	L8 I/O	L9 VCCO_2	L10 VCCO_2	L11 GND	L12 VCCINT	L13 VCCINT	L14 VCCINT	L15 VCCINT	L16 VCCINT	L17 VCCINT	L18 VCCINT	L19 VCCINT	L20 GND	L21 VCCO_7	L22 VCCO_7	L23 I/O	L24 I/O	L25 GND	L26 BufA(3)	L27 BufA(4)	L28 GND	L29 BufA(5)	L30 BufD(32)
M1 I/O	M2 I/O	M3 I/O	M4 I/O	M5 I/O	M6 I/O	M7 I/O	M8 I/O	M9 VCCO_2	M10 VCCO_2	M11 VCCINT	M12 GND	M13 GND	M14 GND	M15 GND	M16 GND	M17 GND	M18 GND	M19 GND	M20 VCCINT	M21 VCCO_7	M22 VCCO_7	M23 BufA(14)	M24 BufA(13)	M25 BufA(12)	M26 BufA(11)	M27 BufA(10)	M28 BufA(0)	M29 BufA(1)	M30 BufA(2)
N1 I/O	N2 I/O	N3 I/O	N4 I/O	N5 I/O	N6 I/O	N7 I/O	N8 I/O	N9 VCCO_2	N10 VCCO_2	N11 VCCINT	N12 GND	N13 GND	N14 GND	N15 GND	N16 GND	N17 GND	N18 GND	N19 GND	N20 VCCINT	N21 VCCO_7	N22 VCCO_7	N23 BufD(4)	N24 BufD(3)	N25 BufD(2)	N26 BufD(1)	N27 BufD(0)	N28 BufD(35)	N29 BufA(16)	N30 BufA(15)
P1 I/O	P2 I/O	P3 I/O	P4 I/O	P5 I/O	P6 GND	P7 I/O	P8 I/O	P9 I/O	P10 VCCO_2	P11 VCCINT	P12 GND	P13 GND	P14 GND	P15 GND	P16 GND	P17 GND	P18 GND	P19 GND	P20 VCCINT	P21 VCCO_7	P22 BufD(12)	P23 BufD(11)	P24 BufD(10)	P25 GND	P26 BufD(9)	P27 BufD(8)	P28 BufD(7)	P29 BufD(6)	P30 BufD(5)
R1 VCCAUX	R2 I/O	R3 I/O	R4 I/O	R5 I/O	R6 I/O	R7 I/O	R8 I/O	R9 I/O	R10 VCCO_2	R11 VCCINT	R12 GND	R13 GND	R14 GND	R15 GND	R16 GND	R17 GND	R18 GND	R19 GND	R20 VCCINT	R21 VCCO_7	R22 BufA(18)	R23 BufA(17)	R24 BufA(8)	R25 BufA(9)	R26 BufD(34)	R27 BufD(15)	R28 BufD(14)	R29 BufD(13)	R30 VCCAUX
T1 VCCAUX	T2 I/O	T3 I/O	T4 I/O	T5 Spare(4)	T6 Spare(3)	T7 Spare(2)	T8 Spare(1)	T9 Spare(0)	T10 VCCO_3	T11 VCCINT	T12 GND	T13 GND	T14 GND	T15 GND	T16 GND	T17 GND	T18 GND	T19 GND	T20 VCCINT	T21 VCCO_6	T22 SDRAM_DQ(6)	T23 SDRAM_DQ(5)	T24 SDRAM_DQ(4)	T25 SDRAM_DQ(3)	T26 SDRAM_DQ(2)	T27 SDRAM_DQ(1)	T28 SDRAM_DQ(0)	T29 Bufr_W_n	T30 VCCAUX
U1 I/O	U2 I/O	U3 I/O	U4 I/O	U5 I/O	U6 GND	U7 I/O	U8 I/O	U9 I/O	U10 VCCO_3	U11 VCCINT	U12 GND	U13 GND	U14 GND	U15 GND	U16 GND	U17 GND	U18 GND	U19 GND	U20 VCCINT	U21 VCCO_6	U22 SDRAM_BA(0)	U23 SDRAM_A(11)	U24 SDRAM_CS_n	U25 GND	U26 SDRAM_RAS_n	U27 SDRAM_CAS_n	U28 SDRAM_WE_n	U29 SDRAM_DQM(0)	U30 SDRAM_DQ(7)
V1 A21_Select	V2 I/O	V3 I/O	V4 I/O	V5 I/O	V6 I/O	V7 I/O	V8 I/O	V9 VCCO_3	V10 VCCO_3	V11 VCCINT	V12 GND	V13 GND	V14 GND	V15 GND	V16 GND	V17 GND	V18 GND	V19 GND	V20 VCCINT	V21 VCCO_6	V22 VCCO_6	V23 SDRAM_DQ(17)	V24 SDRAM_DQ(16)	V25 SDRAM_DQM(2)	V26 SDRAM_A(2)	V27 SDRAM_A(1)	V28 SDRAM_A(0)	V29 SDRAM_A(10)	V30 SDRAM_BA(1)
W1 Adr(21)	W2 MS0_n	W3 MS1_n	W4 MS2_n	W5 MS3_n	W6 DMAR_n	W7 I/O	W8 I/O	W9 VCCO_3	W10 VCCO_3	W11 VCCINT	W12 GND	W13 GND	W14 GND	W15 GND	W16 GND	W17 GND	W18 GND	W19 GND	W20 VCCINT	W21 VCCO_6	W22 VCCO_6	W23 SDRAM_DQ(25)	W24 SDRAM_DQ(24)	W25 SDRAM_DQ(23)	W26 SDRAM_DQ(22)	W27 SDRAM_DQ(21)	W28 SDRAM_DQ(20)	W29 SDRAM_DQ(19)	W30 SDRAM_DQ(18)
Y1 Adr(17)	Y2 Adr(18)	Y3 GND	Y4 Adr(19)	Y5 Adr(20)	Y6 GND	Y7 I/O	Y8 I/O	Y9 VCCO_3	Y10 VCCO_3	Y11 GND	Y12 VCCINT	Y13 VCCINT	Y14 VCCINT	Y15 VCCINT	Y16 VCCINT	Y17 VCCINT	Y18 VCCINT	Y19 VCCINT	Y20 GND	Y21 VCCO_6	Y22 VCCO_6	Y23 I/O	Y24 I/O	Y25 GND	Y26 SDRAM_DQ(29)	Y27 SDRAM_DQ(28)	Y28 GND	Y29 SDRAM_DQ(27)	Y30 SDRAM_DQ(26)
AA1 Adr(11)	AA2 Adr(12)	AA3 Adr(13)	AA4 Adr(14)	AA5 Adr(15)	AA6 Adr(16)	AA7 I/O	AA8 I/O	AA9 VCCO_3	AA10 VCCO_4	AA11 VCCO_4	AA12 VCCO_4	AA13 VCCO_4	AA14 VCCO_4	AA15 VCCO_4	AA16 VCCO_5	AA17 VCCO_5	AA18 VCCO_5	AA19 VCCO_5	AA20 VCCO_5	AA21 VCCO_5	AA22 VCCO_6	AA23 I/O	AA24 I/O	AA25 SDRAM_A(5)	AA26 SDRAM_A(4)	AA27 SDRAM_A(3)	AA28 SDRAM_DQM(3)	AA29 SDRAM_DQ(31)	AA30 SDRAM_DQ(30)
AB1 Adr(7)	AB2 Adr(8)	AB3 Adr(9)	AB4 Adr(10)	AB5 I/O	AB6 I/O	AB7 I/O	AB8 I/O	AB9 VCCO_3	AB10 VCCO_4	AB11 VCCO_4	AB12 VCCO_4	AB13 VCCO_4	AB14 I/O	AB15 Data(3)	AB16 IRQ2_n	AB17 I/O	AB18 VCCO_5	AB19 VCCO_5	AB20 VCCO_5	AB21 VCCO_5	AB22 VCCO_6	AB23 I/O	AB24 I/O	AB25 I/O	AB26 I/O	AB27 SDRAM_A(9)	AB28 SDRAM_A(8)	AB29 SDRAM_A(7)	AB30 SDRAM_A(6)
AC1 GND	AC2 Adr(4)	AC3 Adr(5)	AC4 Adr(6)	AC5 I/O	AC6 I/O	AC7 CCLK	AC8 DONE	AC9 D0	AC10 Data(16)	AC11 Data(11)	AC12 Data(9)	AC13 I/O	AC14 I/O	AC15 Data(2)	AC16 IRQ1_n	AC17 I/O	AC18 I/O	AC19 TestCon(8)	AC20 TestCon(9)	AC21 TestCon(10)	AC22 D7	AC23 M2	AC24 M1	AC25 I/O	AC26 I/O	AC27 SDRAM_DQM(1)	AC28 SDRAM_A(12)	AC29 SDRAM_CKE	AC30 GND
AD1 I/O	AD2 I/O	AD3 I/O	AD4 I/O	AD5 Adr(2)	AD6 Adr(3)	AD7 PWRDWN_B	AD8 I/O	AD9 D1	AD10 Data(15)	AD11 Data(10)	AD12 Data(8)	AD13 I/O	AD14 Data(7)	AD15 Data(1)	AD16 IRQ0_n	AD17 I/O	AD18 I/O	AD19 TestCon(11)	AD20 TestCon(12)	AD21 TestCon(13)	AD22 D6	AD23 I/O	AD24 M0	AD25 SDRAM_DQ(9)	AD26 SDRAM_DQ(8)	AD27 I/O	AD28 I/O	AD29 I/O	AD30 I/O
AE1 I/O	AE2 I/O	AE3 I/O	AE4 I/O	AE5 I/O	AE6 GND	AE7 Data(25)	AE8 Data(24)	AE9 Data(20)	AE10 Data(14)	AE11 I/O	AE12 I/O	AE13 GND	AE14 Data(6)	AE15 Data(0)	AE16 Empty	AE17 I/O	AE18 GND	AE19 I/O	AE20 I/O	AE21 I/O	AE22 I/O	AE23 I/O	AE24 I/O	AE25 GND	AE26 I/O	AE27 I/O	AE28 I/O	AE29 I/O	AE30 I/O
AF1 I/O	AF2 I/O	AF3 I/O	AF4 I/O	AF5 GND	AF6 I/O	AF7 INIT_B	AF8 Data(23)	AF9 Data(19)	AF10 Data(13)	AF11 I/O	AF12 I/O	AF13 I/O	AF14 Data(5)	AF15 SDRAM_CLKin	AF16 SDRAM_CLK	AF17 I/O	AF18 I/O	AF19 I/O	AF20 I/O	AF21 TTC_n(7)	AF22 TTC_n(6)	AF23 TTC_n(4)	AF24 RDWR_B	AF25 I/O	AF26 GND	AF27 I/O	AF28 I/O	AF29 I/O	AF30 I/O
AG1 Data(30)	AG2 Data(31)	AG3 Adr(0)	AG4 GND	AG5 Adr(1)	AG6 DOUT	AG7 D2	AG8 VRP_4	AG9 GND	AG10 Data(12)	AG11 I/O	AG12 GND	AG13 I/O	AG14 Data(4)	AG15 SharcWr_n	AG16 LHC_Clk	AG17 ChaBusy	AG18 I/O	AG19 GND	AG20 I/O	AG21 TTC_n(5)	AG22 GND	AG23 VRN_5	AG24 D5	AG25 CS_B	AG26 SDRAM_DQ(13)	AG27 GND	AG28 SDRAM_DQ(12)	AG29 SDRAM_DQ(11)	AG30 SDRAM_DQ(10)
AH1 Data(26)	AH2 Data(27)	AH3 GND	AH4 Data(28)	AH5 Data(29)	AH6 GNDA16	AH7 D3	AH8 VRN_4	AH9 Data(18)	AH10 I/O	AH11 I/O	AH12 GNDA18	AH13 I/O	AH14 GND	AH15 SharcRd_n	AH16 Clk	AH17 GND	AH18 I/O	AH19 GNDA19	AH20 I/O	AH21 I/O	AH22 TTC_n(3)	AH23 VRP_5	AH24 D4	AH25 GNDA21	AH26 I/O	AH27 I/O	AH28 GND	AH29 SDRAM_DQ(15)	AH30 SDRAM_DQ(14)
AJ1 VCCAUX	AJ2 GND	AJ3 VRP_3	AJ4 AVCCAUXRX16	AJ5 VTRXPAD16	AJ6 AVCCAUXTX16	AJ7 VTTXPAD16	AJ8 Data(22)	AJ9 Data(17)	AJ10 I/O	AJ11 AVCCAUXRX18	AJ12 VTRXPAD18	AJ13 AVCCAUXTX18	AJ14 VTTXPAD18	AJ15 Rocket_XClk	AJ16 Clkx2	AJ17 AVCCAUXRX19 ROCKET_LVDS ROCKET_LVDS T Rocket_RXN	AJ18 VTRXPAD19 ROCKET_LVDS ROCKET_LVDS T Rocket_RXP	AJ19 AVCCAUXTX19 ROCKET_LVDS ROCKET_LVDS S Rocket_TXP	AJ20 VTTXPAD19 ROCKET_LVDS ROCKET_LVDS S Rocket_TXN	AJ21 I/O	AJ22 TTC_n(2)	AJ23 TTC_n(1)	AJ24 AVCCAUXRX21	AJ25 VTRXPAD21	AJ26 AVCCAUXTX21	AJ27 VTTXPAD21	AJ28 VRP_6	AJ29 GND	AJ30 VCCAUX
	AK2 VCCAUX	AK3 VRN_3	AK4 I/O	AK5 I/O	AK6 I/O	AK7 I/O	AK8 Data(21)	AK9 GND	AK10 I/O	AK11 I/O	AK12 I/O	AK13 I/O	AK14 I/O	AK15 VCCAUX	AK16 VCCAUX	AK17 Rocket_RXN	AK18 Rocket_RXP	AK19 Rocket_TXP	AK20 Rocket_TXN	AK21 I/O	AK22 GND	AK23 TTC_n(0)	AK24 I/O	AK25 I/O	AK26 I/O	AK27 I/O	AK28 VRN_6	AK29 VCCAUX	

Input FPGA Power pins:
VCCAUX (2V5) 16 pins
VCCINT (1V5) 32 pins
VCCO_# (3V3) 10 pins each
GND 124 pins

channel_in		Rev V2 2
		Date 7 Feb 2006
Input FPGA		Time 1:50:53 pm
Proj: MROD-X	Proj.No: 38405	Name tonvr
Peter Jansweijer	peterj@nikhef.nl	
NIKHEF	NATIONAAL INSTITUUT VOOR KERN-FYSICA EN HOGE ENERGIE-FYSICA KRUISLAAN 409, 020-592 2000 1098 SJ AMSTERDAM NEDERLAND	Size A3 4 1 4 A
© ET-Nikhef Amsterdam		Dim 420 x 297 mm
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