IC4004
XC2VP7FF896

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|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | A2<br>VCCAUX | A3<br>VRP_2 | A4<br>I/O | A5<br>I/O | A6<br>I/O | A7<br>I/O | A8<br>I/O | A9<br>GND | A10<br>I/O | A11<br>I/O | A12<br>I/O | A13<br>I/O | A14<br>I/O | A15<br>VCCAUX | A16<br>VCCAUX | A17<br>GOL_LVDS<br>GOL_LVDS<br>T<br>GOL_RXN<br>DP_GOL_RX2B | A18<br>GOL_LVDS<br>GOL_LVDS<br>T<br>GOL_RXP<br>DP_GOL_RX2B | A19<br>GOL_LVDS<br>GOL_LVDS<br>S<br>GOL_TXP<br>DP_GOL_TX2B | A20<br>GOL_LVDS<br>GOL_LVDS<br>S<br>GOL_TXN<br>DP_GOL_TX2B | A21<br>I/O | A22<br>GND | A23<br>LEDs(0) | A24<br>I/O | A25<br>I/O | A26<br>I/O | A27<br>I/O | A28<br>VRP_7 | A29<br>VCCAUX | |
| B1<br>VCCAUX | B2<br>GND | B3<br>VRN_2 | B4<br>AVCCAUXRX9 | B5<br>VTRXPAD9 | B6<br>AVCCAUXTX9 | B7<br>VTTXPAD9 | B8<br>I/O | B9<br>I/O | B10<br>I/O | B11<br>AVCCAUXRX7 | B12<br>VTRXPAD7 | B13<br>AVCCAUXTX7 | B14<br>VTTXPAD7 | B15<br>I/O | B16<br>GOL_XClk | B17<br>AVCCAUXRX6 | B18<br>VTRXPAD6 | B19<br>AVCCAUXTX6 | B20<br>VTTXPAD6 | B21<br>I/O | B22<br>GND | B23<br>LEDs(1) | B24<br>AVCCAUXRX4 | B25<br>VTRXPAD4 | B26<br>AVCCAUXTX4 | B27<br>VTTXPAD4 | B28<br>VRN_7 | B29<br>GND | B30<br>VCCAUX |
| C1<br>I/O | C2<br>I/O | C3<br>GND | C4<br>I/O | C5<br>I/O | C6<br>GNDA9 | C7<br>I/O | C8<br>I/O | C9<br>I/O | C10<br>I/O | C11<br>I/O | C12<br>GNDA7 | C13<br>I/O | C14<br>GND | C15<br>I/O | C16<br>I/O | C17<br>GND | C18<br>UpLED | C19<br>GNDA6 | C20<br>I/O | C21<br>I/O | C22<br>I/O | C23<br>LEDs(2) | C24<br>LEDs(3) | C25<br>GNDA4 | C26<br>BufA(6) | C27<br>BufA(7) | C28<br>GND | C29<br>BufCE_n | C30<br>BufA(19) |
| D1<br>I/O | D2<br>I/O | D3<br>I/O | D4<br>GND | D5<br>I/O | D6<br>I/O | D7<br>I/O | D8<br>I/O | D9<br>GND | D10<br>I/O | D11<br>I/O | D12<br>GND | D13<br>I/O | D14<br>I/O | D15<br>I/O | D16<br>GOL_RX_LOS | D17<br>GOL_TX_Disable | D18<br>ErrLED | D19<br>GND | D20<br>GOL_MOD_DEF1 | D21<br>GOL_MOD_DEF0 | D22<br>GND | D23<br>ChaID(1) | D24<br>ChaID(0) | D25<br>DXN | D26<br>BufD(16) | D27<br>GND | D28<br>BufD(33) | D29<br>I/O | D30<br>I/O |
| E1<br>I/O | E2<br>I/O | E3<br>I/O | E4<br>I/O | E5<br>GND | E6<br>VRN_1 | E7<br>VRP_1 | E8<br>I/O | E9<br>I/O | E10<br>I/O | E11<br>I/O | E12<br>I/O | E13<br>I/O | E14<br>I/O | E15<br>I/O | E16<br>I/O | R17<br>GOL_TX_Fault | E18<br>I/O | E19<br>I/O | E20<br>GOL_MOD_DEF2 | E21<br>GOL_RATE_SEL | E22<br>I/O | E23<br>ChaID(2) | E24<br>VRN_0 | E25<br>VRP_0 | E26<br>GND | E27<br>I/O | E28<br>I/O | E29<br>I/O | E30<br>I/O |
| F1<br>I/O | F2<br>I/O | F3<br>I/O | F4<br>I/O | F5<br>TDO | F6<br>GND | F7<br>I/O | F8<br>I/O | F9<br>I/O | F10<br>I/O | F11<br>I/O | F12<br>I/O | F13<br>GND | F14<br>I/O | F15<br>I/O | F16<br>I/O | F17<br>I/O | F18<br>GND | F19<br>I/O | F20<br>I/O | F21<br>TestCon(1) | F22<br>TestCon(0) | F23<br>I/O | F24<br>I/O | F25<br>GND | F26<br>TDI | F27<br>I/O | F28<br>I/O | F29<br>I/O | F30<br>I/O |
| G1<br>I/O | G2<br>I/O | G3<br>I/O | G4<br>I/O | G5<br>I/O | G6<br>I/O | G7<br>TCK | G8<br>I/O | G9<br>I/O | G10<br>I/O | G11<br>I/O | G12<br>I/O | G13<br>I/O | G14<br>I/O | G15<br>I/O | G16<br>Rst_n | G17<br>I/O | G18<br>I/O | G19<br>TestCon(6) | G20<br>TestCon(5) | G21<br>TestCon(4) | G22<br>TestCon(3) | G23<br>TestCon(2) | G24<br>PROG_B | G25<br>BufD(18) | G26<br>BufD(17) | G27<br>I/O | G28<br>I/O | G29<br>I/O | G30<br>I/O |
| H1<br>GND | H2<br>I/O | H3<br>I/O | H4<br>I/O | H5<br>I/O | H6<br>I/O | H7<br>VBATT | H8<br>TMS | H9<br>I/O | H10<br>I/O | H11<br>I/O | H12<br>TestCon(15) | H13<br>I/O | H14<br>I/O | H15<br>I/O | H16<br>I/O | H17<br>I/O | H18<br>I/O | H19<br>TestCon(7) | H20<br>T_Alert_n | H21<br>SMBData | H22<br>SMBClk | H23<br>HSWAP_EN | H24<br>DXP | H25<br>I/O | H26<br>I/O | H27<br>I/O | H28<br>I/O | H29<br>BufD(19) | H30<br>GND |
| J1<br>I/O | J2<br>I/O | J3<br>I/O | J4<br>I/O | J5<br>I/O | J6<br>I/O | J7<br>I/O | J8<br>TestCon(14) | J9<br>VCCO_2 | J10<br>VCCO_1 | J11<br>VCCO_1 | J12<br>VCCO_1 | J13<br>VCCO_1 | J14<br>I/O | J15<br>I/O | J16<br>I/O | J17<br>I/O | J18<br>VCCO_0 | J19<br>VCCO_0 | J20<br>VCCO_0 | J21<br>VCCO_0 | J22<br>VCCO_7 | J23<br>BufD(25) | J24<br>BufD(24) | J25<br>I/O | J26<br>I/O | J27<br>BufD(23) | J28<br>BufD(22) | J29<br>BufD(21) | J30<br>BufD(20) |
| K1<br>I/O | K2<br>I/O | K3<br>I/O | K4<br>I/O | K5<br>I/O | K6<br>I/O | K7<br>I/O | K8<br>I/O | K9<br>VCCO_2 | K10<br>VCCO_1 | K11<br>VCCO_1 | K12<br>VCCO_1 | K13<br>VCCO_1 | K14<br>VCCO_1 | K15<br>VCCO_1 | K16<br>VCCO_0 | K17<br>VCCO_0 | K18<br>VCCO_0 | K19<br>VCCO_0 | K20<br>VCCO_0 | K21<br>VCCO_0 | K22<br>VCCO_7 | K23<br>I/O | K24<br>I/O | K25<br>BufD(31) | K26<br>BufD(30) | K27<br>BufD(29) | K28<br>BufD(28) | K29<br>BufD(27) | K30<br>BufD(26) |
| L1<br>I/O | L2<br>I/O | L3<br>GND | L4<br>I/O | L5<br>I/O | L6<br>GND | L7<br>I/O | L8<br>I/O | L9<br>VCCO_2 | L10<br>VCCO_2 | L11<br>GND | L12<br>VCCINT | L13<br>VCCINT | L14<br>VCCINT | L15<br>VCCINT | L16<br>VCCINT | L17<br>VCCINT | L18<br>VCCINT | L19<br>VCCINT | L20<br>GND | L21<br>VCCO_7 | L22<br>VCCO_7 | L23<br>I/O | L24<br>I/O | L25<br>GND | L26<br>BufA(3) | L27<br>BufA(4) | L28<br>GND | L29<br>BufA(5) | L30<br>BufD(32) |
| M1<br>I/O | M2<br>I/O | M3<br>I/O | M4<br>I/O | M5<br>I/O | M6<br>I/O | M7<br>I/O | M8<br>I/O | M9<br>VCCO_2 | M10<br>VCCO_2 | M11<br>VCCINT | M12<br>GND | M13<br>GND | M14<br>GND | M15<br>GND | M16<br>GND | M17<br>GND | M18<br>GND | M19<br>GND | M20<br>VCCINT | M21<br>VCCO_7 | M22<br>VCCO_7 | M23<br>BufA(14) | M24<br>BufA(13) | M25<br>BufA(12) | M26<br>BufA(11) | M27<br>BufA(10) | M28<br>BufA(0) | M29<br>BufA(1) | M30<br>BufA(2) |
| N1<br>I/O | N2<br>I/O | N3<br>I/O | N4<br>I/O | N5<br>I/O | N6<br>I/O | N7<br>I/O | N8<br>I/O | N9<br>VCCO_2 | N10<br>VCCO_2 | N11<br>VCCINT | N12<br>GND | N13<br>GND | N14<br>GND | N15<br>GND | N16<br>GND | N17<br>GND | N18<br>GND | N19<br>GND | N20<br>VCCINT | N21<br>VCCO_7 | N22<br>VCCO_7 | N23<br>BufD(4) | N24<br>BufD(3) | N25<br>BufD(2) | N26<br>BufD(1) | N27<br>BufD(0) | N28<br>BufD(35) | N29<br>BufA(16) | N30<br>BufA(15) |
| P1<br>I/O | P2<br>I/O | P3<br>I/O | P4<br>I/O | P5<br>I/O | P6<br>GND | P7<br>I/O | P8<br>I/O | P9<br>I/O | P10<br>VCCO_2 | P11<br>VCCINT | P12<br>GND | P13<br>GND | P14<br>GND | P15<br>GND | P16<br>GND | P17<br>GND | P18<br>GND | P19<br>GND | P20<br>VCCINT | P21<br>VCCO_7 | P22<br>BufD(12) | P23<br>BufD(11) | P24<br>BufD(10) | P25<br>GND | P26<br>BufD(9) | P27<br>BufD(8) | P28<br>BufD(7) | P29<br>BufD(6) | P30<br>BufD(5) |
| R1<br>VCCAUX | R2<br>I/O | R3<br>I/O | R4<br>I/O | R5<br>I/O | R6<br>I/O | R7<br>I/O | R8<br>I/O | R9<br>I/O | R10<br>VCCO_2 | R11<br>VCCINT | R12<br>GND | R13<br>GND | R14<br>GND | R15<br>GND | R16<br>GND | R17<br>GND | R18<br>GND | R19<br>GND | R20<br>VCCINT | R21<br>VCCO_7 | R22<br>BufA(18) | R23<br>BufA(17) | R24<br>BufA(8) | R25<br>BufA(9) | R26<br>BufD(34) | R27<br>BufD(15) | R28<br>BufD(14) | R29<br>BufD(13) | R30<br>VCCAUX |
| T1<br>VCCAUX | T2<br>I/O | T3<br>I/O | T4<br>I/O | T5<br>Spare(4) | T6<br>Spare(3) | T7<br>Spare(2) | T8<br>Spare(1) | T9<br>Spare(0) | T10<br>VCCO_3 | T11<br>VCCINT | T12<br>GND | T13<br>GND | T14<br>GND | T15<br>GND | T16<br>GND | T17<br>GND | T18<br>GND | T19<br>GND | T20<br>VCCINT | T21<br>VCCO_6 | T22<br>SDRAM_DQ(6) | T23<br>SDRAM_DQ(5) | T24<br>SDRAM_DQ(4) | T25<br>SDRAM_DQ(3) | T26<br>SDRAM_DQ(2) | T27<br>SDRAM_DQ(1) | T28<br>SDRAM_DQ(0) | T29<br>Bufr_W_n | T30<br>VCCAUX |
| U1<br>I/O | U2<br>I/O | U3<br>I/O | U4<br>I/O | U5<br>I/O | U6<br>GND | U7<br>I/O | U8<br>I/O | U9<br>I/O | U10<br>VCCO_3 | U11<br>VCCINT | U12<br>GND | U13<br>GND | U14<br>GND | U15<br>GND | U16<br>GND | U17<br>GND | U18<br>GND | U19<br>GND | U20<br>VCCINT | U21<br>VCCO_6 | U22<br>SDRAM_BA(0) | U23<br>SDRAM_A(11) | U24<br>SDRAM_CS_n | U25<br>GND | U26<br>SDRAM_RAS_n | U27<br>SDRAM_CAS_n | U28<br>SDRAM_WE_n | U29<br>SDRAM_DQM(0) | U30<br>SDRAM_DQ(7) |
| V1<br>A21_Select | V2<br>I/O | V3<br>I/O | V4<br>I/O | V5<br>I/O | V6<br>I/O | V7<br>I/O | V8<br>I/O | V9<br>VCCO_3 | V10<br>VCCO_3 | V11<br>VCCINT | V12<br>GND | V13<br>GND | V14<br>GND | V15<br>GND | V16<br>GND | V17<br>GND | V18<br>GND | V19<br>GND | V20<br>VCCINT | V21<br>VCCO_6 | V22<br>VCCO_6 | V23<br>SDRAM_DQ(17) | V24<br>SDRAM_DQ(16) | V25<br>SDRAM_DQM(2) | V26<br>SDRAM_A(2) | V27<br>SDRAM_A(1) | V28<br>SDRAM_A(0) | V29<br>SDRAM_A(10) | V30<br>SDRAM_BA(1) |
| W1<br>Adr(21) | W2<br>MS0_n | W3<br>MS1_n | W4<br>MS2_n | W5<br>MS3_n | W6<br>DMAR_n | W7<br>I/O | W8<br>I/O | W9<br>VCCO_3 | W10<br>VCCO_3 | W11<br>VCCINT | W12<br>GND | W13<br>GND | W14<br>GND | W15<br>GND | W16<br>GND | W17<br>GND | W18<br>GND | W19<br>GND | W20<br>VCCINT | W21<br>VCCO_6 | W22<br>VCCO_6 | W23<br>SDRAM_DQ(25) | W24<br>SDRAM_DQ(24) | W25<br>SDRAM_DQ(23) | W26<br>SDRAM_DQ(22) | W27<br>SDRAM_DQ(21) | W28<br>SDRAM_DQ(20) | W29<br>SDRAM_DQ(19) | W30<br>SDRAM_DQ(18) |
| Y1<br>Adr(17) | Y2<br>Adr(18) | Y3<br>GND | Y4<br>Adr(19) | Y5<br>Adr(20) | Y6<br>GND | Y7<br>I/O | Y8<br>I/O | Y9<br>VCCO_3 | Y10<br>VCCO_3 | Y11<br>GND | Y12<br>VCCINT | Y13<br>VCCINT | Y14<br>VCCINT | Y15<br>VCCINT | Y16<br>VCCINT | Y17<br>VCCINT | Y18<br>VCCINT | Y19<br>VCCINT | Y20<br>GND | Y21<br>VCCO_6 | Y22<br>VCCO_6 | Y23<br>I/O | Y24<br>I/O | Y25<br>GND | Y26<br>SDRAM_DQ(29) | Y27<br>SDRAM_DQ(28) | Y28<br>GND | Y29<br>SDRAM_DQ(27) | Y30<br>SDRAM_DQ(26) |
| AA1<br>Adr(11) | AA2<br>Adr(12) | AA3<br>Adr(13) | AA4<br>Adr(14) | AA5<br>Adr(15) | AA6<br>Adr(16) | AA7<br>I/O | AA8<br>I/O | AA9<br>VCCO_3 | AA10<br>VCCO_4 | AA11<br>VCCO_4 | AA12<br>VCCO_4 | AA13<br>VCCO_4 | AA14<br>VCCO_4 | AA15<br>VCCO_4 | AA16<br>VCCO_5 | AA17<br>VCCO_5 | AA18<br>VCCO_5 | AA19<br>VCCO_5 | AA20<br>VCCO_5 | AA21<br>VCCO_5 | AA22<br>VCCO_6 | AA23<br>I/O | AA24<br>I/O | AA25<br>SDRAM_A(5) | AA26<br>SDRAM_A(4) | AA27<br>SDRAM_A(3) | AA28<br>SDRAM_DQM(3) | AA29<br>SDRAM_DQ(31) | AA30<br>SDRAM_DQ(30) |
| AB1<br>Adr(7) | AB2<br>Adr(8) | AB3<br>Adr(9) | AB4<br>Adr(10) | AB5<br>I/O | AB6<br>I/O | AB7<br>I/O | AB8<br>I/O | AB9<br>VCCO_3 | AB10<br>VCCO_4 | AB11<br>VCCO_4 | AB12<br>VCCO_4 | AB13<br>VCCO_4 | AB14<br>I/O | AB15<br>Data(3) | AB16<br>IRQ2_n | AB17<br>I/O | AB18<br>VCCO_5 | AB19<br>VCCO_5 | AB20<br>VCCO_5 | AB21<br>VCCO_5 | AB22<br>VCCO_6 | AB23<br>I/O | AB24<br>I/O | AB25<br>I/O | AB26<br>I/O | AB27<br>SDRAM_A(9) | AB28<br>SDRAM_A(8) | AB29<br>SDRAM_A(7) | AB30<br>SDRAM_A(6) |
| AC1<br>GND | AC2<br>Adr(4) | AC3<br>Adr(5) | AC4<br>Adr(6) | AC5<br>I/O | AC6<br>I/O | AC7<br>CCLK | AC8<br>DONE | AC9<br>D0 | AC10<br>Data(16) | AC11<br>Data(11) | AC12<br>Data(9) | AC13<br>I/O | AC14<br>I/O | AC15<br>Data(2) | AC16<br>IRQ1_n | AC17<br>I/O | AC18<br>I/O | AC19<br>TestCon(8) | AC20<br>TestCon(9) | AC21<br>TestCon(10) | AC22<br>D7 | AC23<br>M2 | AC24<br>M1 | AC25<br>I/O | AC26<br>I/O | AC27<br>SDRAM_DQM(1) | AC28<br>SDRAM_A(12) | AC29<br>SDRAM_CKE | AC30<br>GND |
| AD1<br>I/O | AD2<br>I/O | AD3<br>I/O | AD4<br>I/O | AD5<br>Adr(2) | AD6<br>Adr(3) | AD7<br>PWRDWN_B | AD8<br>I/O | AD9<br>D1 | AD10<br>Data(15) | AD11<br>Data(10) | AD12<br>Data(8) | AD13<br>I/O | AD14<br>Data(7) | AD15<br>Data(1) | AD16<br>IRQ0_n | AD17<br>I/O | AD18<br>I/O | AD19<br>TestCon(11) | AD20<br>TestCon(12) | AD21<br>TestCon(13) | AD22<br>D6 | AD23<br>I/O | AD24<br>M0 | AD25<br>SDRAM_DQ(9) | AD26<br>SDRAM_DQ(8) | AD27<br>I/O | AD28<br>I/O | AD29<br>I/O | AD30<br>I/O |
| AE1<br>I/O | AE2<br>I/O | AE3<br>I/O | AE4<br>I/O | AE5<br>I/O | AE6<br>GND | AE7<br>Data(25) | AE8<br>Data(24) | AE9<br>Data(20) | AE10<br>Data(14) | AE11<br>I/O | AE12<br>I/O | AE13<br>GND | AE14<br>Data(6) | AE15<br>Data(0) | AE16<br>Empty | AE17<br>I/O | AE18<br>GND | AE19<br>I/O | AE20<br>I/O | AE21<br>I/O | AE22<br>I/O | AE23<br>I/O | AE24<br>I/O | AE25<br>GND | AE26<br>I/O | AE27<br>I/O | AE28<br>I/O | AE29<br>I/O | AE30<br>I/O |
| AF1<br>I/O | AF2<br>I/O | AF3<br>I/O | AF4<br>I/O | AF5<br>GND | AF6<br>I/O | AF7<br>INIT_B | AF8<br>Data(23) | AF9<br>Data(19) | AF10<br>Data(13) | AF11<br>I/O | AF12<br>I/O | AF13<br>I/O | AF14<br>Data(5) | AF15<br>SDRAM_CLKin | AF16<br>SDRAM_CLK | AF17<br>I/O | AF18<br>I/O | AF19<br>I/O | AF20<br>I/O | AF21<br>TTC_n(7) | AF22<br>TTC_n(6) | AF23<br>TTC_n(4) | AF24<br>RDWR_B | AF25<br>I/O | AF26<br>GND | AF27<br>I/O | AF28<br>I/O | AF29<br>I/O | AF30<br>I/O |
| AG1<br>Data(30) | AG2<br>Data(31) | AG3<br>Adr(0) | AG4<br>GND | AG5<br>Adr(1) | AG6<br>DOUT | AG7<br>D2 | AG8<br>VRP_4 | AG9<br>GND | AG10<br>Data(12) | AG11<br>I/O | AG12<br>GND | AG13<br>I/O | AG14<br>Data(4) | AG15<br>SharcWr_n | AG16<br>LHC_Clk | AG17<br>ChaBusy | AG18<br>I/O | AG19<br>GND | AG20<br>I/O | AG21<br>TTC_n(5) | AG22<br>GND | AG23<br>VRN_5 | AG24<br>D5 | AG25<br>CS_B | AG26<br>SDRAM_DQ(13) | AG27<br>GND | AG28<br>SDRAM_DQ(12) | AG29<br>SDRAM_DQ(11) | AG30<br>SDRAM_DQ(10) |
| AH1<br>Data(26) | AH2<br>Data(27) | AH3<br>GND | AH4<br>Data(28) | AH5<br>Data(29) | AH6<br>GNDA16 | AH7<br>D3 | AH8<br>VRN_4 | AH9<br>Data(18) | AH10<br>I/O | AH11<br>I/O | AH12<br>GNDA18 | AH13<br>I/O | AH14<br>GND | AH15<br>SharcRd_n | AH16<br>Clk | AH17<br>GND | AH18<br>I/O | AH19<br>GNDA19 | AH20<br>I/O | AH21<br>I/O | AH22<br>TTC_n(3) | AH23<br>VRP_5 | AH24<br>D4 | AH25<br>GNDA21 | AH26<br>I/O | AH27<br>I/O | AH28<br>GND | AH29<br>SDRAM_DQ(15) | AH30<br>SDRAM_DQ(14) |
| AJ1<br>VCCAUX | AJ2<br>GND | AJ3<br>VRP_3 | AJ4<br>AVCCAUXRX16 | AJ5<br>VTRXPAD16 | AJ6<br>AVCCAUXTX16 | AJ7<br>VTTXPAD16 | AJ8<br>Data(22) | AJ9<br>Data(17) | AJ10<br>I/O | AJ11<br>AVCCAUXRX18 | AJ12<br>VTRXPAD18 | AJ13<br>AVCCAUXTX18 | AJ14<br>VTTXPAD18 | AJ15<br>Rocket_XClk | AJ16<br>Clkx2 | AJ17<br>AVCCAUXRX19<br>ROCKET_LVDS<br>ROCKET_LVDS<br>T<br>Rocket_RXN | AJ18<br>VTRXPAD19<br>ROCKET_LVDS<br>ROCKET_LVDS<br>T<br>Rocket_RXP | AJ19<br>AVCCAUXTX19<br>ROCKET_LVDS<br>ROCKET_LVDS<br>S<br>Rocket_TXP | AJ20<br>VTTXPAD19<br>ROCKET_LVDS<br>ROCKET_LVDS<br>S<br>Rocket_TXN | AJ21<br>I/O | AJ22<br>TTC_n(2) | AJ23<br>TTC_n(1) | AJ24<br>AVCCAUXRX21 | AJ25<br>VTRXPAD21 | AJ26<br>AVCCAUXTX21 | AJ27<br>VTTXPAD21 | AJ28<br>VRP_6 | AJ29<br>GND | AJ30<br>VCCAUX |
| | AK2<br>VCCAUX | AK3<br>VRN_3 | AK4<br>I/O | AK5<br>I/O | AK6<br>I/O | AK7<br>I/O | AK8<br>Data(21) | AK9<br>GND | AK10<br>I/O | AK11<br>I/O | AK12<br>I/O | AK13<br>I/O | AK14<br>I/O | AK15<br>VCCAUX | AK16<br>VCCAUX | AK17<br>Rocket_RXN | AK18<br>Rocket_RXP | AK19<br>Rocket_TXP | AK20<br>Rocket_TXN | AK21<br>I/O | AK22<br>GND | AK23<br>TTC_n(0) | AK24<br>I/O | AK25<br>I/O | AK26<br>I/O | AK27<br>I/O | AK28<br>VRN_6 | AK29<br>VCCAUX | |

Input FPGA Power pins:
VCCAUX (2V5) 16 pins
VCCINT (1V5) 32 pins
VCCO_# (3V3) 10 pins each
GND 124 pins

| | | |
|---|---|---|
| channel_in | | Rev V2 2 |
| | | Date 7 Feb 2006 |
| Input FPGA | | Time 1:50:53 pm |
| Proj: MROD-X | Proj.No: 38405 | Name tonvr |
| Peter Jansweijer | peterj@nikhef.nl | |
| NIKHEF | NATIONAAL INSTITUUT VOOR KERN-FYSICA EN HOGE ENERGIE-FYSICA<br>KRUISLAAN 409, 020-592 2000<br>1098 SJ AMSTERDAM NEDERLAND | Size A3 4 1 4 A |
| © ET-Nikhef Amsterdam | | Dim 420 x 297 mm |
| | | Page 2 of 6 |