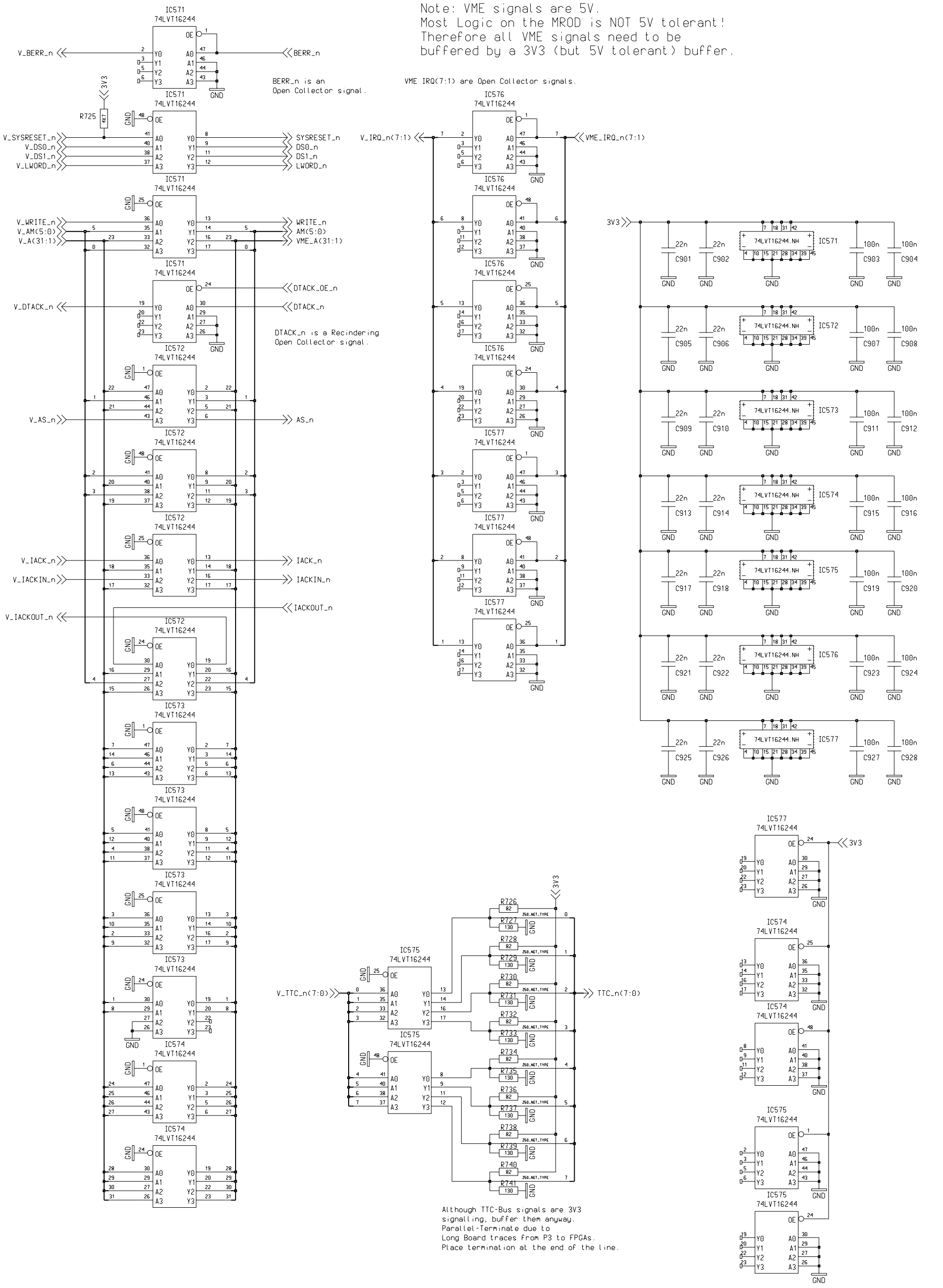
Note: VME signals are 5V.
Most Logic on the MROD is NOT 5V tolerant!
Therefore all VME signals need to be
buffered by a 3V3 (but 5V tolerant) buffer.



MROD-Out	Rev 12
VME Bus Other Buffers	Date 26 Jan 2005
Proj: MROD-X / Proj.No:	Time 12:06:41 pm
Peter Jansweijer / peterj@nikhef.nl	Name Peter Jansweijer
NIKHEF ©ET-Nikhef Amsterdam	Size A3 / 4 1 4 A
NATIONAAL INSTITUUT VOOR KERN-FYSICA EN HOGE ENERGIE-FYSICA KRUISLAAN 409, 020-592 2000 1098 SJ AMSTERDAM NEDERLAND	Dim 297 x 420 mm
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