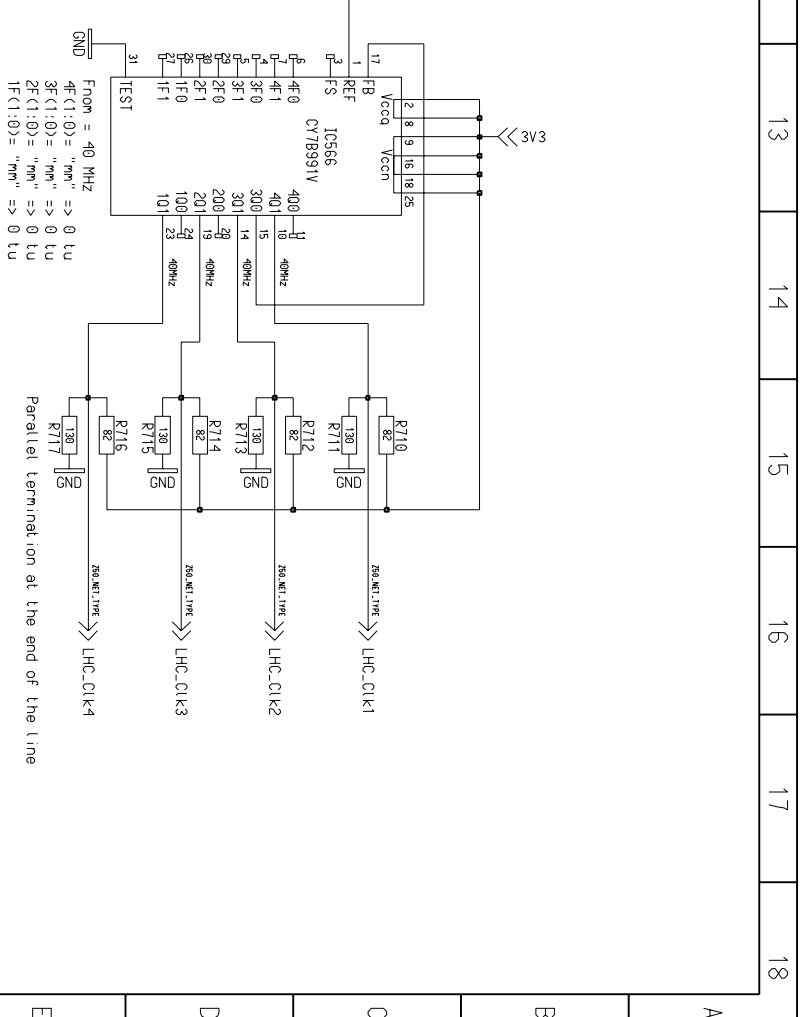
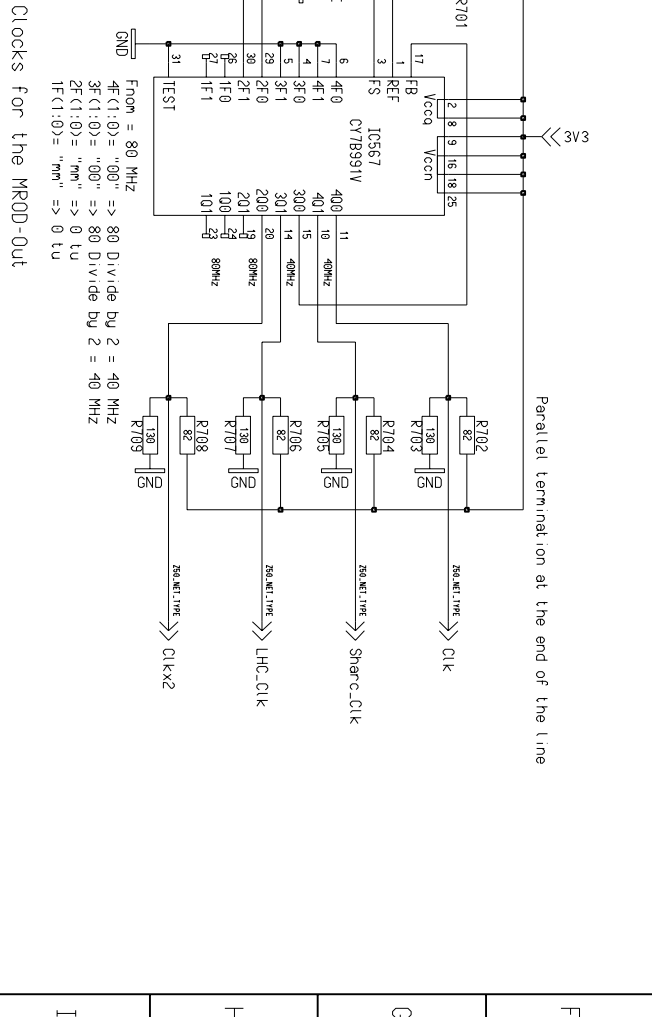


Rocket-IO Inter-FPGA Link Frequency
 80 MHz 16 bit = 40 MHz 32 bit
 = 160 MB/s



LHC Clocks for the MRD-Ins



Clocks for the MRD-Out

MRD-Out		Rev	34
Clocks		Date	26 Jan 2005
Proj: MRD-X		Time	12:05:37 pm
Proj: No:38405		Name	Peter Jansweijer
Peter Jansweijer		Size	A3
petern@nikhef.nl		Dim	420 x 297 mm
NATIONAAL INSTITUUT VOOR KERN- FYSICA EN HOOG ENERGIE-FYSICA KRUISLAAN 409, 020-592 2000 1098 SJ AMSTERDAM NEDERLAND		Page	14 of 19
©ET-Nikhef Amsterdam			