



Note that the 33 ohm series termination resistors for the LRL, LD0Mn and LFF_n signals should be placed near the S-Link connector.

Note that series termination is incorporated by the Digital Impedance (DIT) feature of the Output FPGA.

MRD-Out

S-Link and Output FPGA Auxiliary Connections

Proj: MRD-Out	Proj: No: 38405	Rev	61
Other (Output) FPGA Control signals		Date	26 Jan 2005
		Time	12:04:12 pm
		Name	peter.j
		Size	A3
		Dim	420 x 297 mm
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