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|-------------------------------------------------------------------------------------------------------------------------------|--|------|------------------|
| channel_in                                                                                                                    |  | Rev  | 45               |
| Input FPGA Power Supply Decoupling                                                                                            |  | Date | 26 Jan 2005      |
| Proj: MR00-X                                                                                                                  |  | Time | 12:10:10 pm      |
| Peter Jansweijer                                                                                                              |  | Name | Peter Jansweijer |
| petern@nikhef.nl                                                                                                              |  | Size | A3               |
| NATIONAAL INSTITUUT VOOR KERN-<br>FYSICA EN HOOG ENERGIE-FYSICA<br>KRUISLAAN 409, 020-532 2000<br>1098 SJ AMSTERDAM NEDERLAND |  | Dim  | 420 x 297 mm     |
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