

Connections for different test setups with the Hewlett Packard 1662A Logic Analyzer.

Four test setup files are stored on disk.

Testing the VME part of the MROD-Out:

MR_O_VME	
Connector	Analyzer POD
J201	1
J202	2
J203	3
J204	4

Testing the Sharc Cluster of the MROD-Out:

MR_O_SHA	
Connector	Analyzer POD
J201	1
J202	2
J205	3
J204	4

Testing the S-LINK Output of the MROD-Out:

MR_O_SLK	
Connector	Analyzer POD
J206	1
J202	2
J205	3
J204	4

Testing the TTC Interface of the MROD-Out:

MR_O_TTC	
Connector	Analyzer POD
J206	1
J202	2
J205	3
J204	4