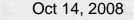
Thoughts over a data acquisition system for reading out KM3NeT Optical **Modules**







Data Communication

Real-Time

Need High Bandwidth

10 Gbps => 100 ps/bit => 10 PMTs in a 1 ns slot

More than 10 PMTs => determine priority and accept that you sometimes loose data

Link utilization not 100%

Data

Some framing overhead

Some link

synchronization overhead

Semi Real-Time

- Postpone a transmission and code the delay in the data
 Data can be smeared over time => lower bandwidth => less power
- Need some electronics (FPGA)

Store and forward

- The <u>same</u> electronics could timestamp events

delay

Timing transfer over data-link phase-locks and calibrates the Local Clock

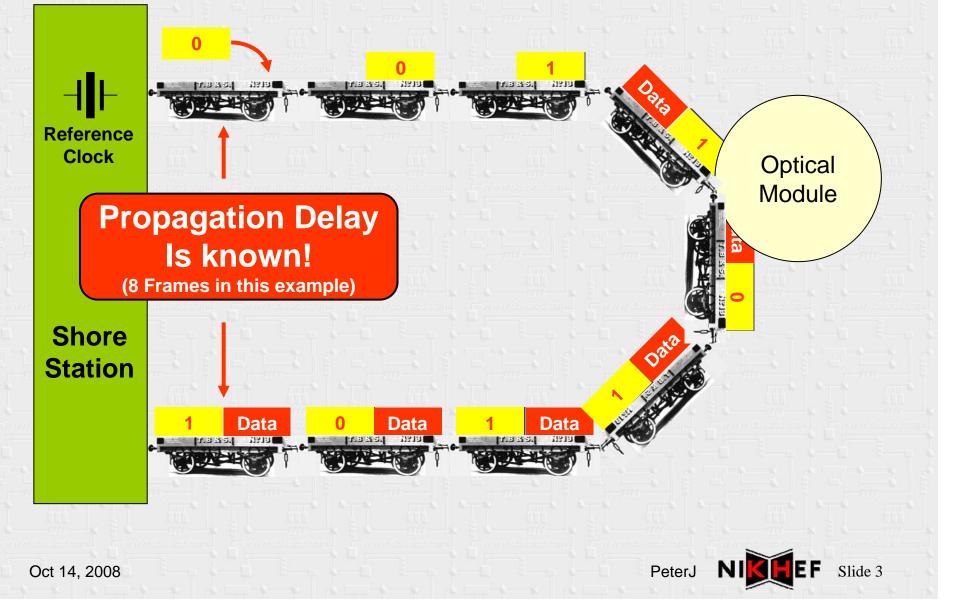
- Low bandwidth
- On shore data is already pre-formatted

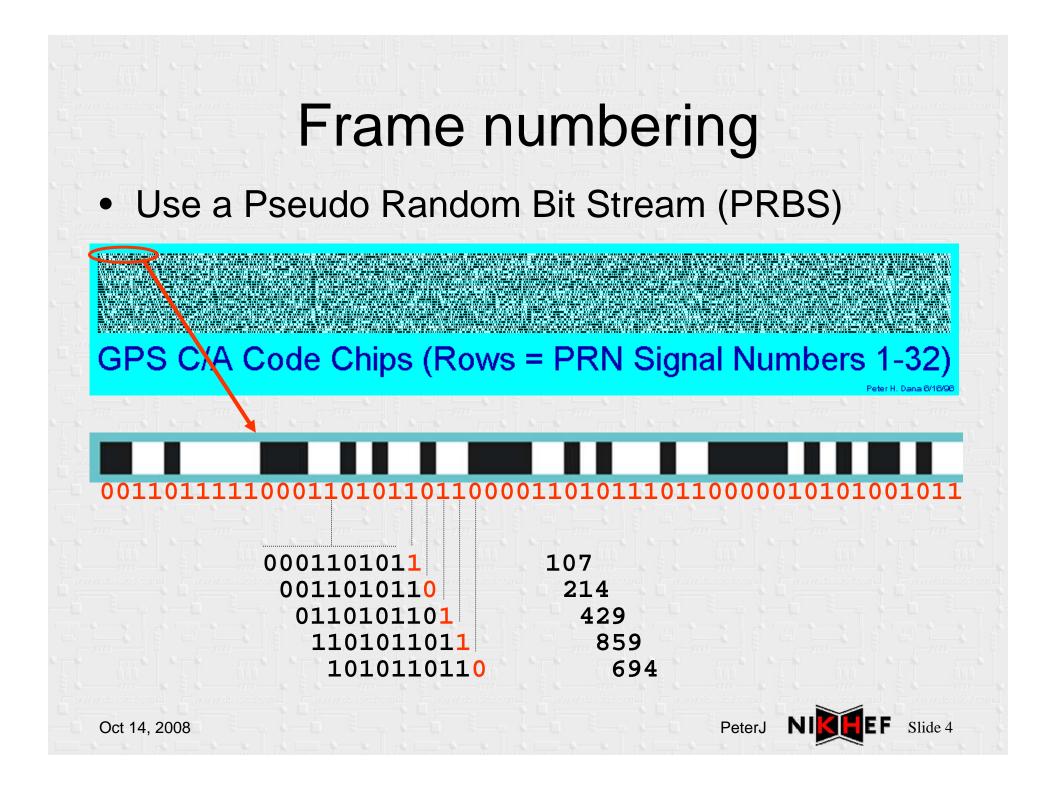
Oct 14, 2008

PeterJ N



Frame Numbering and Calibration





Conclusion

- All Receivers (in Optical Modules and in Shore Station) are phase locked to the Shore Station Transmitter Clock.
- Calibration is done automatically and constantly. Data can be corrected on the fly.
- Frames can be numbered with a 1-bit overhead.
- This proposal fits both "Semi Real-Time" and "Store and Forward" concepts.

NIKHEF Slide 5

PeterJ

