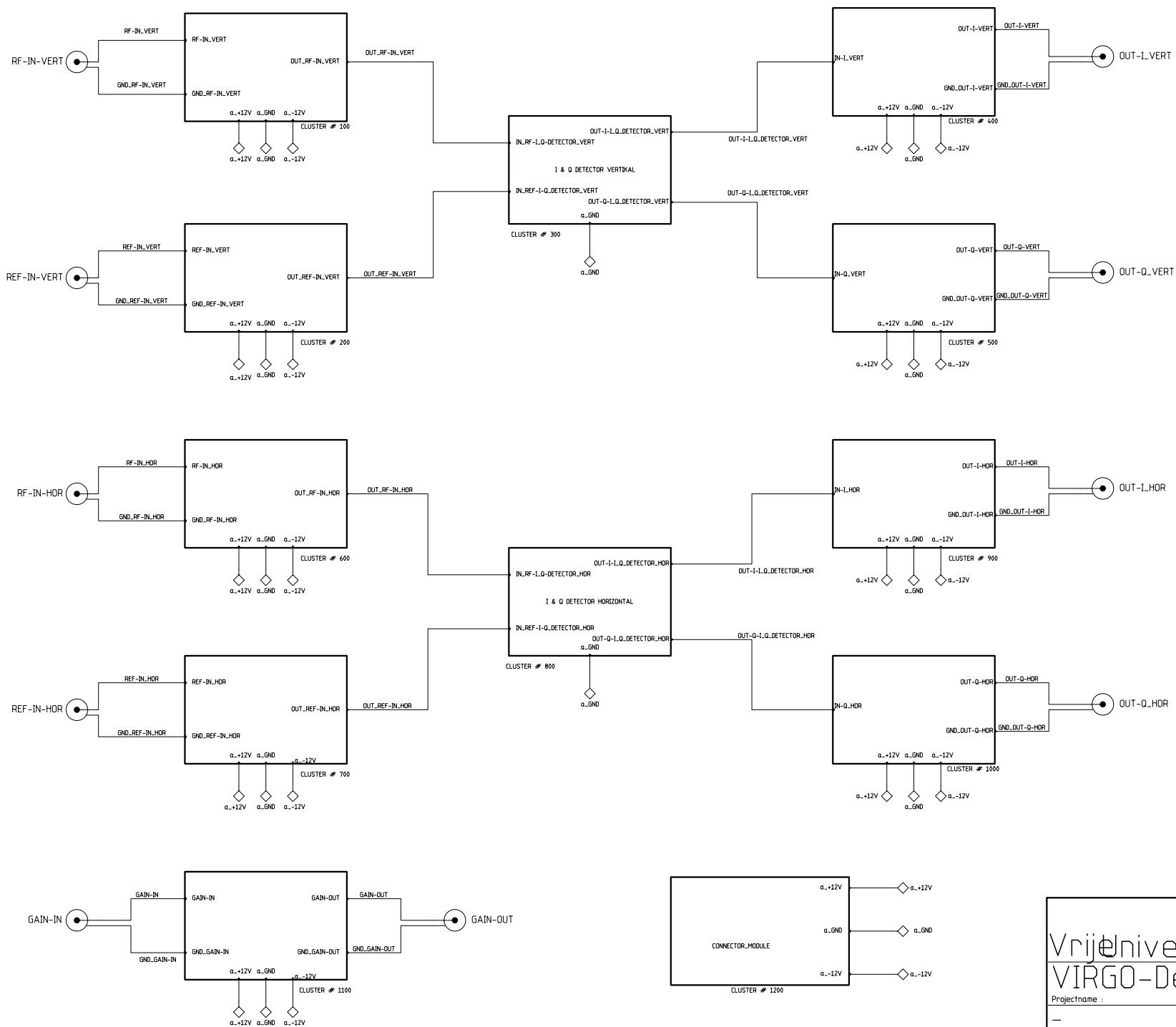


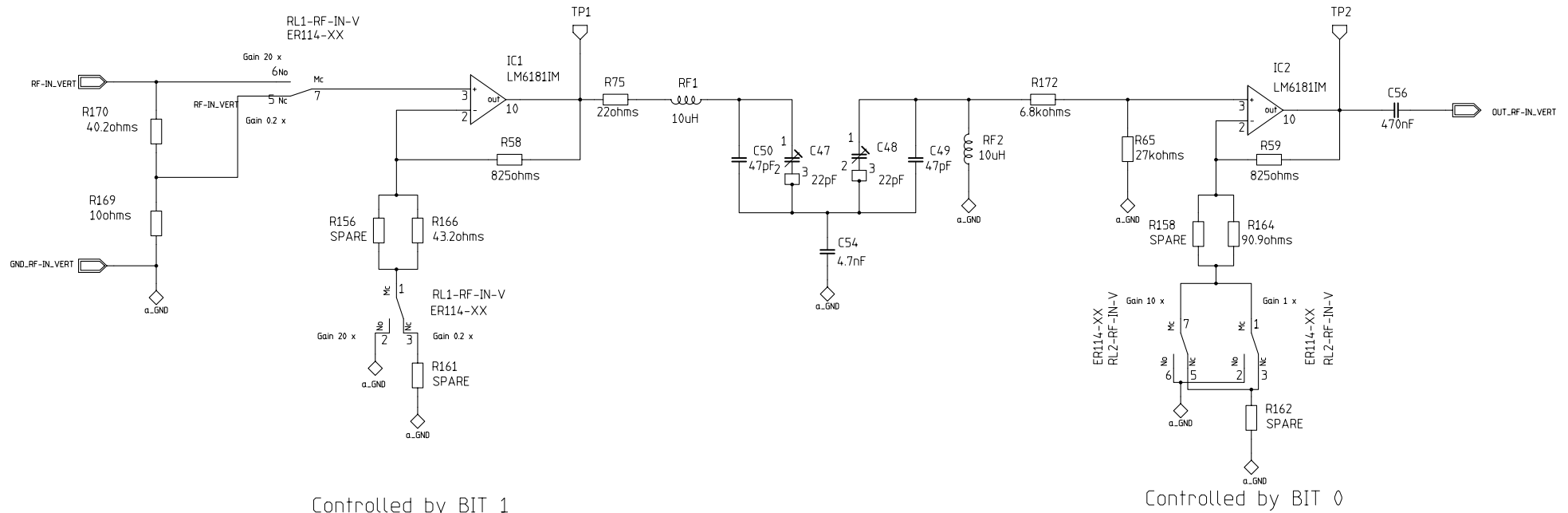


Gain 20 x / 0.2x

Band pass filter 6.26 MHz

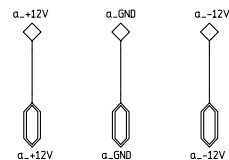
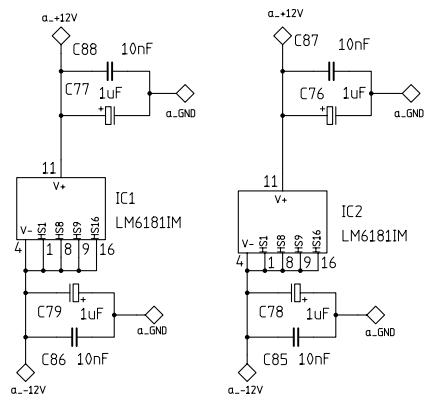
Gain 10x / 1x

Bandwidth 300 kHz



Controlled by BIT 1

Controlled by BIT 0



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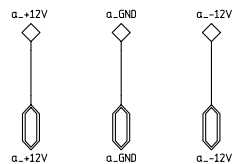
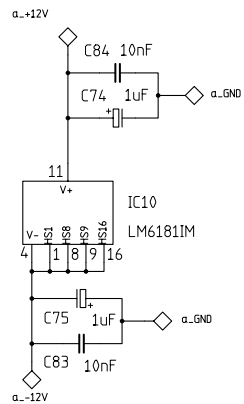
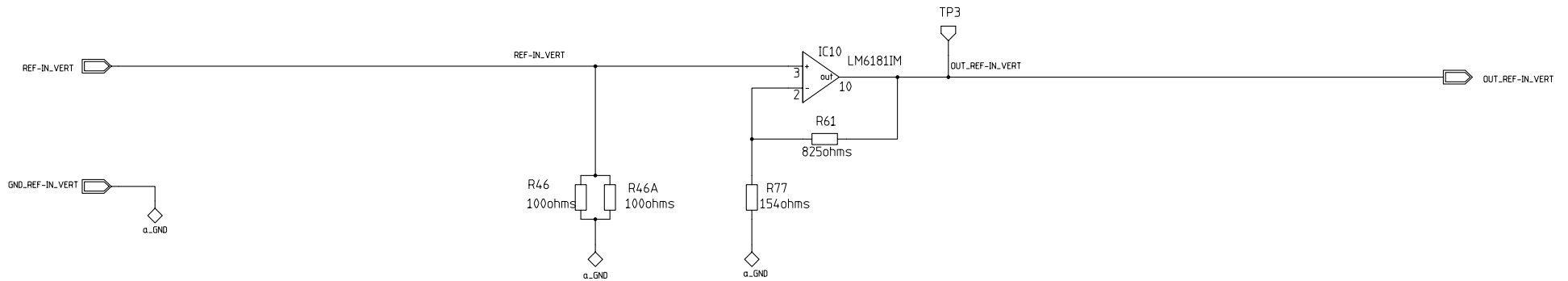
Last modified 08-11-2006

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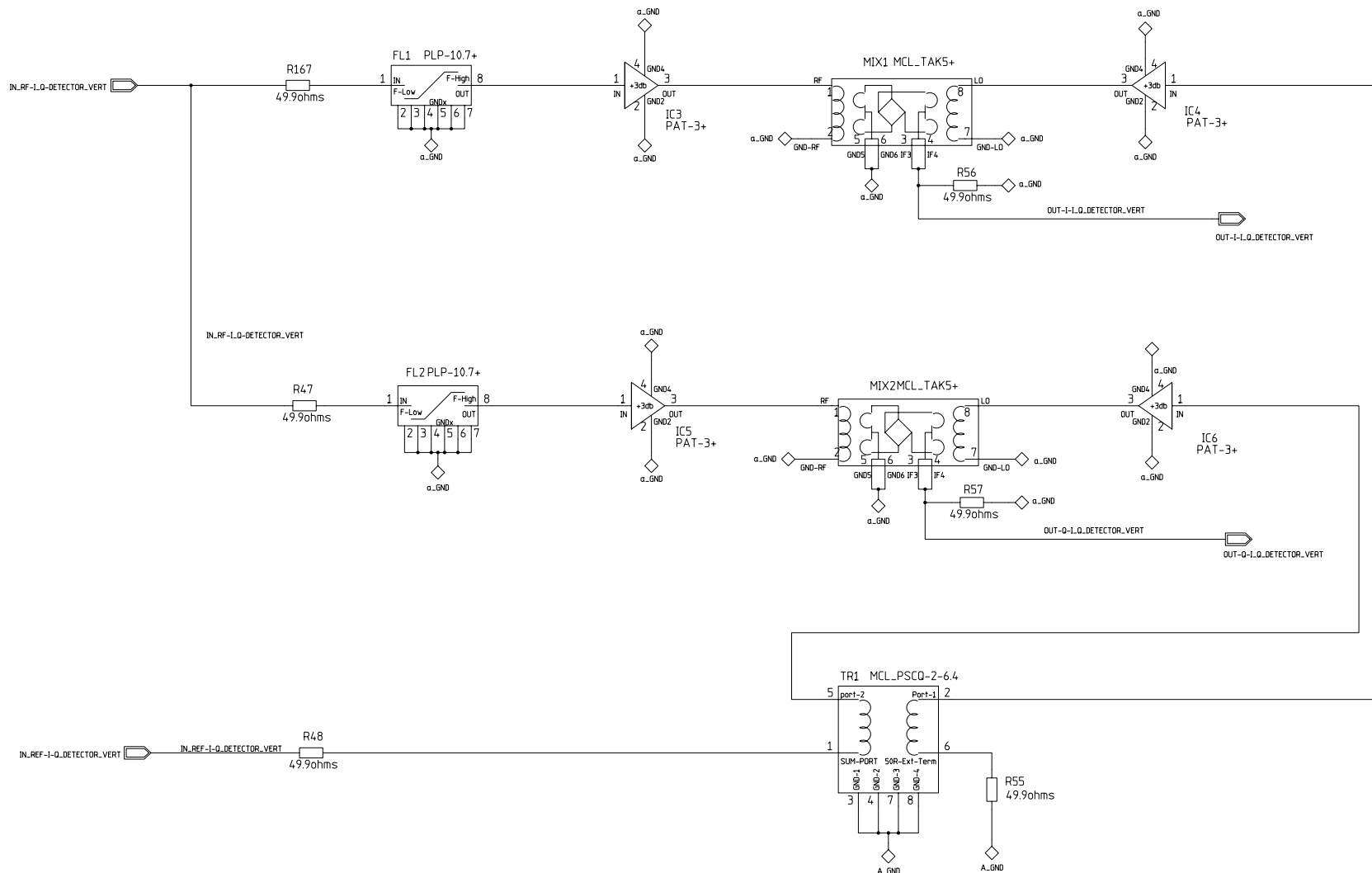
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Vrije Universiteit Amsterdam Elektronica groep
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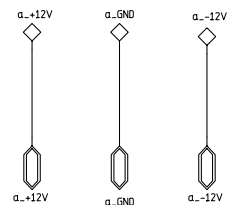
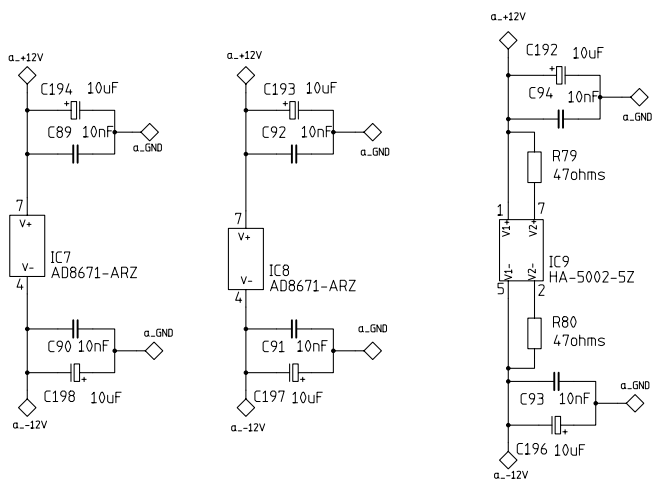
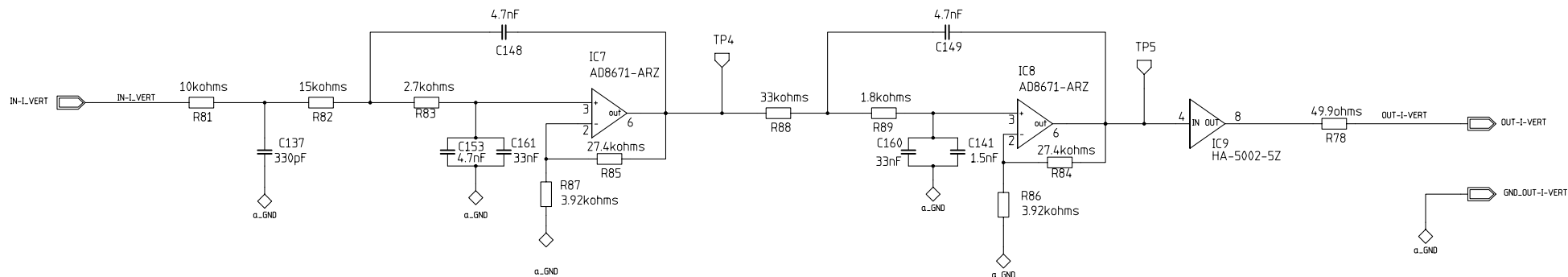
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Projectnumber : 20062001
Pagetnumber :

Designer: H.Voet / M.Molenaar
Schematic: VIRGO-Demodulator
PCB : VIRGO-Demodulator

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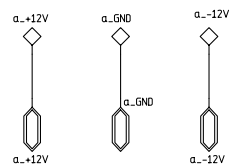
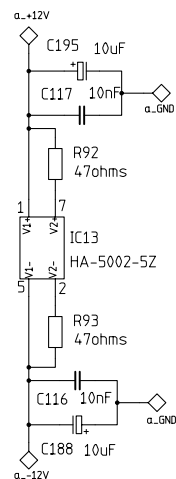
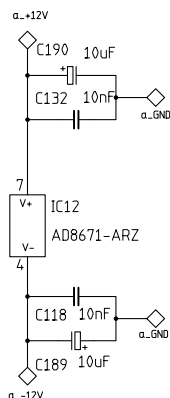
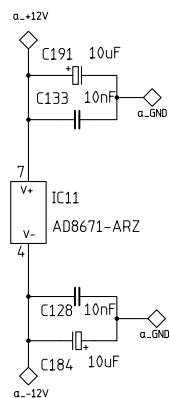
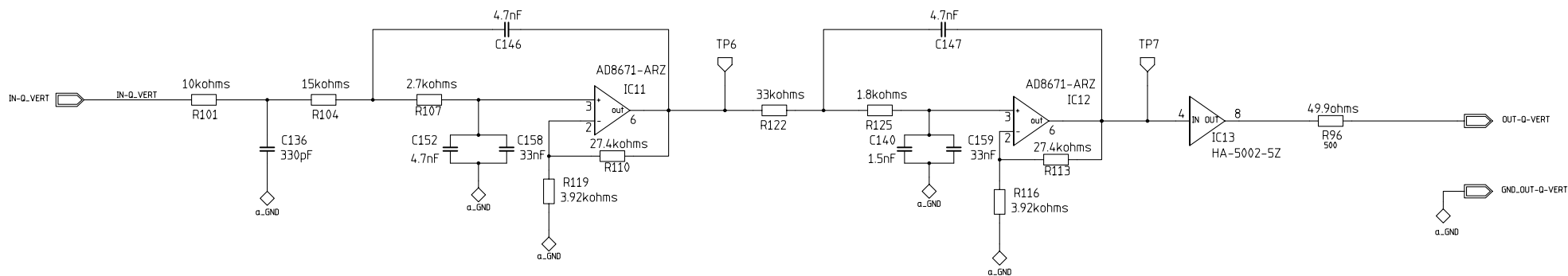
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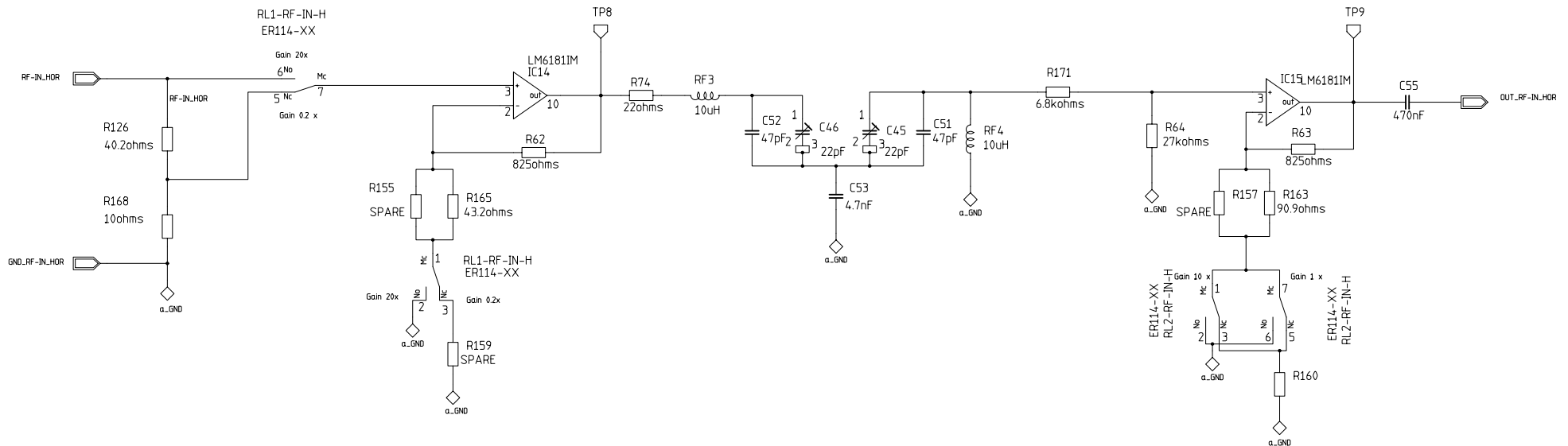


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Gain 20 x / 0.2x

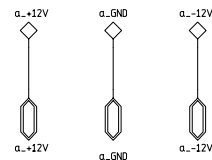
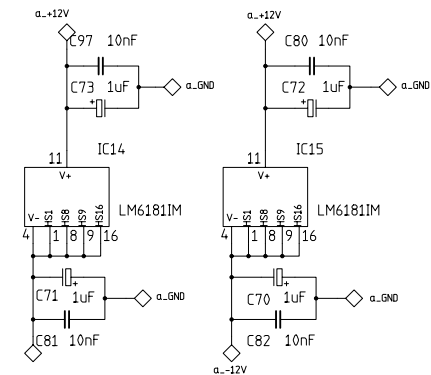
Band pass filter 6.26 MHz
Bandwidth 300kHz

Gain 10x / 1x

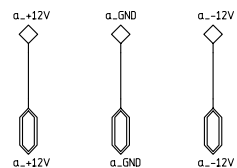
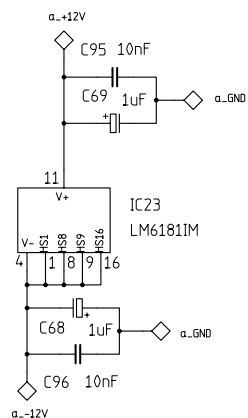
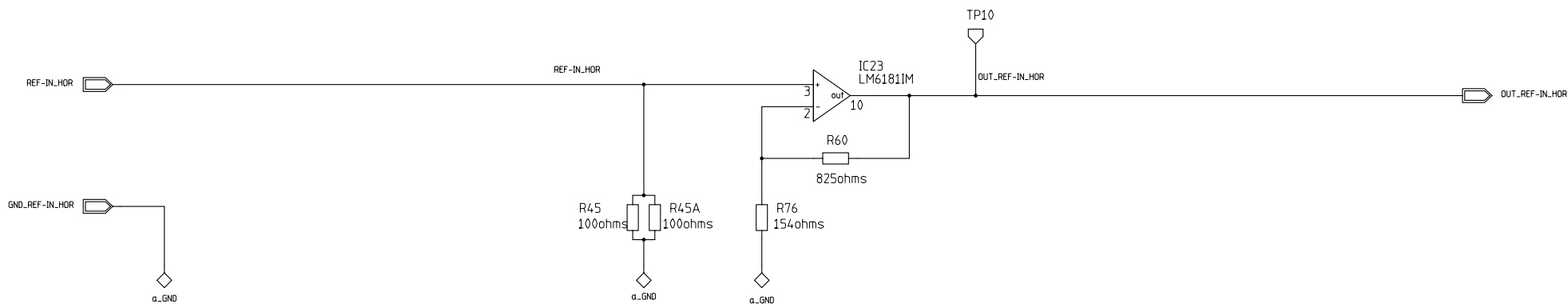


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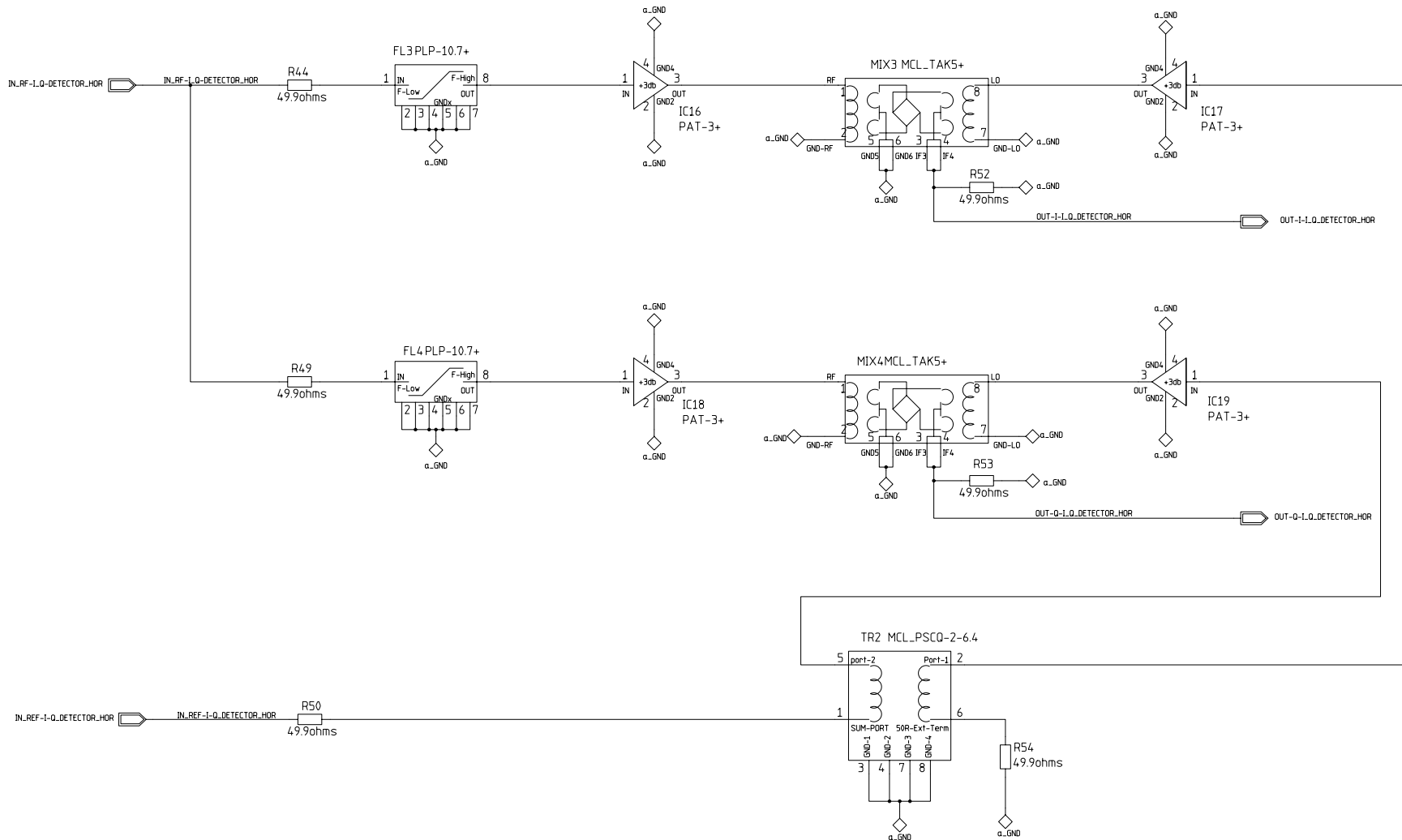
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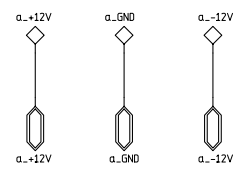
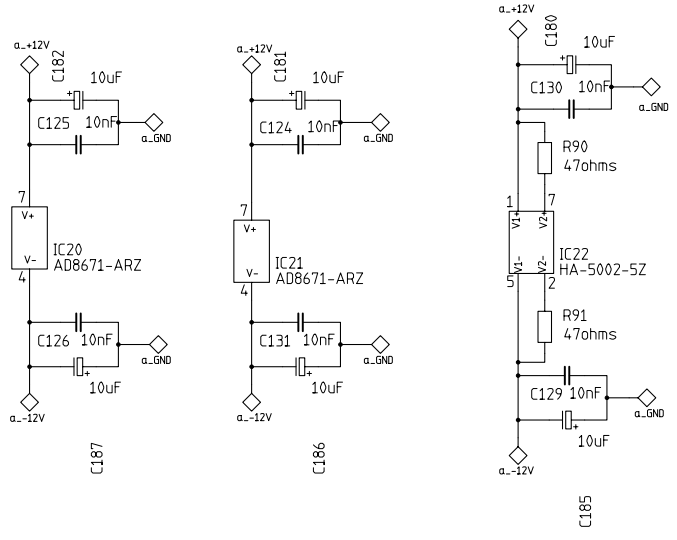
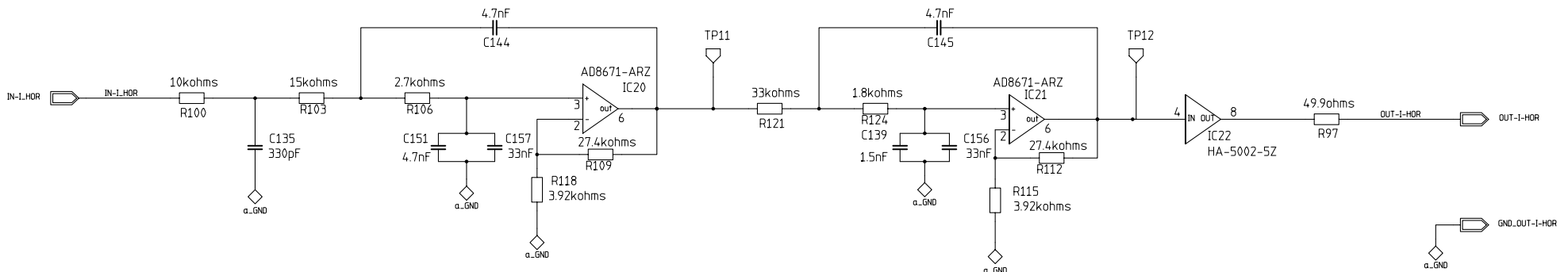
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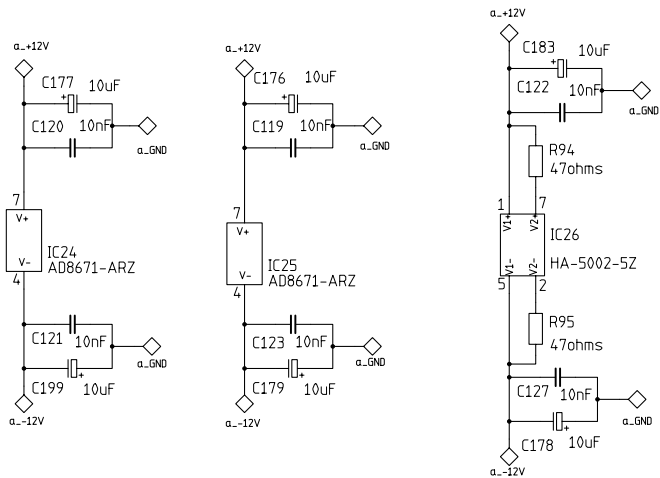
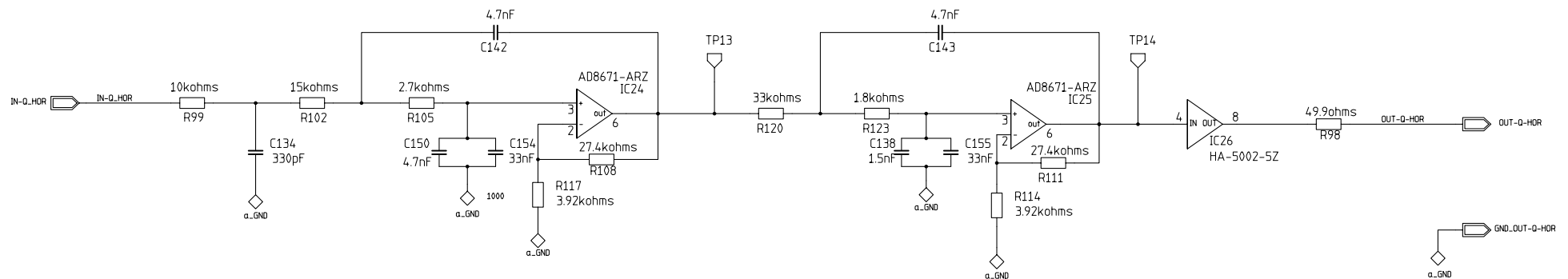
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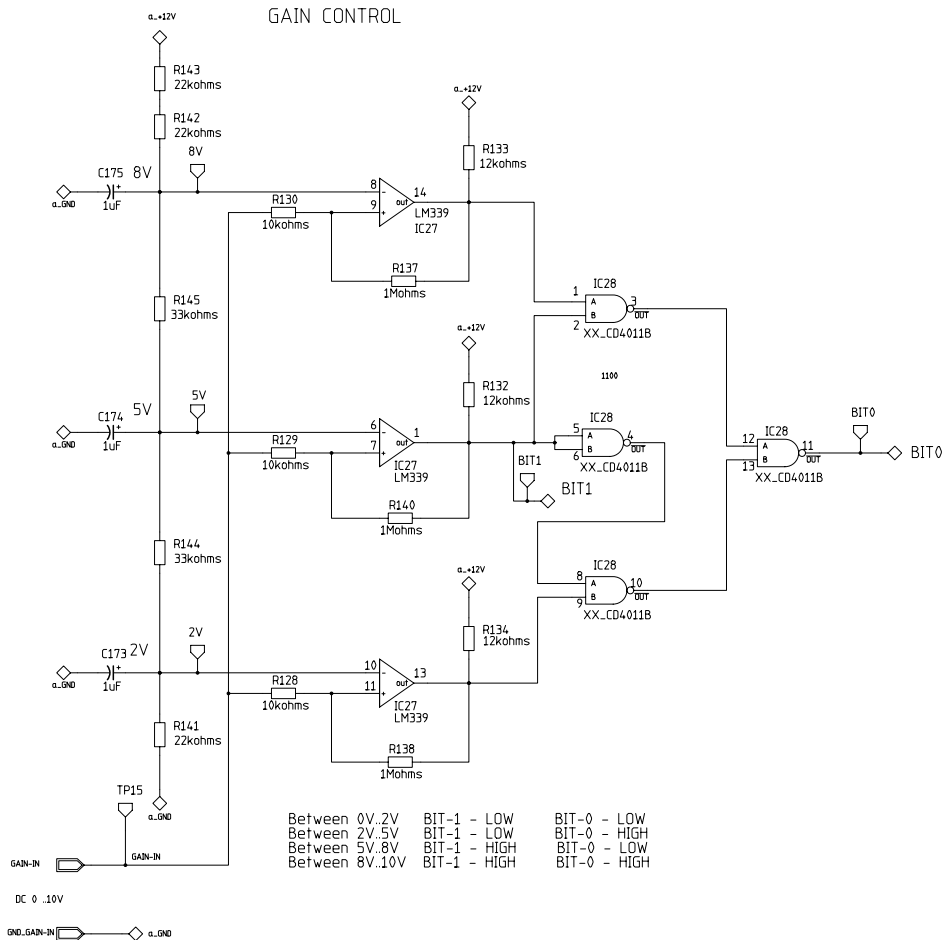
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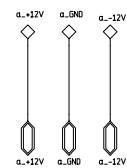
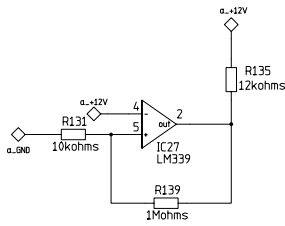
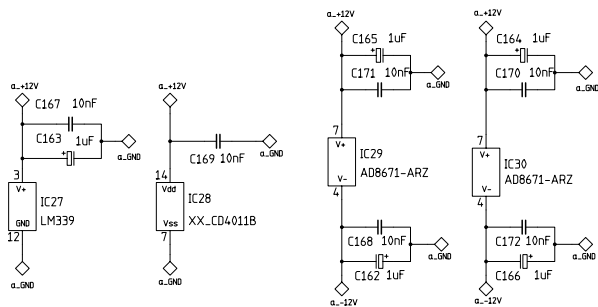
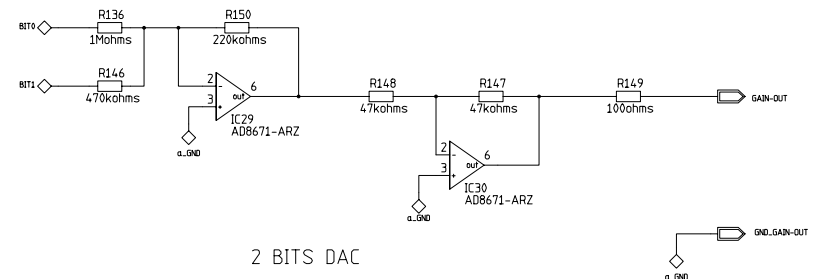
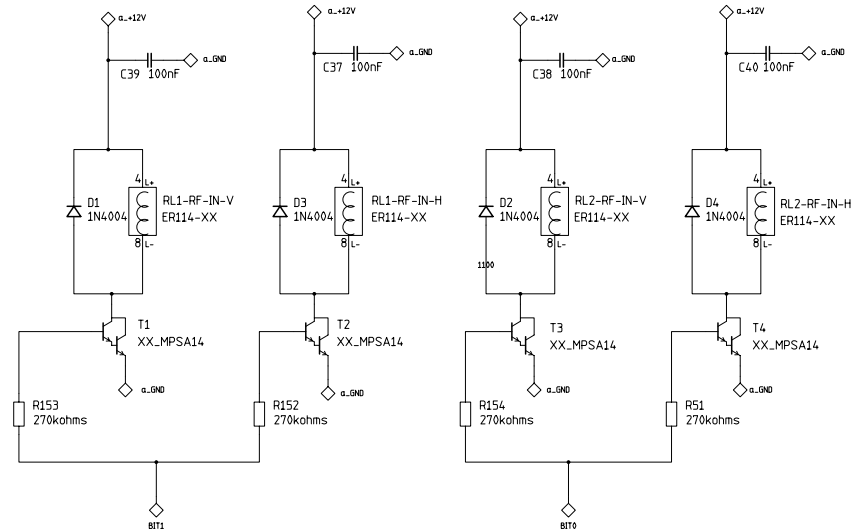


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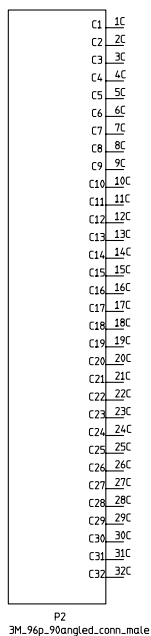
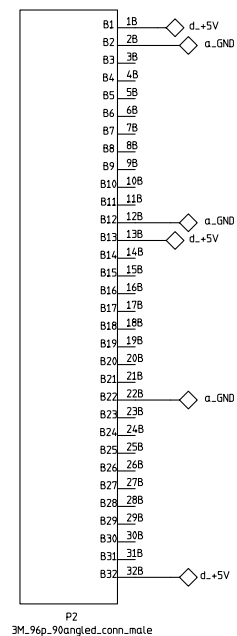
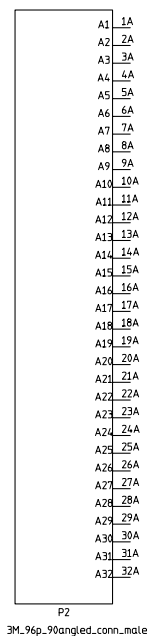
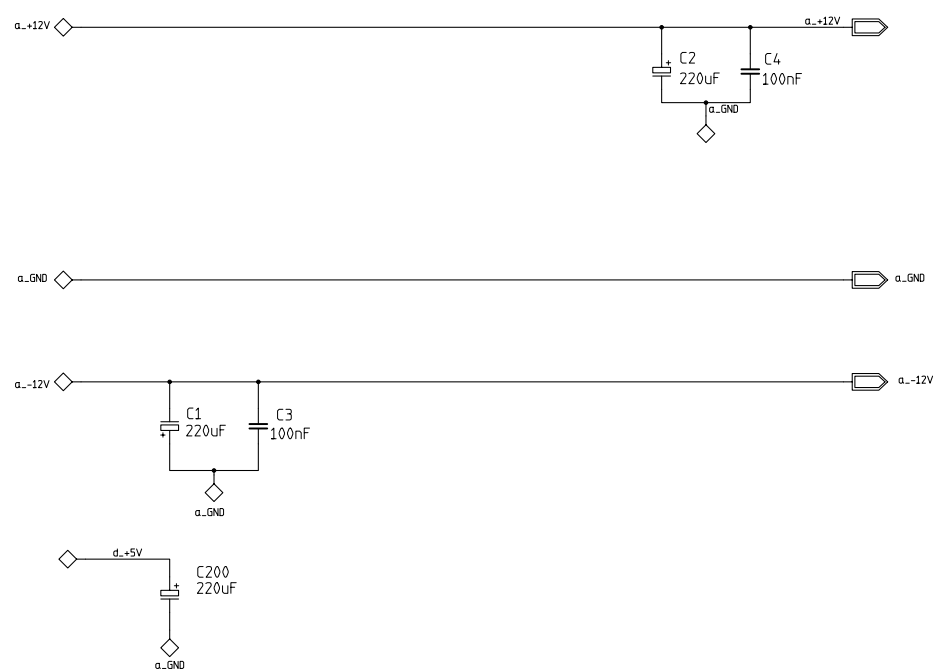
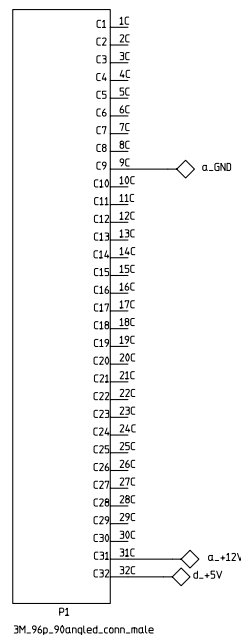
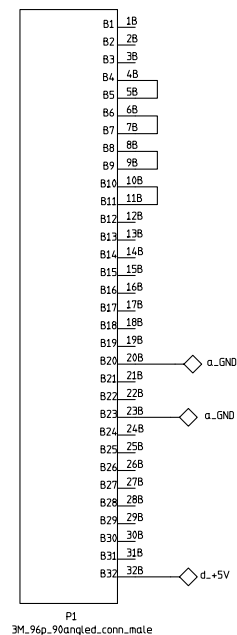
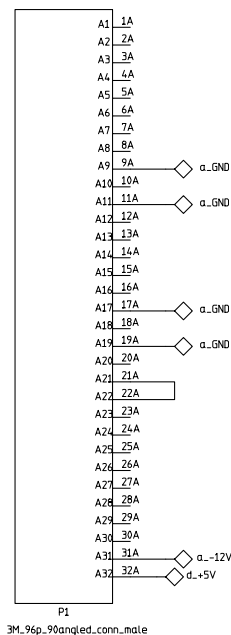


Gain 20x / 0.2x

Gain 10x / 1x



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