

Related topics

Half-wave rectifier, full-wave rectifier, Graetz rectifier, diode, Zener diode, avalanche effect, charging capacitor, ripple, r.m.s. value, internal resistance, smoothing factor, ripple voltage, voltage stabilisation, voltage doubling.

Principle and task

The ripple of the output voltage of various rectifier circuits is measured as a function of the load current strength and the charging capacitance. The characteristics of a voltage stabilizer and of a multiplier are investigated.

Equipment

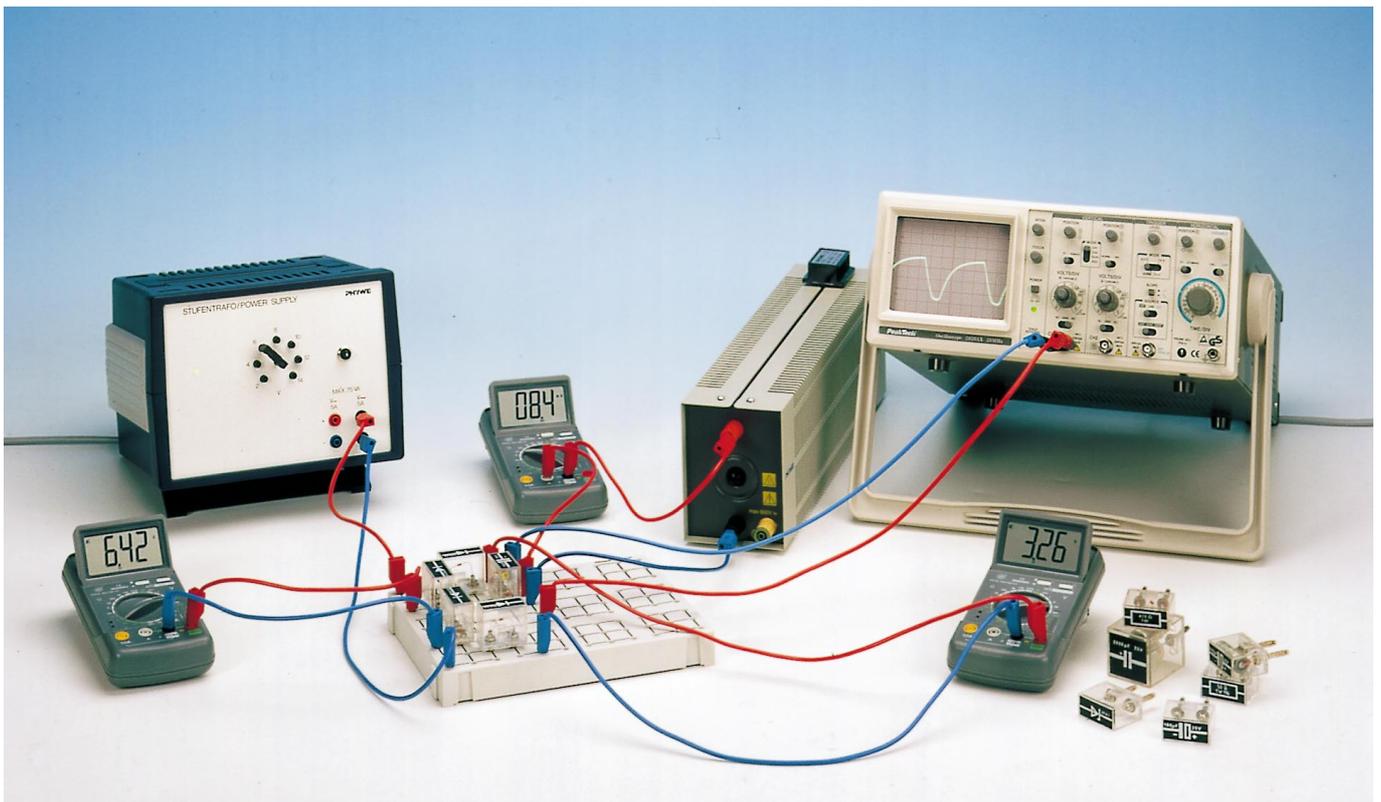
Plug-in board, 4 mm plugs	06033.00	1
PEK semiconductor diode/si/IN4148	39106.02	4
PEK electrol.capacitor 500 mm F/35 V	39105.26	1
PEK electrol. capacitor 10 mm F/ 70 V	39105.28	4
PEK electro. capacitor 2000 mm F/25 V	39113.08	1
Capacitor, electrolytic, 1000 mic-F	06049.09	1
PEK carbon resistor 1W 5% 470 Ohm	39104.15	1
PEK carbon resistor 2W 5% 47 Ohm	39104.62	1
PEK low power zener diode ZF 4.7	39132.01	1
Multitap transf., 14 VAC/12 VDC, 5 A	13533.93	1
Oscilloscope, 20 MHz, 2 channels	11454.93	1
Digital multimeter	07134.00	3
Rheostat, 330 Ohm, 1.0 A	06116.02	1
Adapter, BNC-plug/socket 4 mm	07542.26	1
PEK connect.plug white 19 mm pitch	39170.00	3
Connecting cord, 250 mm, red	07360.01	2
Connecting cord, 250 mm, blue	07360.04	2

Connecting cord, 500 mm, red	07361.01	4
Connecting cord, 500 mm, blue	07361.04	4

Problems

1. Using the half-wave rectifier:
 - a) to display the output voltage (without charging capacitor) on the oscilloscope
 - b) to measure the diode current I_D as a function of the output current strength I_o (with the charging capacitor)
 - c) to measure the ripple component U_{ACpp} of the output voltage as a function of the output current ($C = \text{constant}$)
 - d) to measure the ripple as a function of the capacitance ($I_o = \text{constant}$)
 - e) to measure the output voltage U_o as a function of the input voltage U_i ($I_o = 0$).
2. Using the bridge rectifier:
 - a) to display the output voltage (without charging capacitor) on the oscilloscope
 - b) to measure the current through one diode, I_D , as a function of the output current I_o (with the charging capacitor)
 - c) to measure the ripple of the output voltage as a function of the output current ($C = \text{constant}$)
 - d) to measure the ripple as a function of the capacitance ($I_o = \text{constant}$)
 - e) to measure the output voltage as a function of the input voltage.

Fig. 1: Bridge rectifier with voltage stabilization.



- To measure the voltage at the charging capacitor, U_C , and the output voltage of a stabilized voltage source as a function of the input voltage U_i .
- To measure the output voltage of a voltage multiplier circuit as a function of the input voltage.

Set-up and procedure

Set up the experiment on the universal plug-board in accordance with Fig. 1 and the circuit diagrams in Figs. 2 to 5.

The output current is varied by the load resistor R_L . Care should be taken that the current does not exceed the maximum permissible IA through the silicon diodes.

Fig. 2: Half-wave rectifier circuit.

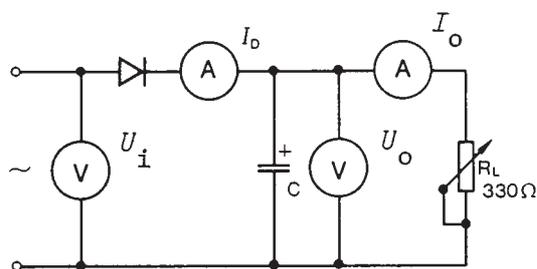


Fig. 3: Experimental bridge rectifier circuit.

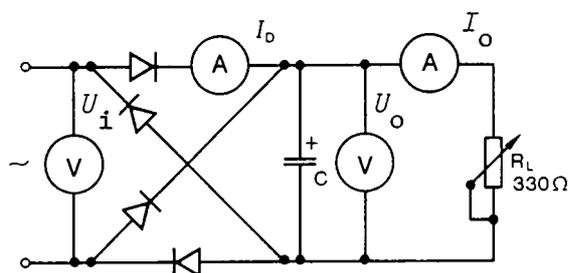


Fig. 4: Experimental voltage stabiliser circuit.

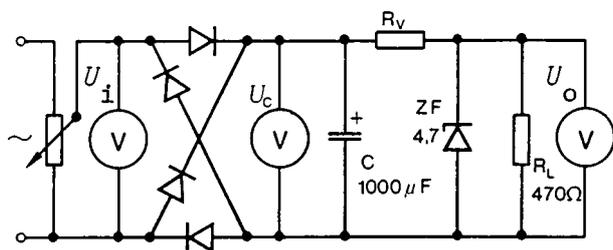


Fig. 5: Voltage multiplier circuit.

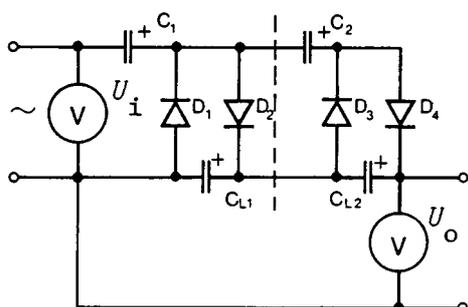
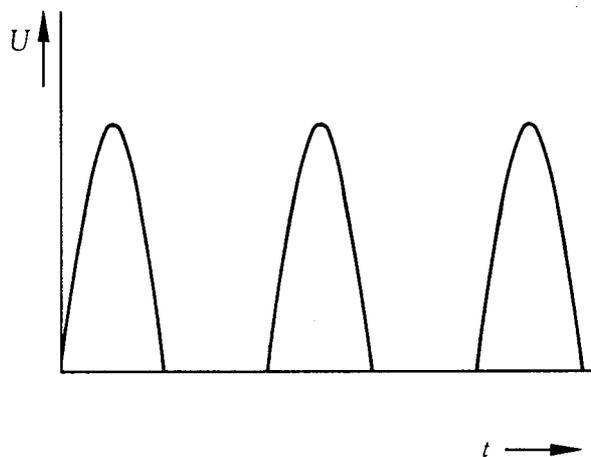


Fig. 6: Output alternating voltage of a half-wave rectifier, $f = 50$ Hz.



Theory and evaluation

1. In a half-wave rectifier (Fig. 2), only the positive half-waves of the input alternating voltage appear at the output, i.e. If there is no charging capacitor we obtain a pulsating direct voltage.

When the charging capacitor C is connected it becomes charged to the peak value of the alternating voltage $U_o = \sqrt{2} U_{i\text{ rms}}$ during the positive half-wave. As soon as the input voltage falls below the output voltage the diode blocks the circuit. The maximum inverse voltage at the diode is

$$U_{\text{inv.max.}} = 2 \sqrt{2} U_{i\text{ rms}}$$

On load, the load resistor discharges the capacitor as long as the rectifier continues to block. The output voltage then consists of a direct voltage superimposed on an alternating voltage (ripple) U_{ACpp} . The ripple amplitude is given by

$$U_{ACpp} \sim \frac{I_o}{C \cdot f'}$$

where C is the charging capacitance and f the frequency of the alternating voltage. The frequency of the ripple f' in the half-wave rectifier is $f' = f = 50$ Hz.

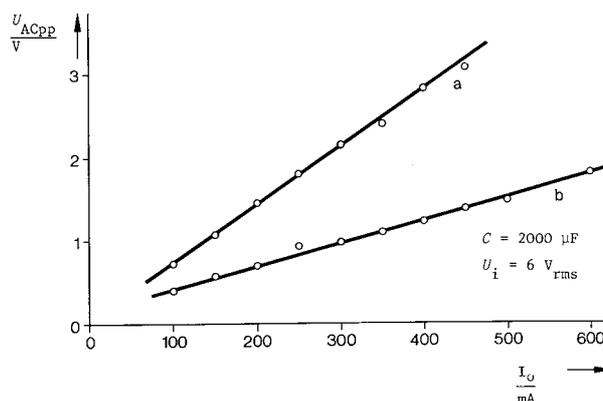
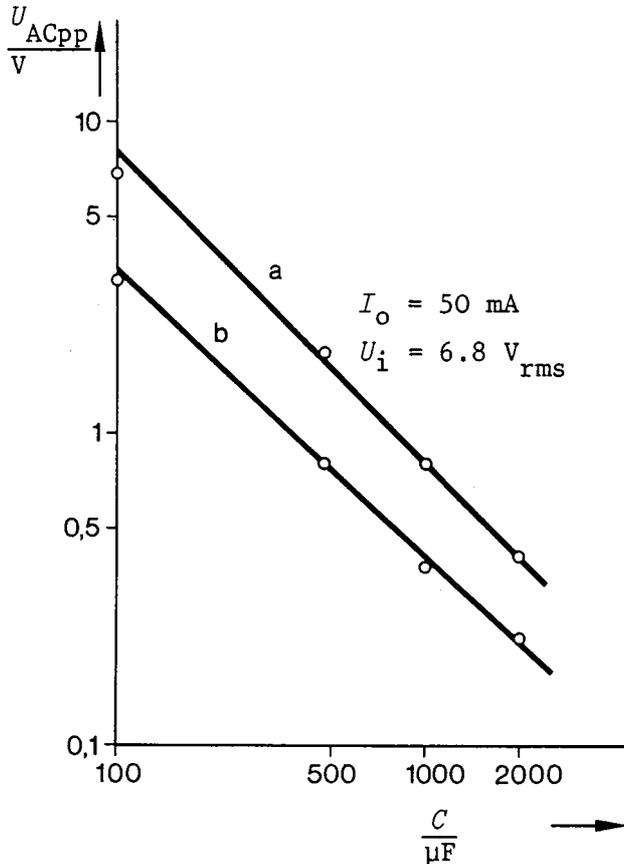


Fig. 7: Ripple of the output voltage as a function of the charging current: a) half-wave rectifier, b) bridge rectifier.

Fig. 8: Ripple as a function of capacitance:
a) half-wave rectifier b) bridge rectifier.



Applying the expression

$$U_{ACpp} = a \cdot C^b$$

to the measured values from Fig. 8, we obtain

$$b_1 = -0.89 \pm 0.02 \text{ (bridge rectifier)}$$

and

$$b_2 = -0.94 \pm 0.04 \text{ (half-wave rectifier)}$$

This shows, within the limits of error, that ripple is inversely proportional to capacitance. (Note; the capacitance values given for electrolytic capacitors are subject to a tolerance of -10% to +50%)

For the half-wave rectifier on no-load the output voltage is

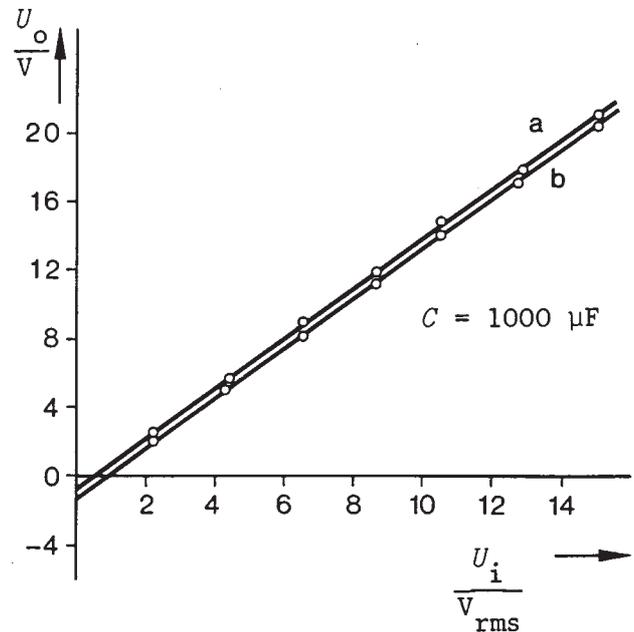
$$U_{0o} = \sqrt{2} U_{i,rms} - U_D,$$

where U_D is the forward voltage of the diode (between 0.5 and 0.8 V in the case of silicon diodes).

From the regression lines and measured values in Fig. 9 we obtain the following, using the expression:

$$U_{0o} = a + b U_{i,rms}.$$

Fig. 9: Output voltage (no-load) as a function of the input voltage: a) half-wave rectifier, b) bridge rectifier.



Half-wave rectifier:

$$a = -0.6 \text{ V} \rightarrow U_D = 0.6 \pm 0.1 \text{ V}$$

$$s_a = 0.1 \text{ V}$$

$$b = 1.45$$

$$s_b = 0.01$$

Bridge rectifier:

$$a = -1.1 \text{ V} \rightarrow U_D = 0.55 \pm 0.05 \text{ V}$$

$$s_a = 0.1 \text{ V}$$

$$b = 1.45$$

$$s_b = 0.01$$

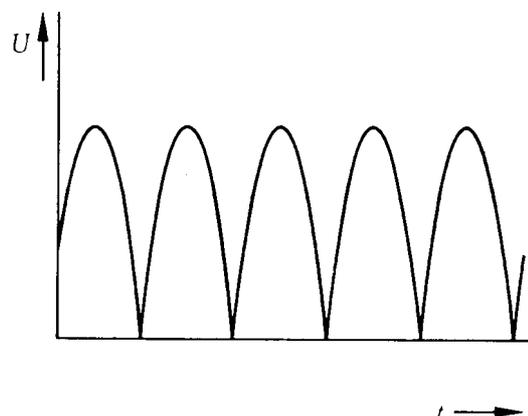


Fig. 10: Output voltage of the bridge rectifier (without charging capacitor), frequency 100 Hz.

Fig. 11: Diode current as a function of the output current;
a) half-wave rectifier, b) bridge rectifier.

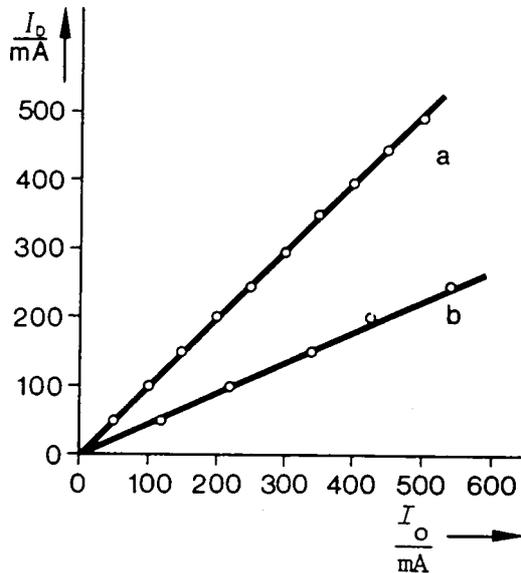
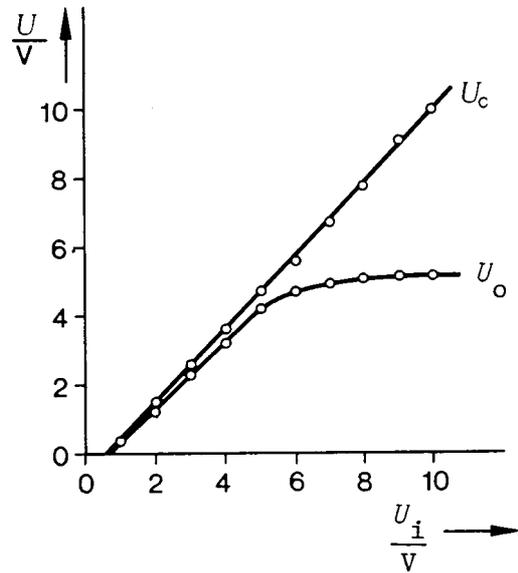


Fig. 12: Capacitor voltage U_c and output voltage U_o of the circuit in Fig. 4.



2. In full-wave rectification in the bridge circuit (Graetz rectifier), the diodes connect the negative pole of the voltage source to earth and the positive pole to the positive pole of the capacitor. Every half-wave is thus utilised.

The maximum voltage at a diode is then

$$U_{\text{inv. max.}} = \sqrt{2} U_{i \text{ rms.}}$$

Contrary to the action of the half-wave rectifier, the r.m.s. current flowing through a diode in a fullwave rectifier is half the output current as only one pair of diodes is switched in at any time during a half-cycle. Fig. 11 confirms this relationship: $I_D = g(I_o)$ gives a straight line of gradient 1 in the case of a half-wave rectifier, but of gradient 0.5 in the case of a bridge rectifier.

The frequency of the ripple is $f' = 2f$ in the bridge rectifier. The ripple is thus represented by

$$U_{\text{ACpp}} \sim \frac{I_o}{2Cf}$$

The measured values in Fig. 7 and 8 confirm this.

As two series-connected diodes are driven in the conducting direction in each half-wave, the output voltage

$$U_o = \sqrt{2} U_{i \text{ rms.}} - 2 U_D \text{ (cf Fig. 9).}$$

3. When Zener voltage is reached the inverse current of a Zener diode rises steeply. This characteristic is used to stabilize the voltage:

If U_c (Fig. 4) exceeds the Zener voltage of 4.7 V, the current increases to such an extent that the voltage at the diode remains almost constant because of the voltage drop over R_V .

The stabilization factor (smoothing factor) can be obtained from the gradient of the U_Q -curve (Fig. 12)

For a point above approx. 6 V we obtain

$$G = \frac{\Delta U_c}{\Delta U_o} = 13$$

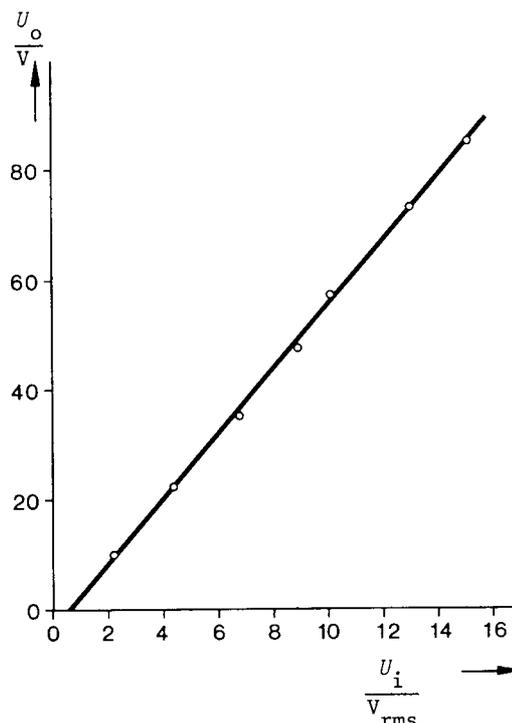


Fig. 13: Ripple of the output voltage as a function of the charging current: a) half-wave rectifier, b) bridge rectifier.

4. Voltage multipliers are used to generate high voltages at low current consumption. The Greinacher rectifier (cascade connection) shown in Fig. 5 consists of $n = 2$ cascades. Each stage generates a direct voltage $U = 2\sqrt{2} U_{i\text{ rms}}$ across C_L : n times the voltage thus reaches the capacitors arranged in series C_L .

For the unloaded voltage, U_{00} :

$$U_{00} = 2n \sqrt{2} U_{i\text{ rms}} - 2n U_D$$

Applying the regression expression $U_{00} = a + b U_{i\text{ rms}}$ to the measured values in Fig. 13 we obtain:

$$a = -3.8\text{ V} \rightarrow U_D = 0.9 \pm 0.2\text{ V}$$

$$s_a = 0.9\text{ V}$$

$$b = 5.9 \rightarrow n = 2.09 \pm 0.04$$

$$s_b = 0.1$$

Bibliography

U. Tietze and Ch. Schenk, Halbleiter-Schaltungstechnik (semiconductor techniques), Springer-Verlag, Berlin, Heidelberg and New York 1974.