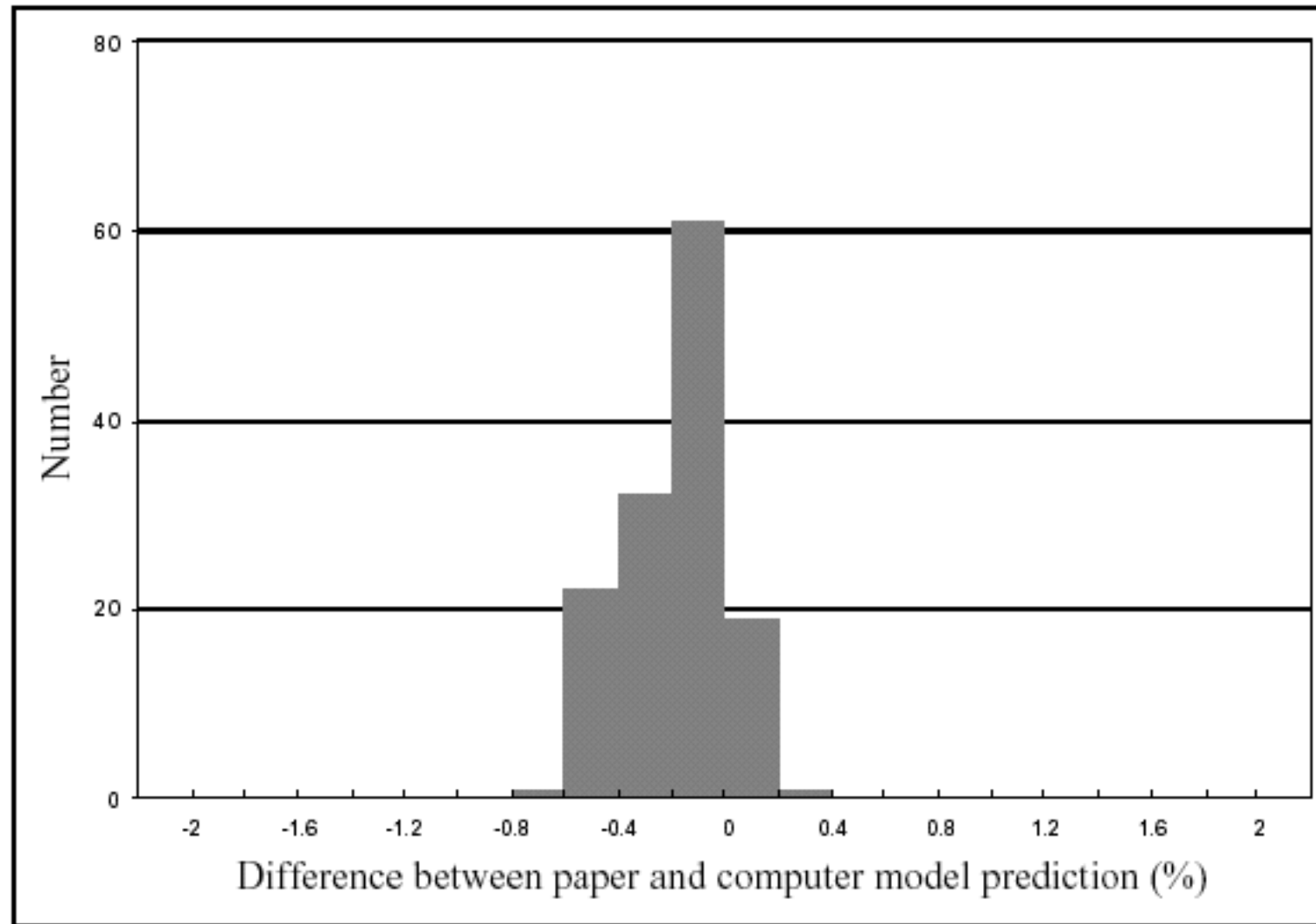
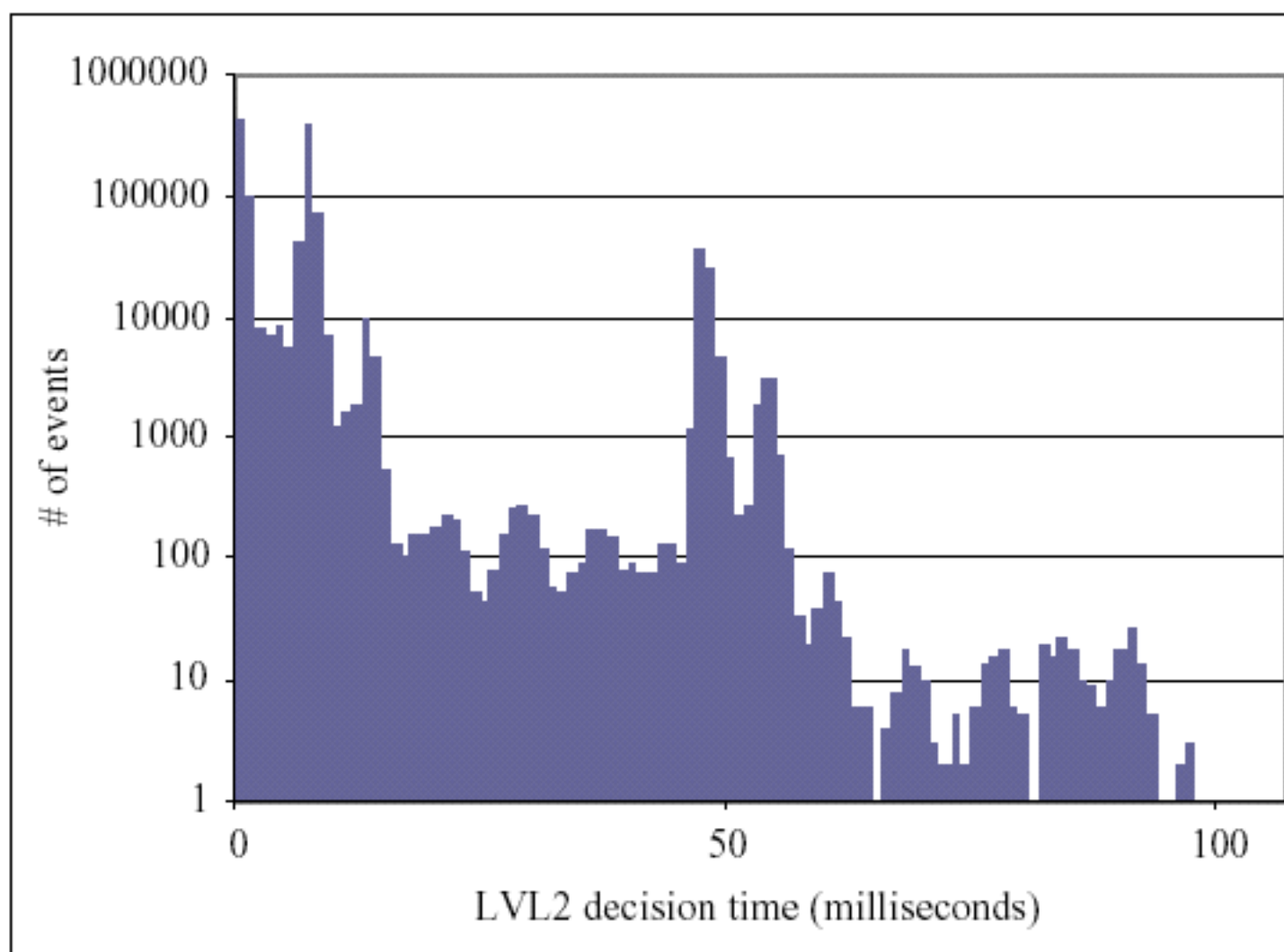


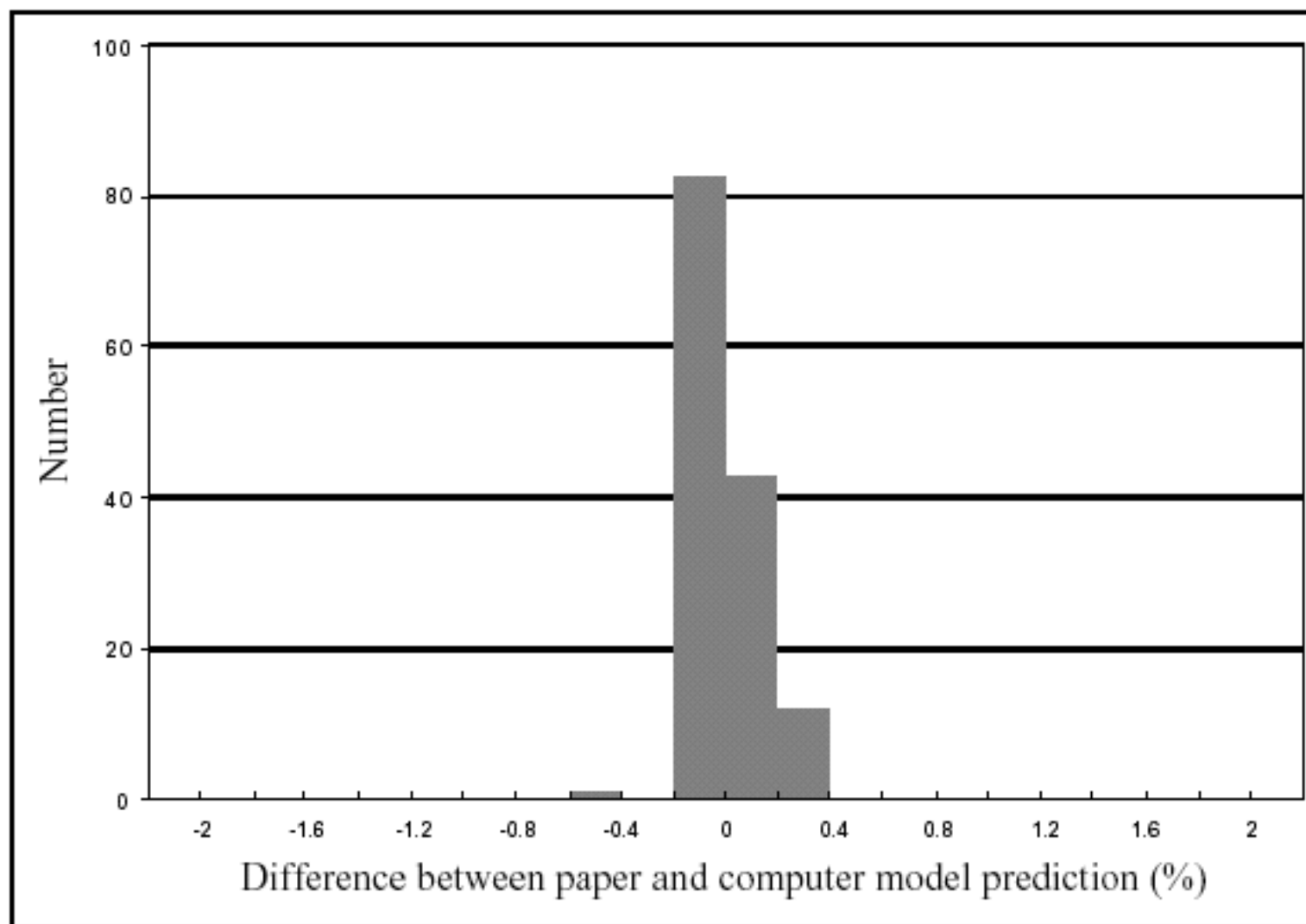
*Figure 1. Low luminosity, sequential processing, 67.1 % LVL2 processor utilization, 512 LVL2 processors (512 processors)*



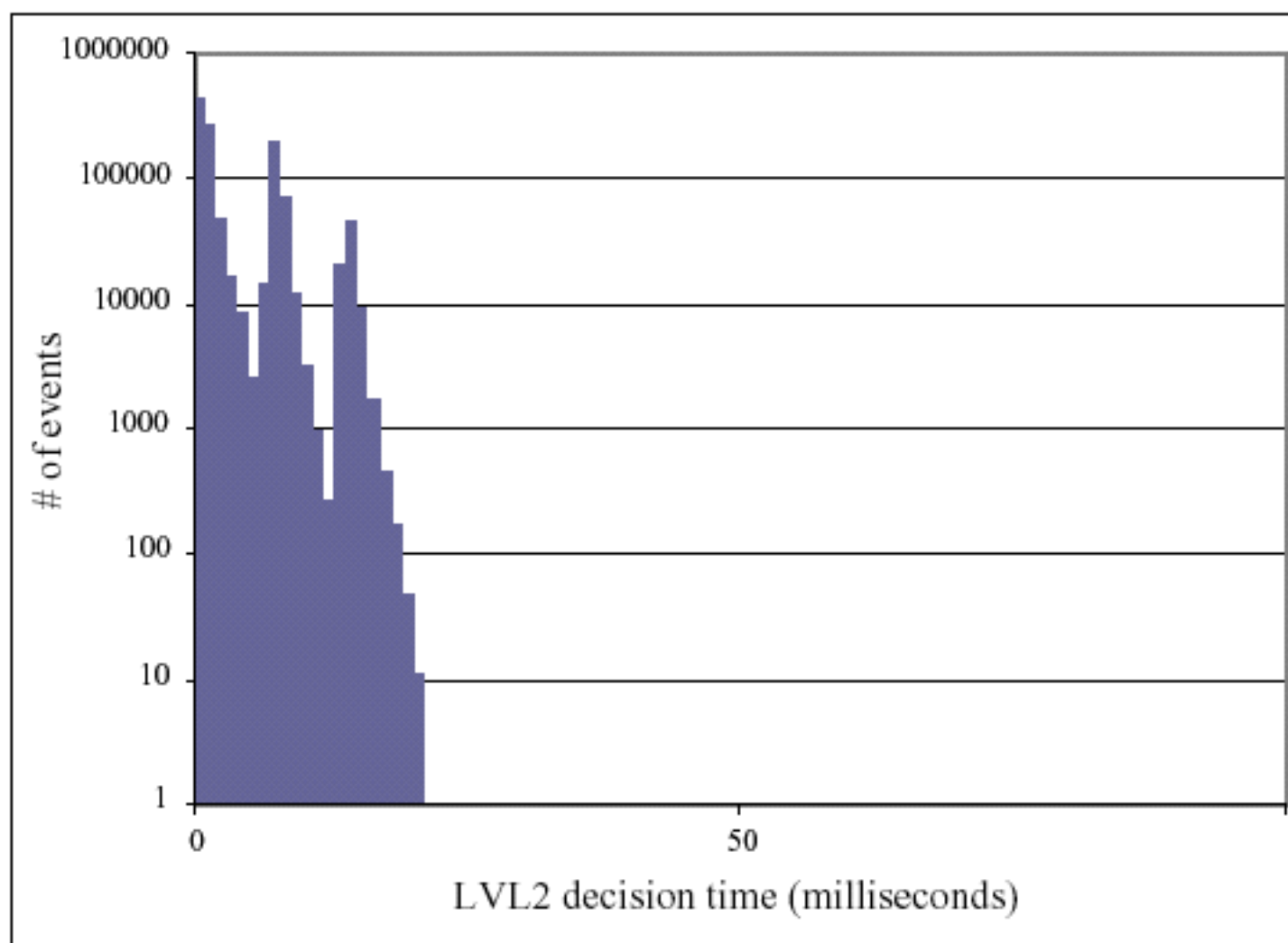
*Figure 16. Low luminosity, nominal LVL1 rate, fully sequential processing, difference between paper and computer model results.*



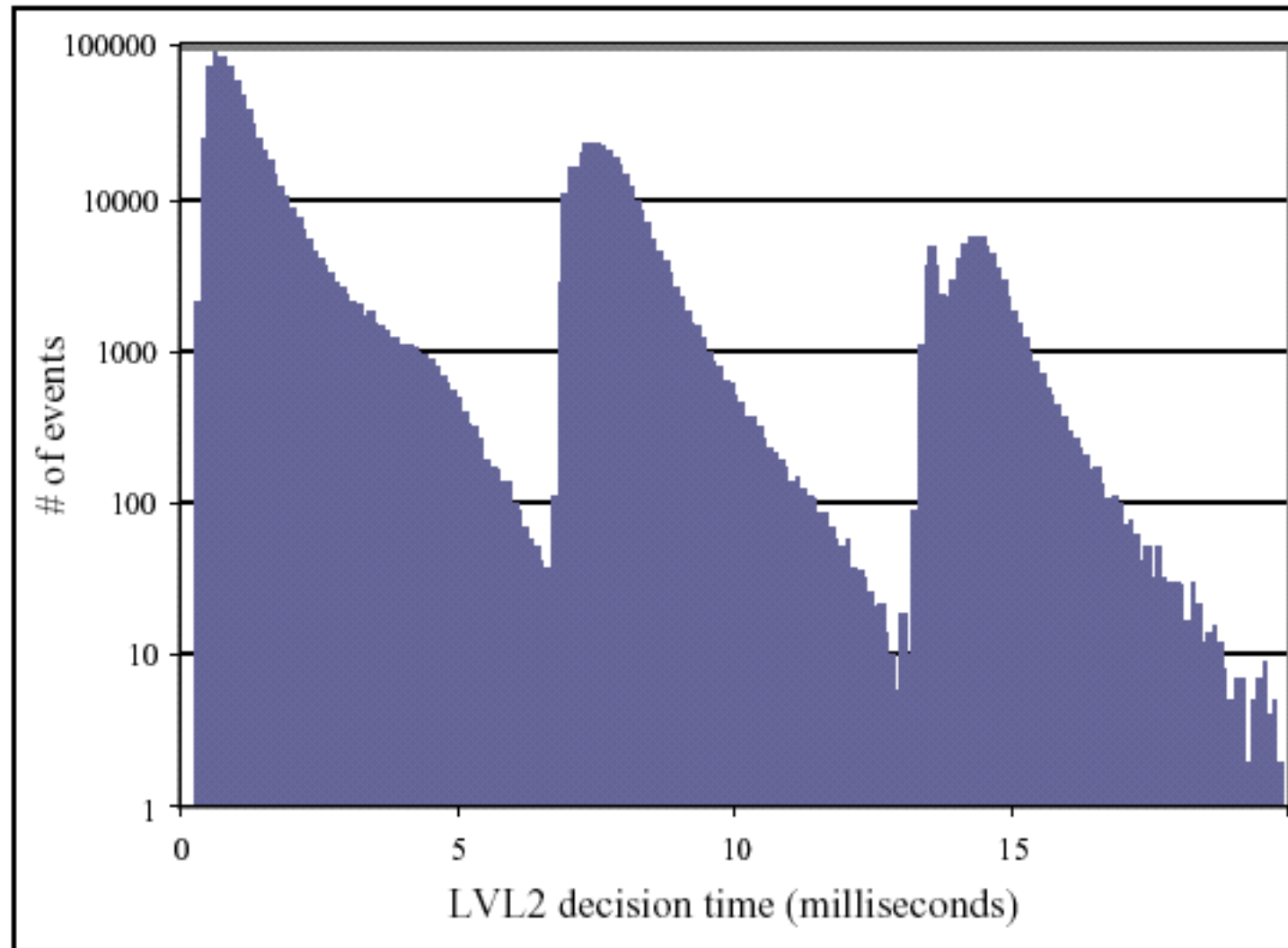
*Figure 3. Intermediate luminosity, sequential processing, 79.3 % LVL2 processor utilization (640 processors)*



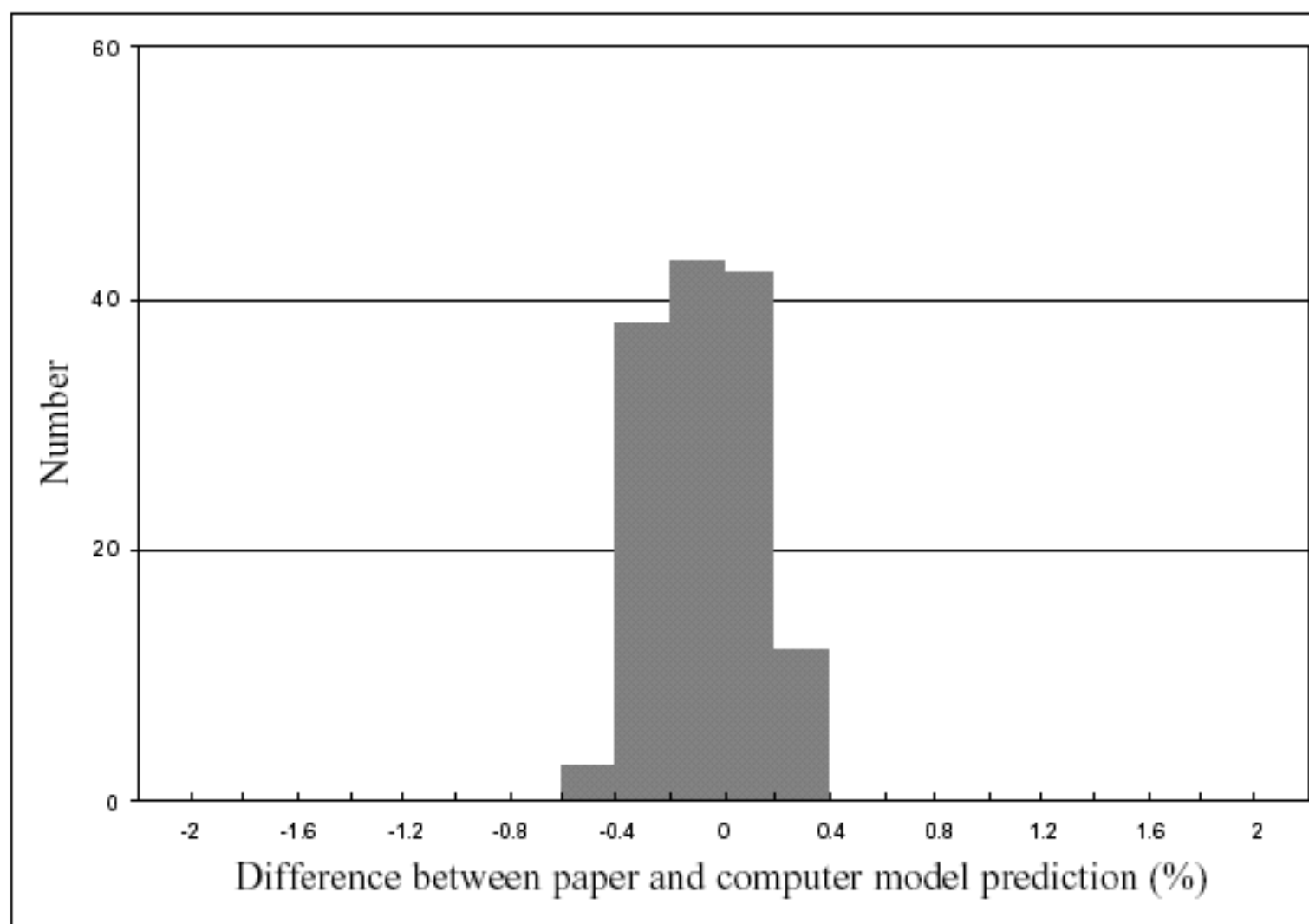
*Figure 18. Intermediate luminosity, difference between paper and computer model results.*



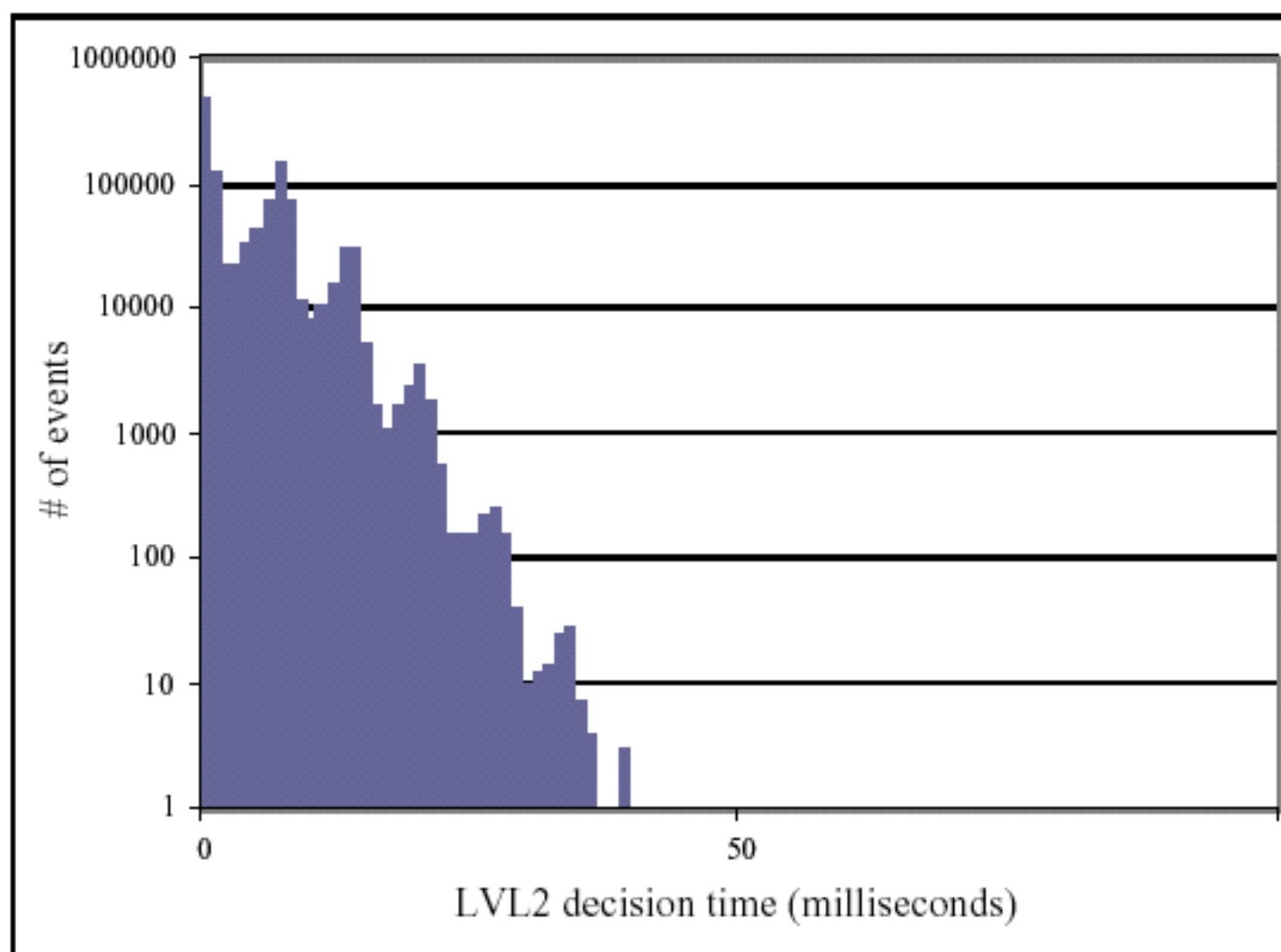
*Figure 5. High luminosity, nominal LVL1 rate, sequential processing, 46.2 % LVL2 processor utilization (256 processors).*



*Figure 6. High luminosity, nominal LVL1 rate, sequential processing, 46.2 % LVL2 processor utilization (enlarged left part of Figure 5).*

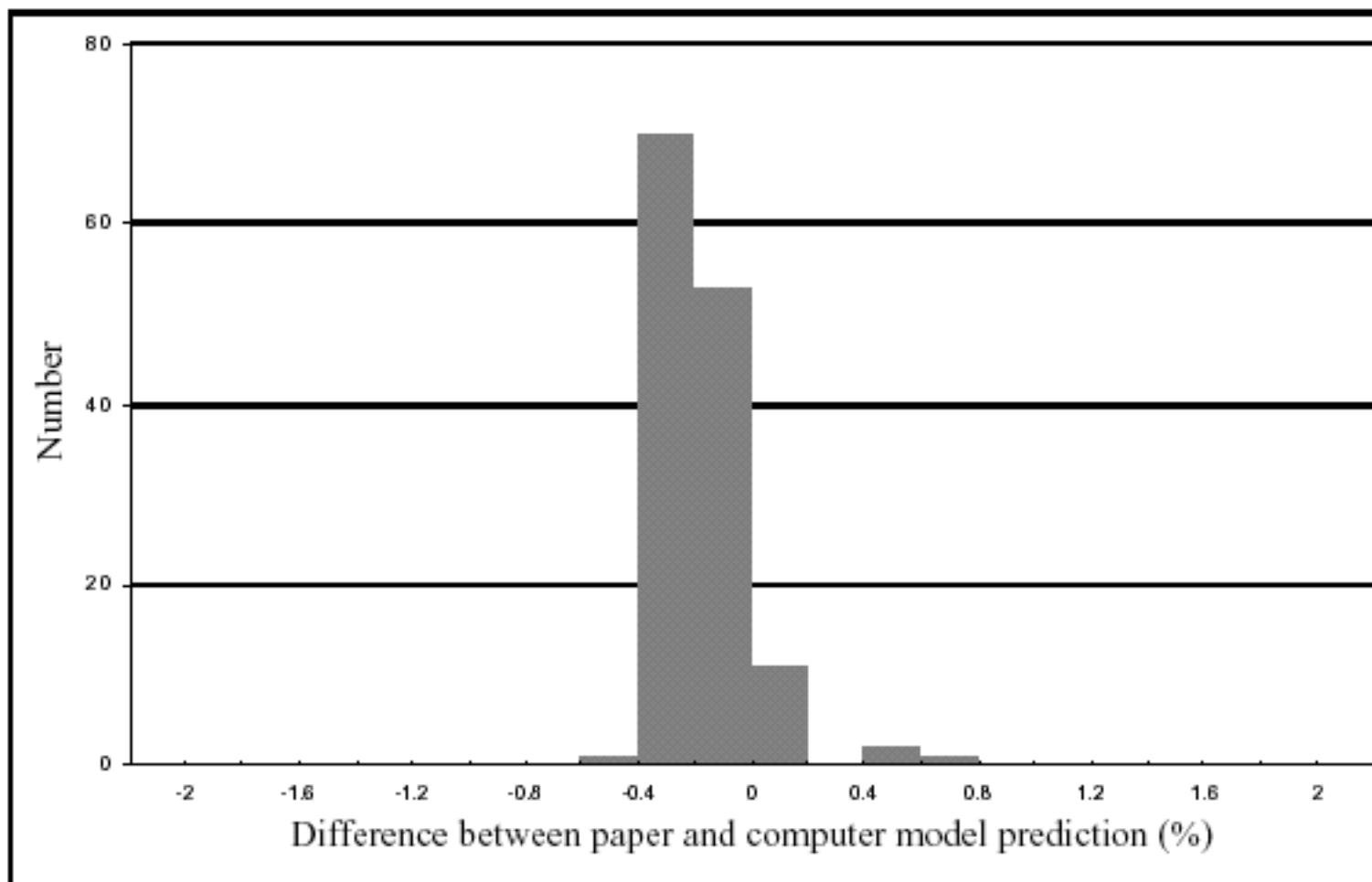


*Figure 13. High luminosity, 75 kHz LVL1 rate, difference between paper and computer model results.*

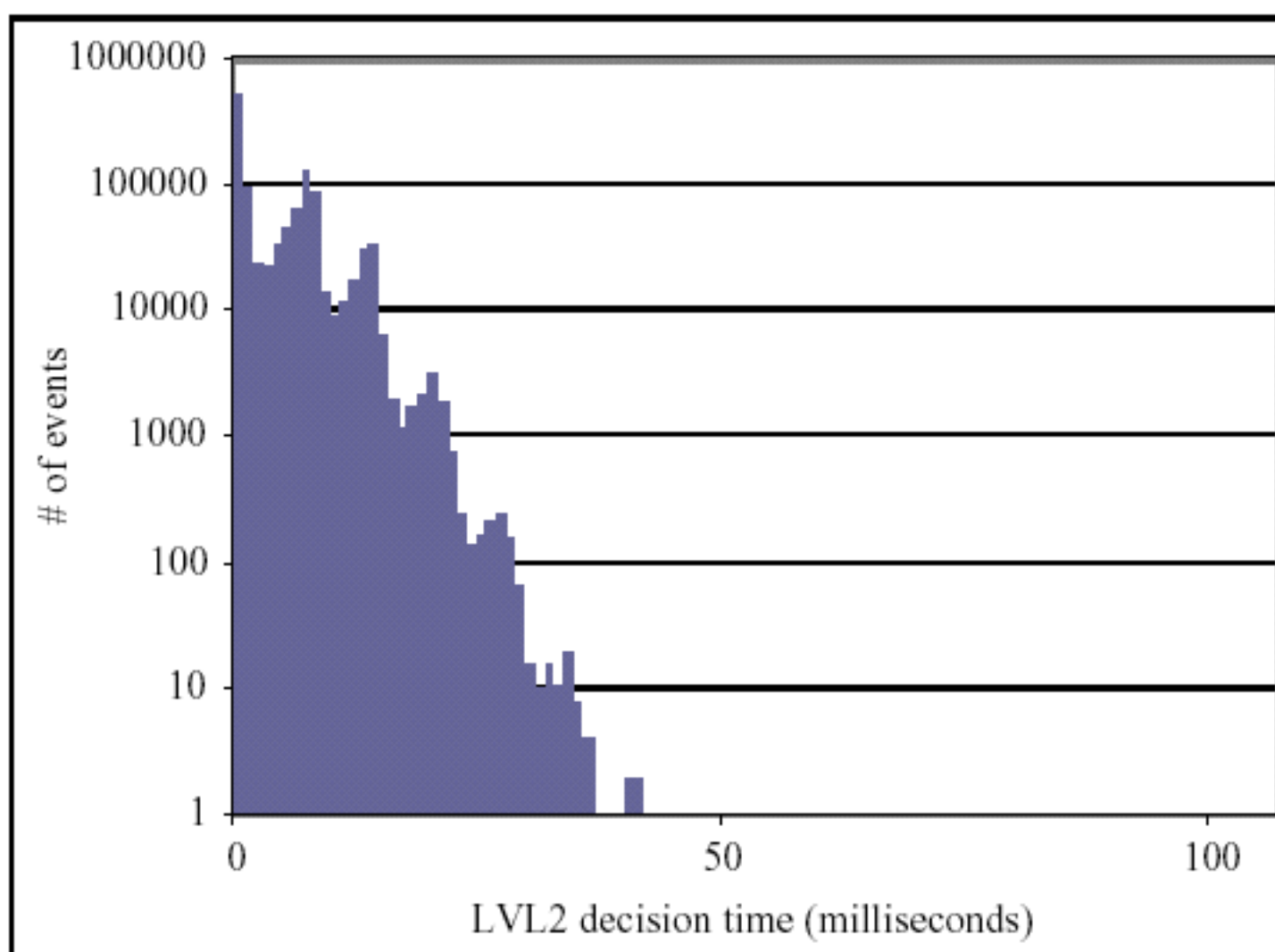


*Figure 8. High luminosity, nominal LVL1 rate, fully sequential processing, 85.4 % LVL2 processor utilization (104 processors)*

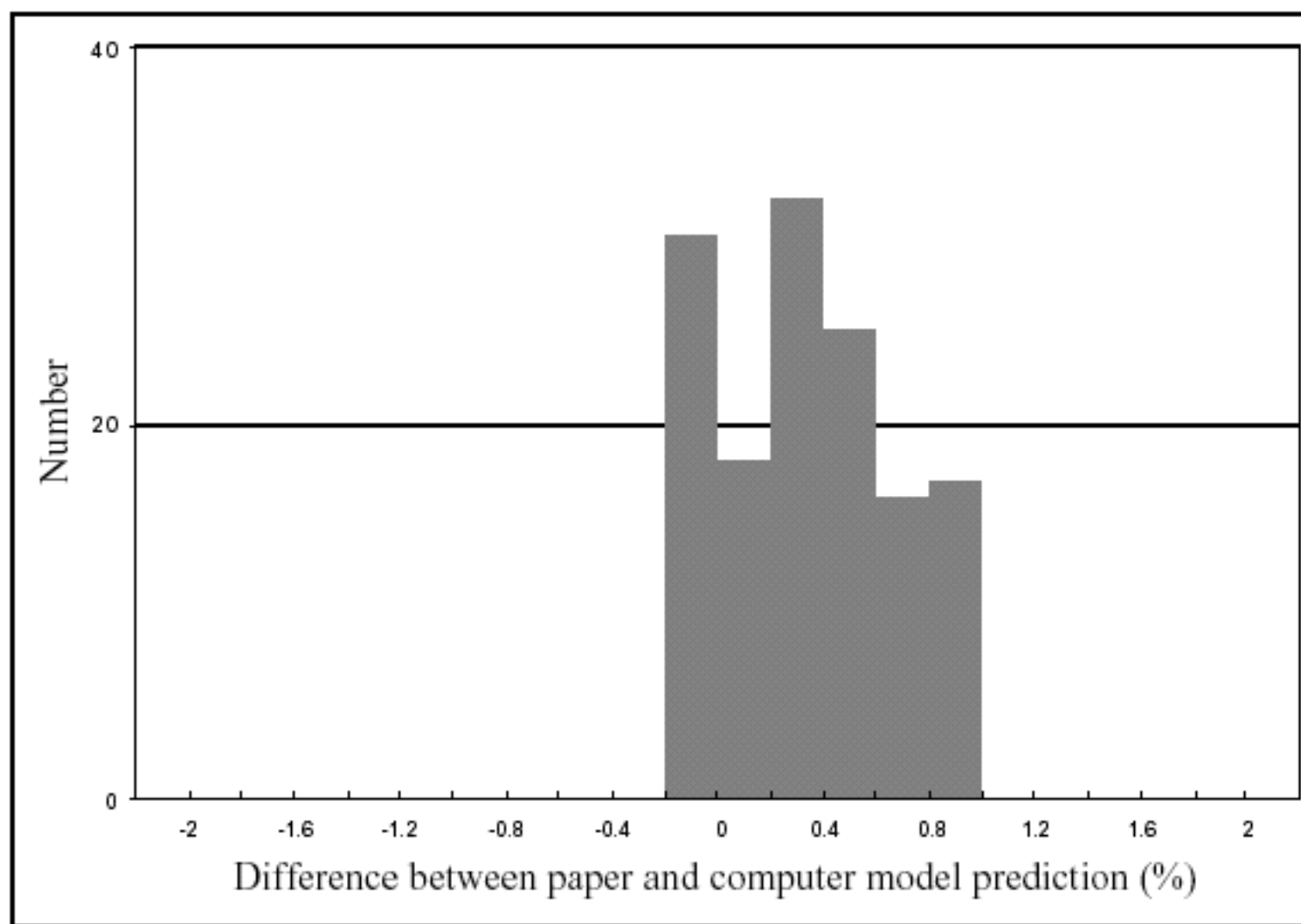




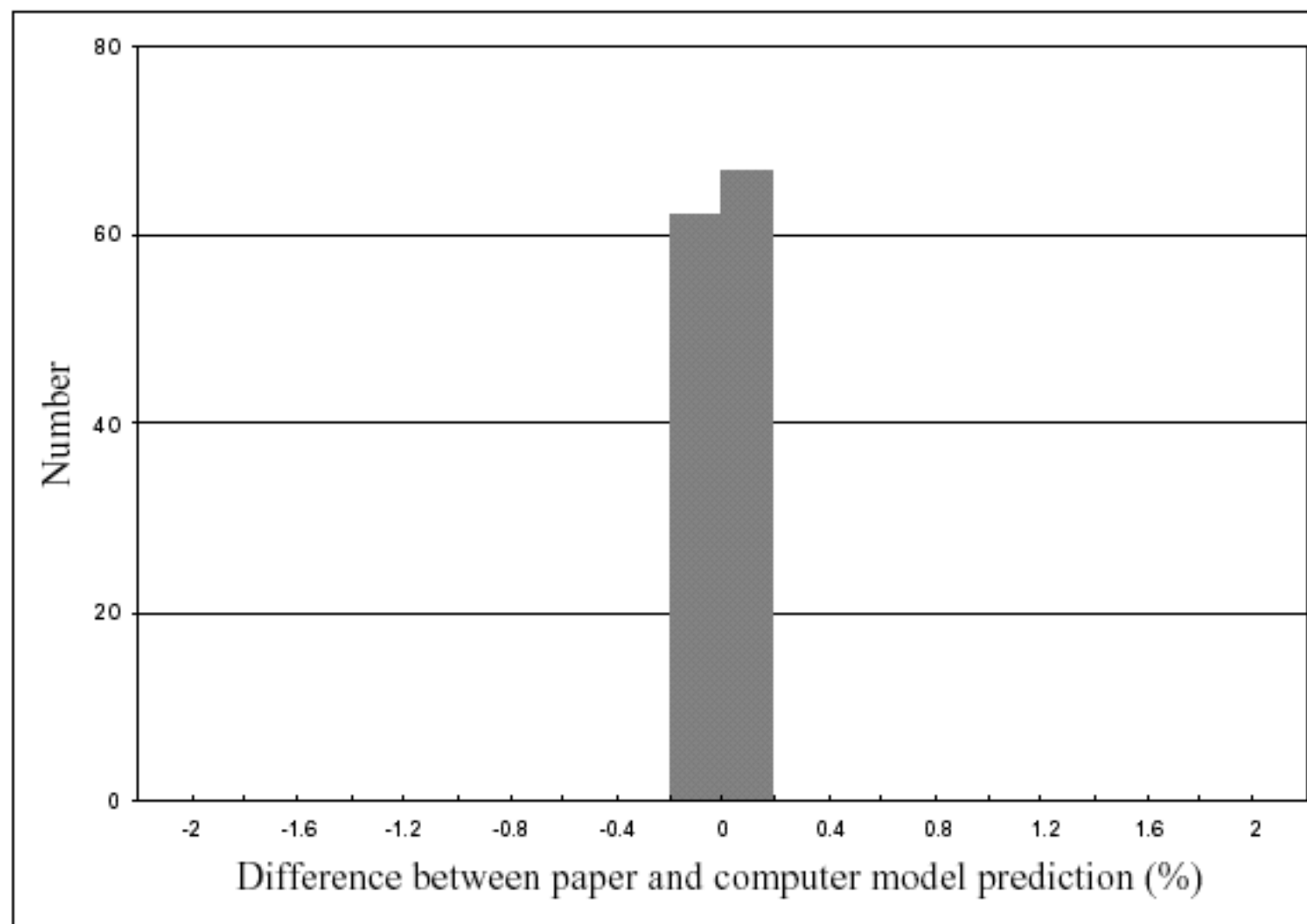
*Figure 12. High luminosity, nominal LVL1 rate, fully sequential processing, difference between paper and computer model results.*



*Figure 10. High luminosity, 75 kHz LVL1 rate, fully sequential processing, 85.6% LVL2 processor utilization (200 processors)*



*Figure 14. High luminosity, 75 kHz LVL1 rate, fully sequential processing, difference between paper and computer model results.*



*Figure 15. High luminosity, 75 kHz LVL1 rate, fully sequential processing, difference between paper and computer model results, but with event building rate in paper model set equal to that found with the computer model.*