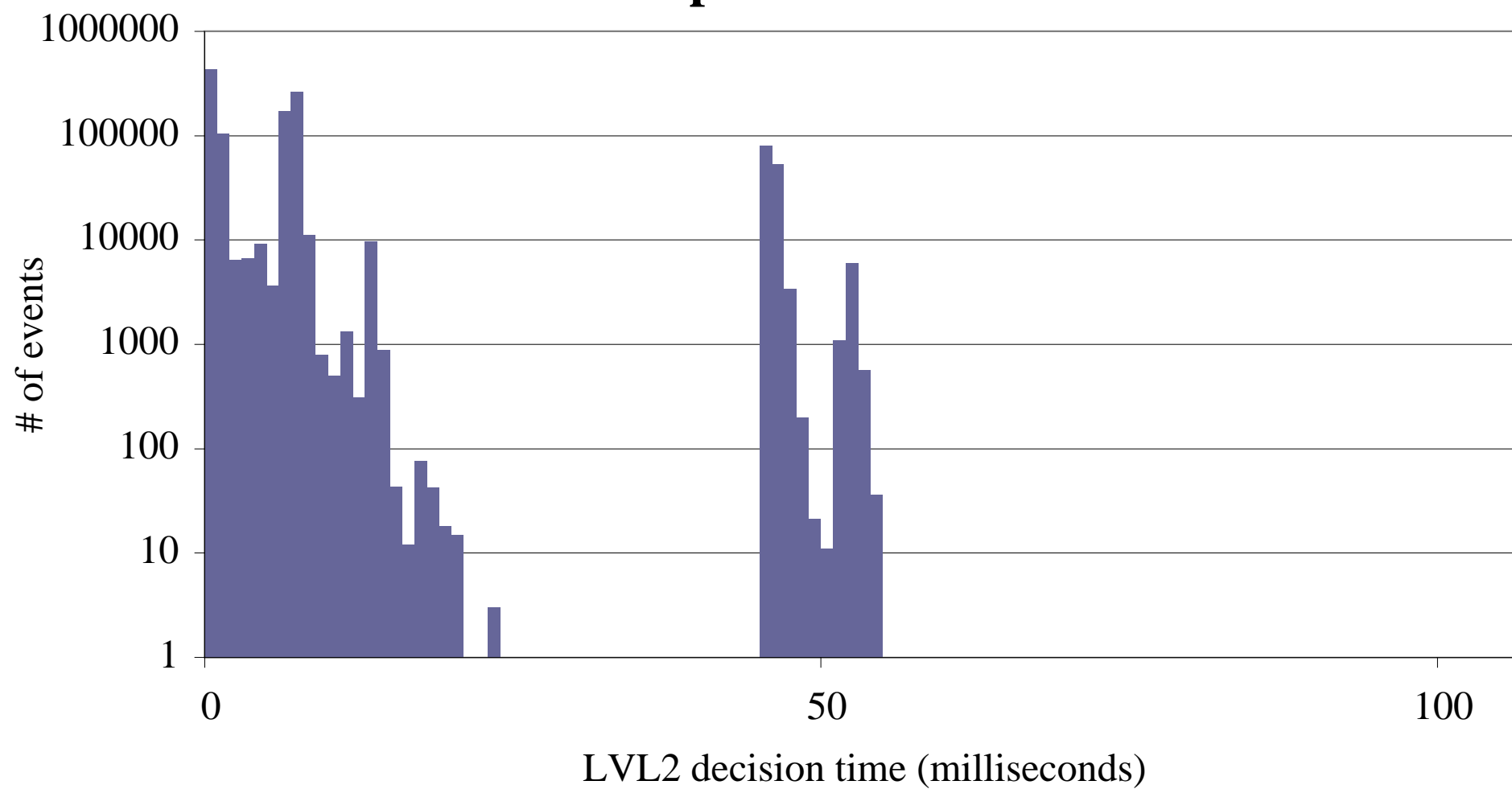
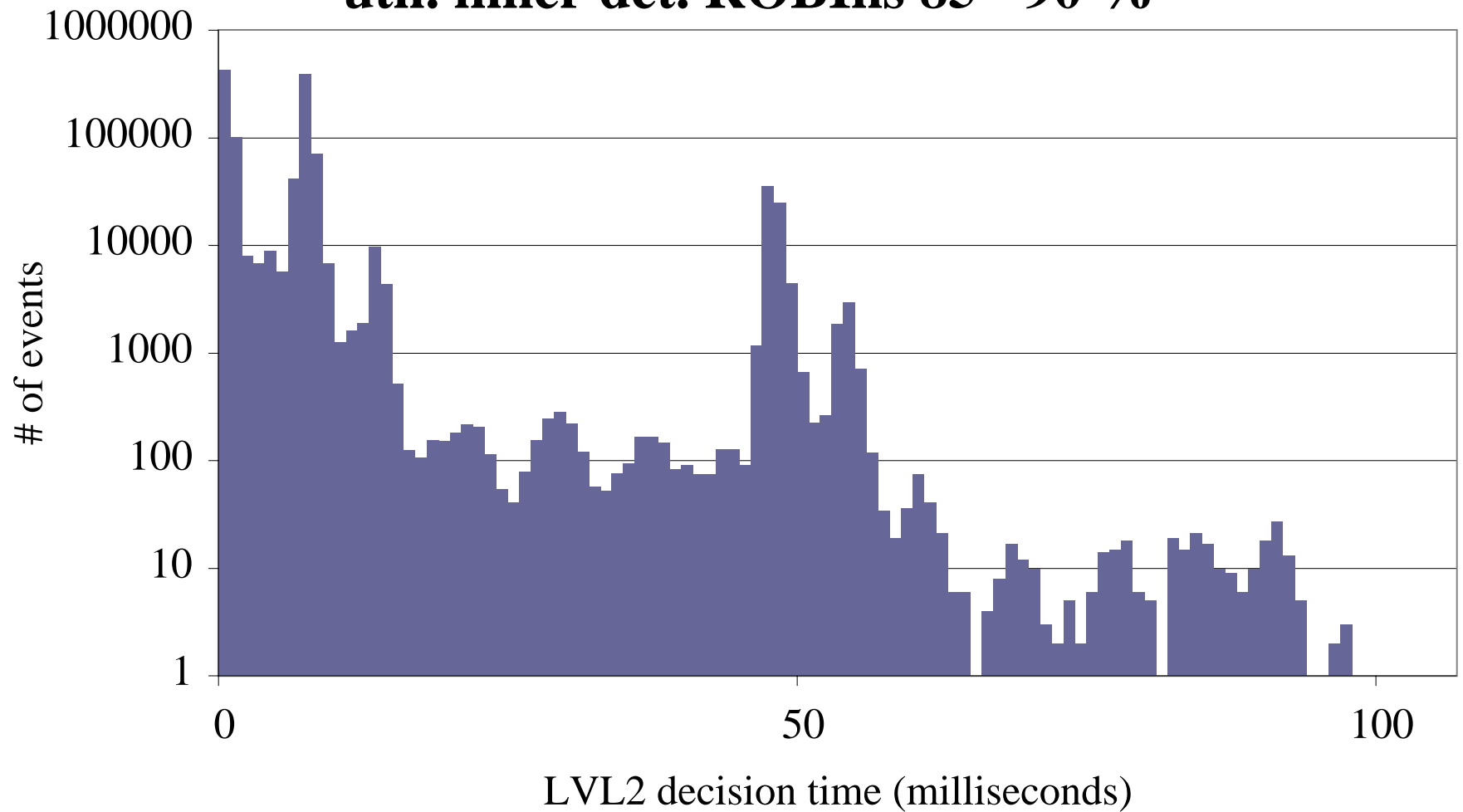


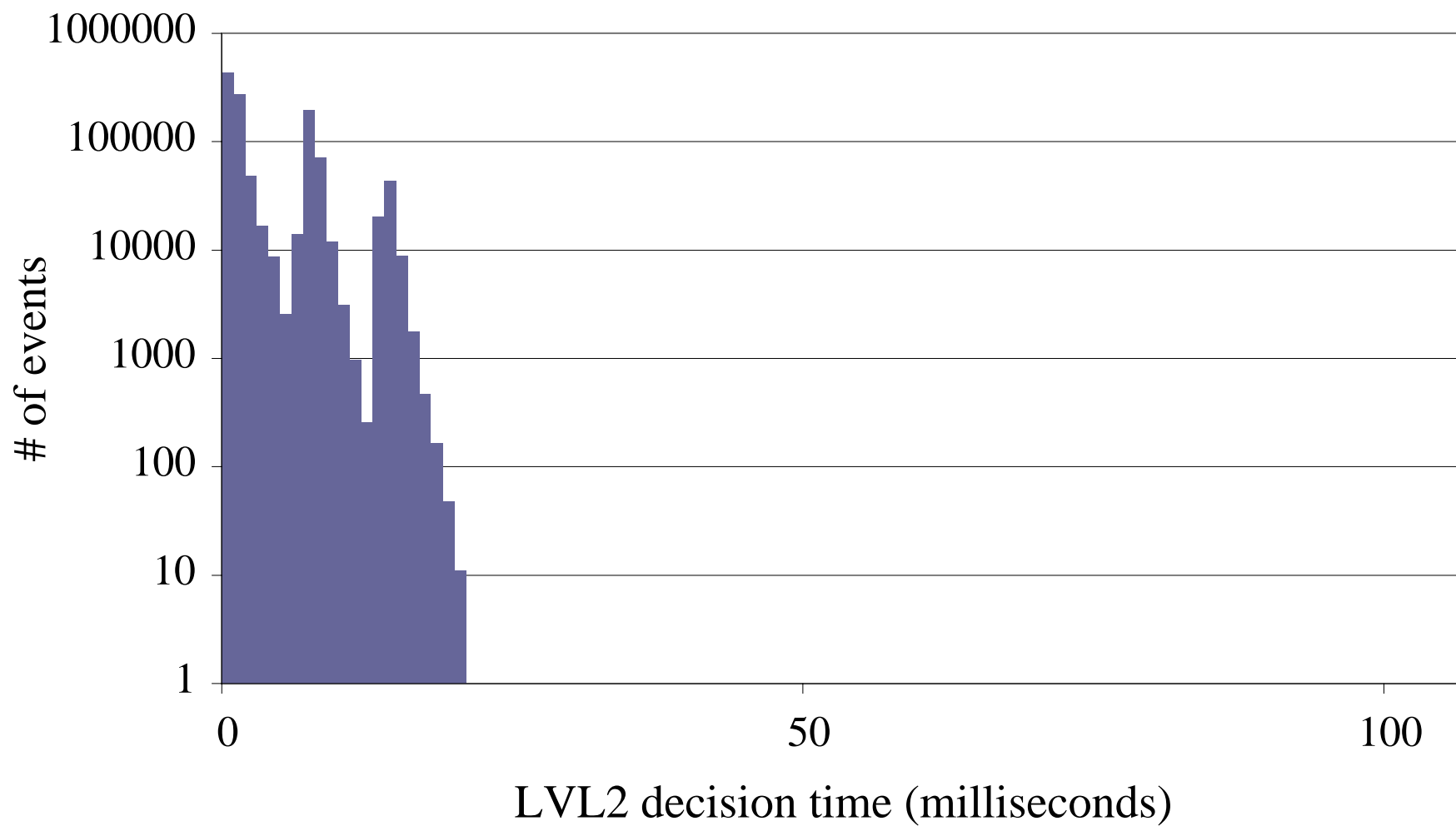
**Low L, 40 kHz, proc. util. 63.7 %,
512 processors**



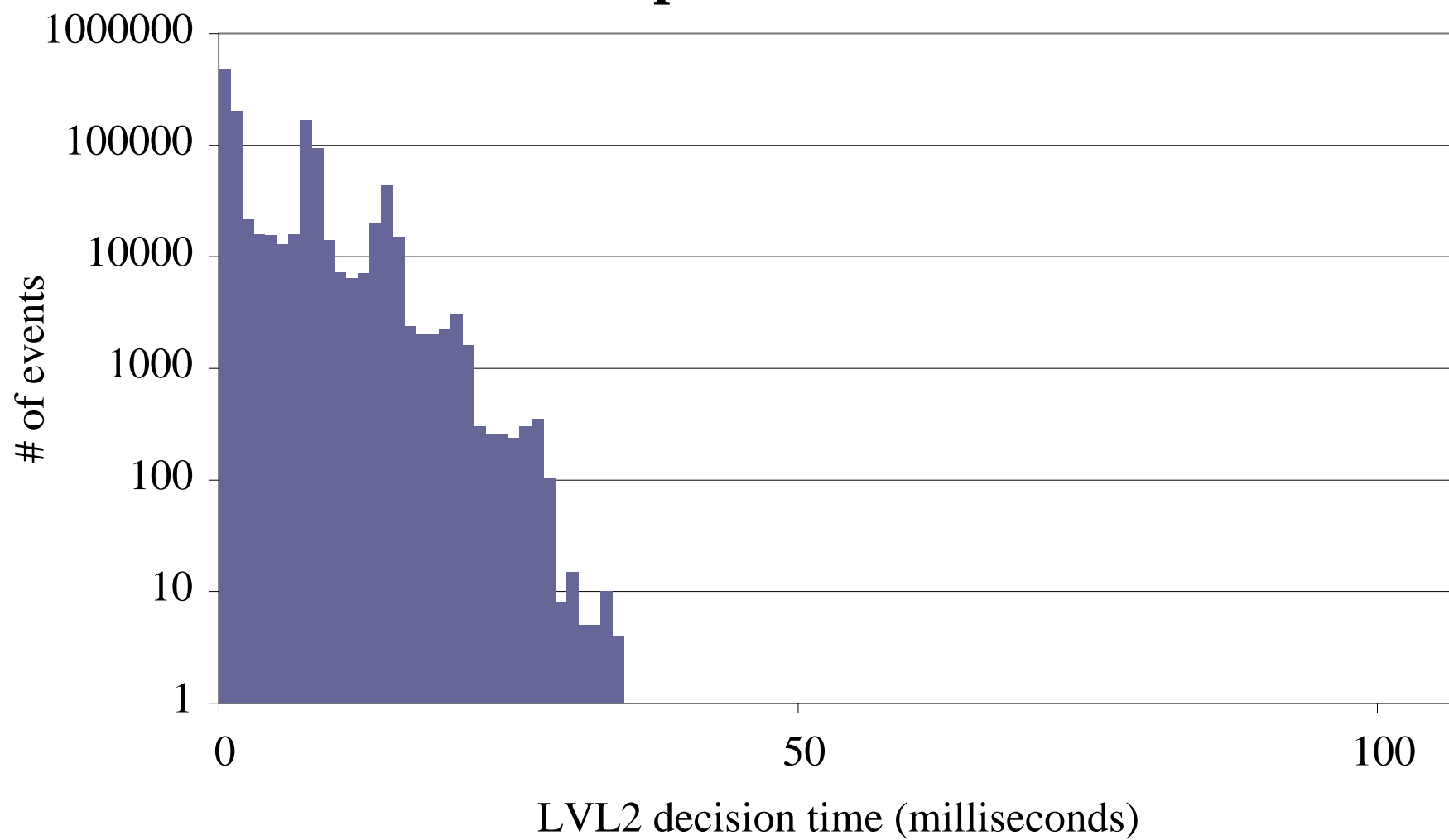
**Interm. L, proc. util. 74.1 %,
640 processors,
util. inner det. ROBINs 85 - 90 %**



**High L, 40 kHz, proc. util. = 41.2 %,
256 processors**



**High L, 75 kHz, proc. util. = 81.6 %,
256 processors**



**High L, 75 kHz, fully sequential,
proc. util. = 85.6 %, 200 processors**

