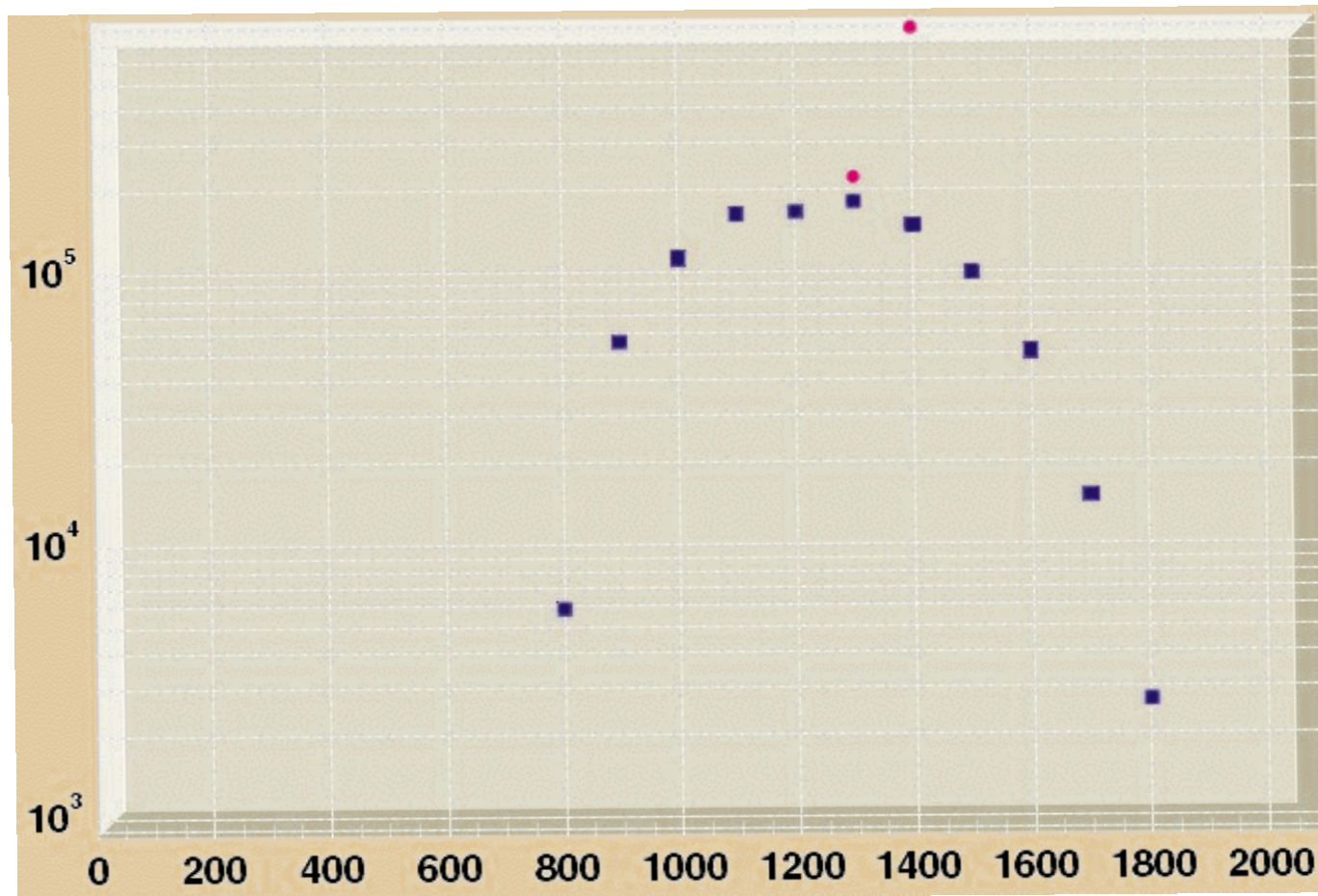


# Update on ATM Test Setup Model

- ☐ Reminder of what measurements were "modeled"
- ☐ Updated plots with slightly better correspondence to measurements

# Plots from June



1 Supervisor / 8 destinations / 100 microsec algorithm time

# Simdaq change

- ❑ Layout of SRB now more correct
  - ❑ Rol processors field all transactions (no central decision receiver / previous way is still there if desired)

# Conclusions

- ☐ Need to work a bit more
  - ☐ finish off with new simdaq and do complete set of measurements
  - ☐ pilot project studies
- ☐ Look for source of additional 10% time spread
  - ☐ Alternatively might ask what if any consequences similar timespread has on final system