“MVME 2600/2700 Series
Single Board Computer
Programmers Reference Guide
V26000A PG4”

↓

Table 1.5 PREP Memory
Map Example:

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Zero-Based PCI/ISA
Memory Space:
C0000000 – FCFFFFFF → PCI Memory Space
(Universe):
00000000 – 3CFFFFFF → VMEbus Via PCI
Slave Image 0..3
Default:

			Maps Back to CPU Address:
Not Used:	00000000 – 0FFFFFFF		C0000000 – CFFFFFFF
Image 3:	10000000 – 1FFFFFFF		D0000000 – DFFFFFFF
Image 1:	20000000 – 20FFFFFF	→	E0000000 – E0FFFFFF
Image 2:	21000000 – 217FFFFF		E1000000 – E17FFFFF
Not Used:	21800000 – 2FFEFFFF		E1800000 – EFFEFFFF
Image 0:	2FFF0000 – 2FFFFFFF		EFFF0000 – EFFFFFFF