

MROD-In Serial Number	FPGA version (“FPGA.pof, FPGA_1.pof”)	Remark
1004770001	24-05-2004 17:57 (Checksum 0x02BA877C 0x0042B705)	PJ 02-08-2004
1004770002	21-11-2003 16:52 (Checksum 0x02B9CFF9 0x00471496)	
1004770003	24-05-2004 17:57 (Checksum 0x02BA877C 0x0042B705)	Done by Thei H8
1004770004	24-05-2004 17:57 (Checksum 0x02BA877C 0x0042B705)	Done by Thei H8
1004770005	24-05-2004 17:57 (Checksum 0x02BA877C 0x0042B705)	Done by Thei H8
1004770006	24-05-2004 17:57 (Checksum 0x02BA877C 0x0042B705)	Done by Thei H8
1004770007	24-05-2004 17:57 (Checksum 0x02BA877C 0x0042B705)	Done by Thei H8
1004770008	24-05-2004 17:57 (Checksum 0x02BA877C 0x0042B705)	Done by Thei H8
1004770009	24-05-2004 17:57 (Checksum 0x02BA877C 0x0042B705)	PJ 02-08-2004
1004770010	21-11-2003 16:52 (Checksum 0x02B9CFF9 0x00471496)	
2-01	24-05-2004 17:57 (Checksum 0x02BA877C 0x0042B705)	Done by GK/JS
2-02	24-05-2004 17:57 (Checksum 0x02BA877C 0x0042B705)	PJ 02-08-2004
2-03	24-05-2004 17:57 (Checksum 0x02BA877C 0x0042B705)	PJ 02-08-2004
2-04	24-05-2004 17:57 (Checksum 0x02BA877C 0x0042B705)	PJ 02-08-2004
2-05	24-05-2004 17:57 (Checksum 0x02BA877C 0x0042B705)	PJ 02-08-2004
2-06	24-05-2004 17:57 (Checksum 0x02BA877C 0x0042B705)	PJ 02-08-2004
2-07	24-05-2004 17:57 (Checksum 0x02BA877C 0x0042B705)	PJ 02-08-2004
2-08	24-05-2004 17:57 (Checksum 0x02BA877C 0x0042B705)	
2-09	24-05-2004 17:57 (Checksum 0x02BA877C 0x0042B705)	PJ 02-08-2004
2-10	24-05-2004 17:57 (Checksum 0x02BA877C 0x0042B705)	PJ 02-08-2004

Remarks:

25-06-2003 16:42 LDOWN_n Synchronization bug Fixed

SerNo 1004770002 Booting not reliable (15-08-2003)

21-11-2003 16:52 3 Leading NULL words

SerNo 1004770010 Booting not reliable (11-02-2004)

24-05-2004 17:57 Simplified output state machine

MROD-Out Serial Number	FPGA version ("FPGA.pof")
00	17-02-2004 14:39 (Checksum 0x01624F4A)
01	17-02-2004 14:39 (Checksum 0x01624F4A)
02	17-02-2004 14:39 (Checksum 0x01624F4A)
03	17-02-2004 14:39 (Checksum 0x01624F4A)
04	
05	17-02-2004 14:39 (Checksum 0x01624F4A)

Remarks:

21-06-2002 12:05 Working version (without S-Link FIFO Empty Flag routed to Sharc-B)
09-07-2003 9:55 S-Link FIFO Empty flag also routed to Sharc-B, Extra Synchronisation
FF in S-Link Reset State Machine
18-11-2003 11:25 S-Link FIFO now 1024 deep / S-Link FIFO Reset on URESET_n
17-02-2004 14:39 S-Link FIFO Half-Full Flag plus IRQ on Both Shares.