

## Connections for different test setups with the Hewlett Packard 1662A Logic Analyzer.

Four test setup files are stored on disk.

Testing the MROD-In:

MR_IN	
Connector	Analyzer POD
J5	1
J6	2

Testing the VME part of the MROD-Out:

MR_O_VME	
Connector	Analyzer POD
J101	1
J102	2
J103	3
J104	4

Testing the Sharc Cluster of the MROD-Out:

MR_O_SHA	
Connector	Analyzer POD
J101	1
J102	2
J105	3
J104	4

Testing the S-LINK Output of the MROD-Out:

MR_O_SLK	
Connector	Analyzer POD
J106	1
J102	2
J105	3
J104	4

Testing the TTC Interface of the MROD-Out:

MR_O_TTC	
Connector	Analyzer POD
J106	1
J102	2
J105	3
J104	4