

IC502
ADSP21160N

A1	DATA(14)	A2	DATA(13)	A3	DATA(10)	A4	DATA(8)	A5	DATA(4)	A6	DATA(2)	A7	TRST_n	A8	TRST_n	A9	RESET_n	A10	RPBA	A11	RPBA	A12	IRQ0_n	A13	FLAG1	A14	TIMEP	A15	NC_A14	A16	NC_A15	A17	TPS1b	A18	RPFS1b	A19	RCLK0	A20	DPO	A21	LOGAT(4)
SharC(14)	SharC(13)	SharC(10)	SharC(8)	SharC(4)	SharC(2)	SharC_TDOa	SharC_TRST_n	RstSharCAB_n	RPBA	IRQ0_n	FLAG1	TIMEP	NC_A14	NC_A15	TPS1b	RPFS1b	RCLK0	DPO	LOGAT(4)																						
B1	DATA(22)	B2	DATA(16)	B3	DATA(15)	B4	DATA(9)	B5	DATA(6)	B6	DATA(3)	B7	DATA(0)	B8	TCK	B9	EMC_n	B10	IRQ2b_n	B11	FLAG2	B12	FLAG2	B13	NC_C13	B14	NC_C14	B15	DTL	B16	RCLK2	B17	RPFS2b	B18	RPFS0	B19	TCLK0	B20	LOGAT(5)	B21	LOGAT(2)
SharC(22)	SharC(16)	SharC(15)	SharC(9)	SharC(6)	SharC(3)	SharC(0)	Share_TCK	Share_EMU_n	IRQ2b_n	FLAG2	FLAG2	NC_C13	NC_C14	DTL	RCLK2	RPFS2b	RPFS0	TCLK0	LOGAT(5)	LOGAT(2)																					
C1	DATA(24)	C3	DATA(18)	C4	DATA(17)	C5	DATA(11)	C6	DATA(7)	C7	DATA(5)	C8	DATA(1)	C9	TMS	C10	TDO	C11	IRQ1_n	C12	FLAG2	C13	NC_C12	C14	NC_C13	C15	DTL	C16	DTL	C17	LOGAT(7)	C18	LOGAT(7)	C19	LOGAT(6)	C20	LOGAT(6)				
SharC(24)	SharC(18)	SharC(17)	SharC(11)	SharC(7)	SharC(5)	SharC(1)	Share_TMS	Share_TDOb	IRQ1_n	FLAG2	NC_C12	NC_C13	DTL	LOGAT(7)	LOGAT(7)	LOGAT(6)	LOGAT(6)																								
D1	DATA(28)	D2	DATA(25)	D3	DATA(20)	D4	DATA(19)	D5	DATA(12)	D6	VDDEXT_D6	VDDINT_D7	D8	VDDEXT_D8	D9	VDDEXT_D9	D10	VDDEXT_D10	D11	VDDEXT_D11	D12	VDDEXT_D12	D13	VDDINT_D13	D14	VDDEXT_D14	D15	TPFS0b	D16	L1DAT(7)	D17	LOGAT(7)	D18	LOGAT(6)	D19	LOGAT(3)	D20	LOGAT(1)	D21	L1CLK	
SharC(28)	SharC(25)	SharC(20)	SharC(19)	SharC(12)	VDDEXT	VDDINT	VDDEXT	VDDEXT	VDDEXT	VDDEXT	VDDEXT	VDDINT	VDDEXT	TPFS0b	L1DAT(7)	LOGAT(7)	LOGAT(6)	LOGAT(3)	LOGAT(1)	L1CLK																					
E1	DATA(30)	E2	DATA(29)	E3	DATA(23)	E4	DATA(21)	E5	VDDEXT_E5	E6	VDDINT_E6	E7	VDDINT_E7	E8	VDDINT_E8	E9	VDDINT_E9	E10	VDDINT_E10	E11	GND	E12	VDDINT_E12	E13	VDDINT_E13	E14	VDDINT_E14	E15	VDDINT_E15	E16	VDDEXT_E16	E17	L1DAT(6)	E18	L1DAT(5)	E19	L1ACK	E20	L1DAT(1)		
SharC(30)	SharC(29)	SharC(23)	SharC(21)	VDDEXT	VDDINT	VDDINT	VDDINT	VDDINT	VDDINT	VDDINT	GND	VDDINT	VDDINT	VDDINT	VDDINT	VDDINT	VDDINT	VDDINT	VDDINT	VDDINT	VDDINT	VDDINT	VDDINT	VDDINT	VDDINT	VDDINT	VDDEXT	L1DAT(6)	L1DAT(5)	L1ACK	L1DAT(1)										
F1	DATA(34)	F2	DATA(33)	F3	DATA(27)	F4	DATA(26)	F5	VDDEXT_F5	F6	VDDINT_F6	F7	GND_F7	F8	GND_F8	F9	GND_F9	F10	GND_F10	F11	GND_F11	F12	GND_F12	F13	GND_F13	F14	GND_F14	F15	VDDINT_F15	F16	VDDEXT_F16	F17	L1DAT(4)	F18	L1DAT(3)	F19	L1DAT(0)	F20	L2DAT(7)		
SharC(34)	SharC(33)	SharC(27)	SharC(26)	VDDEXT	VDDINT	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	VDDINT	VDDEXT	L1DAT(4)	L1DAT(3)	L1DAT(0)	L2DAT(7)									
G1	DATA(38)	G2	DATA(35)	G3	DATA(32)	G4	DATA(31)	G5	VDDEXT_G5	G6	VDDINT_G6	G7	GND_G7	G8	GND_G8	G9	GND_G9	G10	GND_G10	G11	GND_G11	G12	GND_G12	G13	GND_G13	G14	GND_G14	G15	VDDINT_G15	G16	VDDEXT_G16	G17	L1DAT(2)	G18	L2DAT(6)	G19	L2DAT(4)	G20	L1CLK		
SharC(38)	SharC(35)	SharC(32)	SharC(31)	VDDEXT	VDDINT	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	VDDINT	VDDEXT	L1DAT(2)	L2DAT(6)	L2DAT(4)	L1CLK									
H1	DATA(40)	H2	DATA(39)	H3	DATA(37)	H4	DATA(36)	H5	VDDEXT_H5	H6	VDDINT_H6	H7	GND_H7	H8	GND_H8	H9	GND_H9	H10	GND_H10	H11	GND_H11	H12	GND_H12	H13	GND_H13	H14	GND_H14	H15	VDDINT_H15	H16	VDDEXT_H16	H17	L2DAT(5)	H18	L2ACK	H19	L2DAT(3)	H20	L2DAT(1)		
SharC(40)	SharC(39)	SharC(37)	SharC(36)	VDDEXT	VDDINT	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	VDDINT	VDDEXT	L2DAT(5)	L2ACK	L2DAT(3)	L2DAT(1)									
J1	DATA(44)	J2	DATA(43)	J3	DATA(42)	J4	DATA(41)	J5	VDDEXT_J5	J6	VDDINT_J6	J7	GND_J7	J8	GND_J8	J9	GND_J9	J10	GND_J10	J11	GND_J11	J12	GND_J12	J13	GND_J13	J14	GND_J14	J15	VDDINT_J15	J16	VDDEXT_J16	J17	L2DAT(2)	J18	L2DAT(0)	J19	HRB_n	J20	HRB_n		
SharC(44)	SharC(43)	SharC(42)	SharC(41)	VDDEXT	VDDINT	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	VDDINT	VDDEXT	L2DAT(2)	L2DAT(0)	HRB_n	HRB_n									
K1	CLK_CFG_0	K2	DATA(46)	K3	DATA(45)	K4	DATA(47)	K5	VDDEXT_K5	K6	VDDINT_K6	K7	GND_K7	K8	GND_K8	K9	GND_K9	K10	GND_K10	K11	GND_K11	K12	GND_K12	K13	GND_K13	K14	GND_K14	K15	VDDINT_K15	K16	VDDEXT_K16	K17	BR4_n	K18	BR4_n	K19	BR4_n	K20	BR4_n		
SharC(46)	SharC(45)	SharC(47)	VDDEXT	VDDINT	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	VDDINT	VDDEXT	BR4_n	BR4_n	BR4_n	BR4_n									
L1	CLKIN	L3	CLK_CFG_1	L3	AGND	L4	CLK_CFG_2	L5	VDDEXT_L5	L6	VDDINT_L6	L7	GND_L7	L8	GND_L8	L9	GND_L9	L10	GND_L10	L11	GND_L11	L12	GND_L12	L13	GND_L13	L14	GND_L14	L15	VDDINT_L15	L16	VDDEXT_L16	L17	BR5_n	L18	BR5_n	L19	BR5_n	L20	BR5_n		
SharC_Clk	VDDEXT	GND	GND	VDDEXT	VDDINT	VDDINT	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	VDDINT	VDDEXT	BR5_n	BR5_n	BR5_n	BR5_n									
M1	AVDD	M2	CLK_CFG_3	M3	CLKOUT	M4	NC_M4	M5	VDDEXT_M5	M6	VDDINT_M6	M7	GND_M7	M8	GND_M8	M9	GND_M9	M10	GND_M10	M11	GND_M11	M12	GND_M12	M13	GND_M13	M14	GND_M14	M15	VDDINT_M15	M16	VDDEXT_M16	M17	PAGE	M18	PAGE	M19	PA_n	M20	PA_n		
AVDD	GND	GND	GND	VDDEXT	VDDINT	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	VDDINT	VDDEXT	PAGE	PAGE	PAGE	PAGE	PA_n	PA_n							
N1	NC_N1	N2	NC_N2	N3	DATA(48)	N4	DATA(51)	N5	VDDEXT_N5	N6	VDDINT_N6	N7	GND_N7	N8	GND_N8	N9	GND_N9	N10	GND_N10	N11	GND_N11	N12	GND_N12	N13	GND_N13	N14	GND_N14	N15	VDDINT_N15	N16	VDDEXT_N16	N17	L3DAT(5)	N18	L3DAT(6)	N19	L3DAT(4)	N20	L3CLK		
NC_N1	NC_N2	DATA(48)	DATA(51)	VDDEXT	VDDINT	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	VDDINT	VDDEXT	L3DAT(5)	L3DAT(6)	L3DAT(4)	L3CLK									
P1	DATA(49)	P2	DATA(50)	P3	DATA(52)	P4	DATA(55)	P5	VDDEXT_P5	P6	VDDINT_P6	P7	GND_P7	P8	GND_P8	P9	GND_P9	P10	GND_P10	P11	GND_P11	P12	GND_P12	P13	GND_P13	P14	GND_P14	P15	VDDINT_P15	P16	VDDEXT_P16	P17	L3DAT(2)	P18	L3DAT(1)	P19	L3DAT(3)	P20	L3ACK		
SharC(49)	SharC(50)	SharC(52)	SharC(55)	VDDEXT	VDDINT	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	VDDINT	VDDEXT	L3DAT(2)	L3DAT(1)	L3DAT(3)	L3ACK									
R1	DATA(53)	R2	DATA(54)	R3	DATA(57)	R4	DATA(60)	R5	VDDEXT_R5	R6	VDDINT_R6	R7	GND_R7	R8	GND_R8	R9	GND_R9	R10	GND_R10	R11	GND_R11	R12	GND_R12	R13	GND_R13	R14	GND_R14	R15	GND_R15	R16	VDDEXT_R16	R17	L4DAT(4)	R18	L4DAT(6)	R19	L4DAT(7)	R20	L3DAT(0)		
SharC(53)	SharC(54)	SharC(57)	SharC(60)	VDDEXT	VDDINT	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	VDDEXT	VDDEXT	L4DAT(4)	L4DAT(6)	L4DAT(7)	L3DAT(0)									
T1	DATA(56)	T2	DATA(58)	T3	DATA(59)	T4	DATA(63)	T5	VDDEXT_T5	T6	VDDINT_T6	T7	VDDINT_T7	T8	VDDINT_T8	T9	VDDINT_T9	T10	VDDINT_T10	T11	VDDINT_T11	T12	VDDINT_T12	T13	VDDINT_T13	T14	VDDINT_T14	T15	VDDINT_T15	T16	VDDEXT_T16	T17	L4DAT(5)	T18	L4DAT(6)	T19	L4CLK	T20	L4DAT(4)		
SharC(56)	SharC(58)	SharC(59)	SharC(63)	VDDEXT	VDDINT	VDDINT	VDDINT	VDDINT	VDDINT	VDDINT	VDDINT	VDDINT	VDDINT	VDDINT	VDDINT	VDDINT	VDDINT	VDDINT	VDDINT	VDDINT	VDDINT	VDDINT	VDDINT	VDDINT	VDDINT	VDDINT	VDDINT	VDDEXT	VDDEXT	L4DAT(5)	L4DAT(6)	L4CLK	L4DAT(4)								
U1	DATA(61)	U2	DATA(62)	U3	ADDR(3)	U4	ADDR(2)	U5	VDDEXT_U5	U6	VDDEXT_U6	U7	VDDEXT_U7	U8	VDDEXT_U8	U9	VDDEXT_U9	U10	VDDEXT_U10	U11	VDDEXT_U11	U12	VDDEXT_U12	U13	VDDEXT_U13	U14	VDDEXT_U14	U15	VDDEXT_U15	U16	VDDEXT_U16	U17	LOGAT(7)	U18	L4DAT(0)	U19	L4DAT(1)	U20	L4DAT(2)		
SharC(61)	SharC(62)	SharC(3)	SharC(2)	VDDEXT	VDDEXT	VDDEXT	VDDEXT	VDDEXT	VDDEXT	VDDEXT	VDDEXT	VDDEXT	VDDEXT	VDDEXT	VDDEXT	VDDEXT	VDDEXT	VDDEXT	VDDEXT	VDDEXT	VDDEXT	VDDEXT	VDDEXT	VDDEXT	VDDEXT	VDDEXT	VDDEXT	VDDEXT	VDDEXT	VDDEXT	LOGAT(7)	L4DAT(0)	L4DAT(1)	L4DAT(2)							
V1	ADDR(4)	V2	ADDR(6)	V3	ADDR(7)	V4	ADDR(10)	V5	ADDR(14)	V6	ADDR(18)	V7	ADDR(22)	V8	ADDR(25)	V9	ADDR(28)	V10	ID0	V11	ADDR(1)	V12	MS1_n	V13	CSB_n	V14	RD1_n	V15	RD2_n	V16	ADDR(5)	V17	ADDR(2)	V18	L5ACK	V19	L5DAT(4)	V20	L5DAT(6)		
SharC(4)	SharC(6)	SharC(7)	SharC(10)	SharC(14)	SharC(18)	SharC(22)	SharC(25)	SharC(28)	GND	SharC(1)	MS1_n	CSB_n	RD1_n	RD2_n	ADDR(5)	ADDR(2)	L5ACK	L5DAT(4)	L5DAT(6)																						
W1	ADDR(5)	W2	ADDR(9)	W3	ADDR(12)	W4	ADDR(15)	W5	ADDR(17)	W6	ADDR(20)	W7	ADDR(23)	W8	ADDR(26)	W9	ADDR(29)	W10	ID1	W11	ADDR(0)	W12	BMSB_n	W13	MS2_n	W14	CIF_n	W15	RDH_n	W16	DMAG2_n	W17	BOOT	W18	L5DAT(1)	W19	L5DAT(3)	W20	L5DAT(5)		
SharC(5)	SharC(9)	SharC(12)	SharC(15)	SharC(17)	SharC(20)	SharC(23)	SharC(26)	SharC(29)	VDDEXT	SharC(0)	BMSB_n	MS2_n	CIF_n	RDH_n	DMAG2_n	BOOT	L5DAT(1)	L5DAT(3)	L5DAT(5)																						
X1	ADDR(8)	X2	ADDR(11)	X3	ADDR(13)	X4	ADDR(16)	X5	ADDR(19)	X6	ADDR(21)	X7	ADDR(24)	X8	ADDR(27)	X9	ADDR(30)	X10	ADDR(31)	X11	ID2	X12	BRST	X13	MS0_n	X14	MS3_n	X15	WRH_n	X16	WRL_n	X17	DMAG1_n	X18	DMAR1_n	X19	BOOT	X20	L5CLK		
SharC(8)	SharC(11)	SharC(13)	SharC(16)	SharC(19)	SharC(21)	SharC(24)	SharC(27)	SharC(30)	SharC(31)	GND	BRST	MS0_n	MS3_n	WRH_n	WRL_n	DMAG1_n	DMAR1_n	BOOT	L5CLK																						

Clock Configuration:
CLK_CFG(3:0) = "0010"
=> Core / CLKIN Ration 2:1

SHARC B
ID = "010"

Booting Mode:
EBOOT = '0', LBOOT = '0', BMS_n = '1' (Input)
=> Host Port Booting

SHARC Power pins:
VDDINT (1V9) 40 pins
VDDEXT (3V3) 43 pins
GND 82 pins
NC 9 pins

MROD-Out		Rev	V2	3			
		Date	7 Feb 2006				
SHARC-B		Time	1:25:46 pm				
Proj: MROD-X	Proj.No: 38405	Name	tonvr				
Peter Jansweijer	peterj@nikhef.nl	Size	A3	4	1	4	A
NIKHEF <small>© ET-Nikhef Amsterdam</small>		<small>NATIONAAL INSTITUUT VOOR KERN-FYSICA EN HOGE ENERGIE-FYSICA KRUISLAAN 409, 020-592 2000 1098 SJ AMSTERDAM NEDERLAND</small>					
		Dim	420 x 297 mm				
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