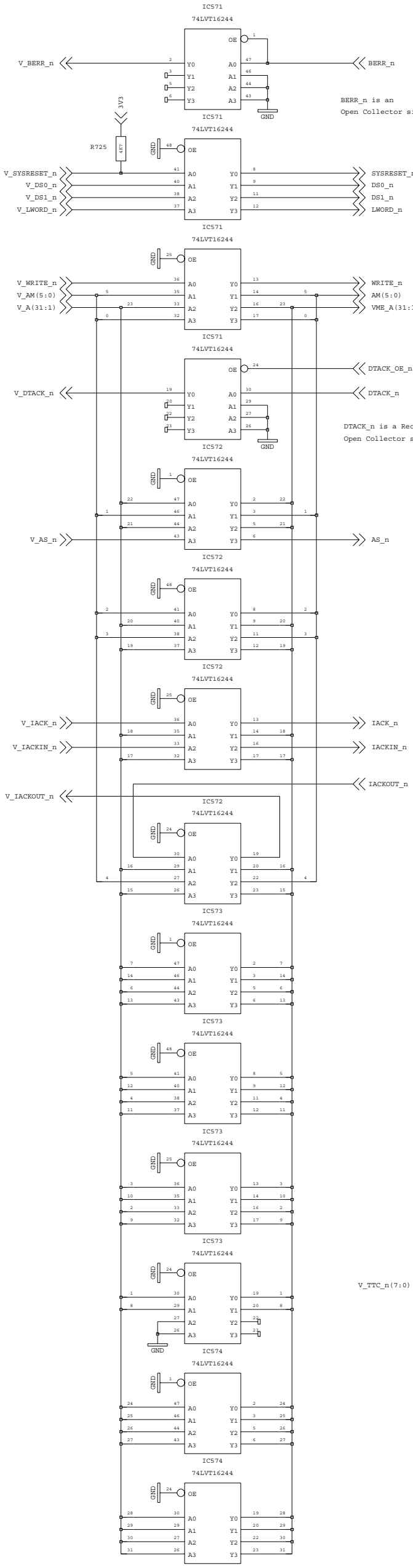
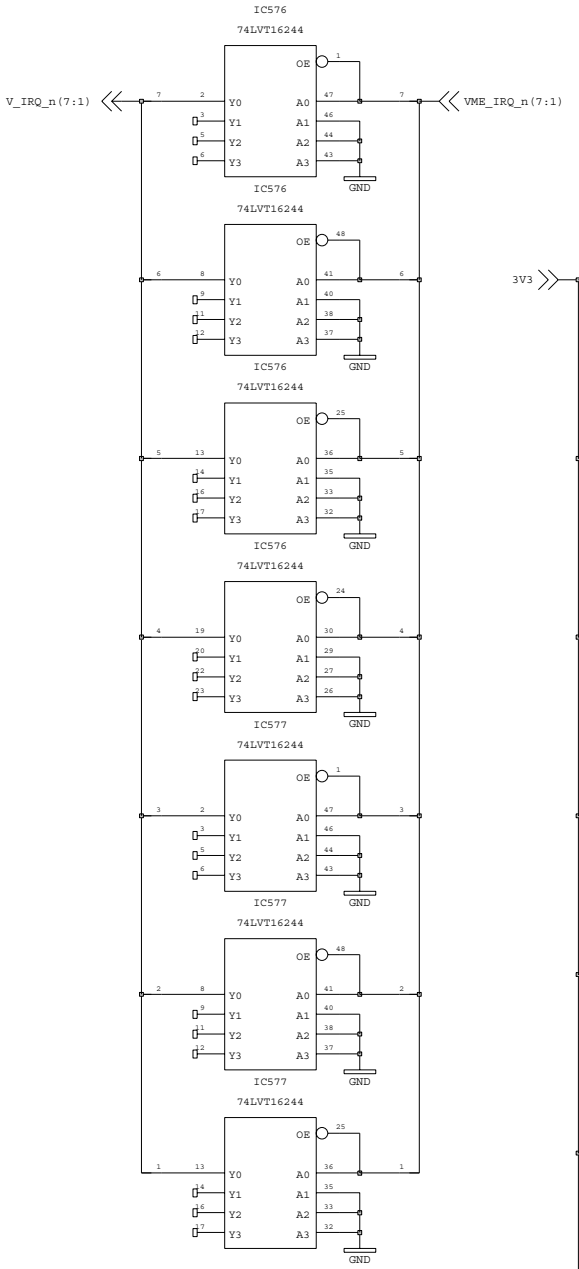
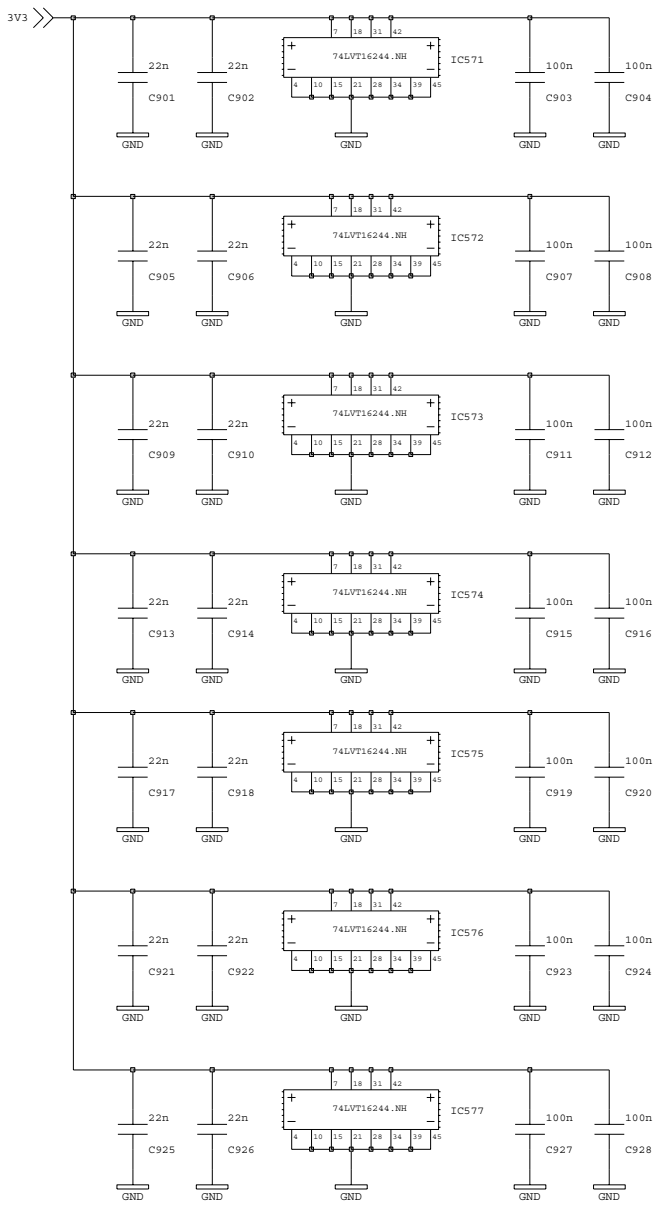
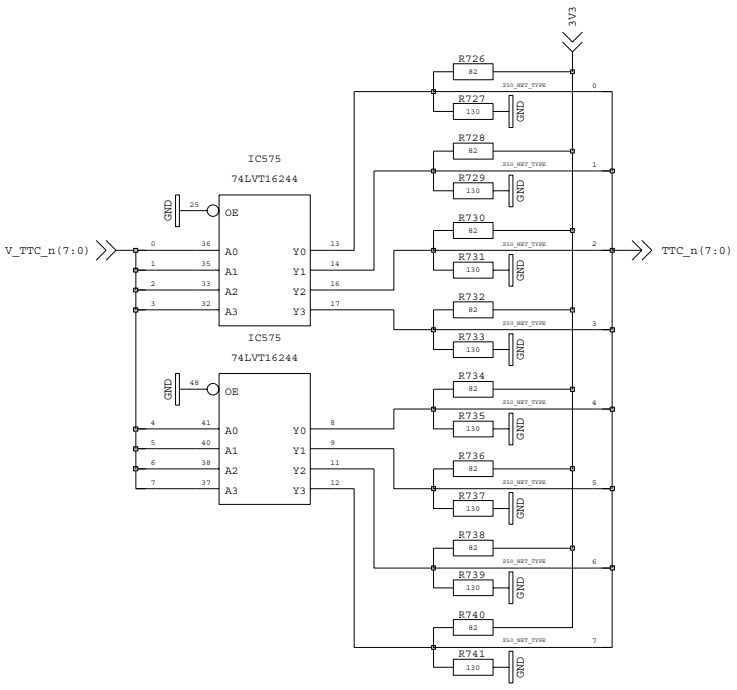
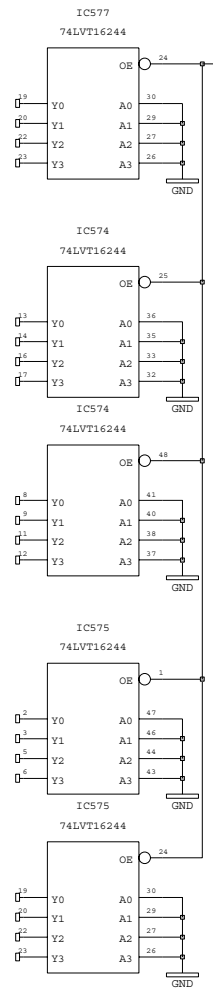
Note: VME signals are 5V.
Most Logic on the MROD is NOT 5V tolerant!
Therefore all VME signals need to be
buffered by a 3V3 (but 5V tolerant) buffer.











MROD-Out		Rev V2 3
		Date 7 Feb 2006
VME Bus Other Buffers		Time 1:35:25 pm
Proj: MROD-X	Proj.No:	Name Ton van Reen
Peter Jansweijer	peterj@nikhef.nl	
NIKHEF	NATIONAAL INSTITUUT VOOR KERN-FYSICA EN HOGE ENERGIE-FYSICA KRUISLAAN 409, 020-592 2000 1098 SJ AMSTERDAM NEDERLAND	Size A3 4 1 4 A
© ET-Nikhef Amsterdam		Dim 297 x 420 mm
		Page 17 of 19