



2V5 @ 1,5A

3V3

IC6005
LT1963A-DD

R6005
2%

R6006
2%

R6007
2%

C6098
47u

C6099
100n

C6100
100n

C6101
47u

AVX TAJB476K010R

VCCA_MGT => MGT Power (estimated 31 + 49 = 80 mA)

VT2V5_MGT => MGT TX (RX) Termination (estimated 11 mA)



Series Termination for Not (yet) used



Series Termination for Sharc Addressbus



Series Termination for General purpose



Series Termination for Buffer Memory interface

channel_in		Rev	V2	2
		Date	7 Feb 2006	
Input FPGA MGT Pwr Decoupling, Termination		Time	1:52:01 pm	
Proj:	MROD-X	Proj.No:	38405	
Peter Jansweijer		peterj@nikhef.nl		
NIKHEF <small>NATIONAAL INSTITUUT VOOR KERN-FYSICA EN HOGE ENERGIE-FYSICA KRUISLAAN 409, 020-592 2000 1098 SJ AMSTERDAM NEDERLAND</small>		Size	A3	4 1 4 A
		Dim	420 x 297 mm	
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