M2 M1 M0
0 1 1 Master SelectMAP
1 1 0 Slave SelectMAP (default)
1 0 1 Boundary scan

ROD_Busy
FPGA Config
ASP Connected

JTAG Parallel IV Connector

TEST 50ohm
TEST LVDS

SER NR:

FEMB-D-40-X-3.3-160

NIKHEF
Amsterdam

LAYERS
The house of PCB design

LocalBAR

Sharc JTAG Chain

MROD-X
ET38405.01.05.V1
rev.date:050121

This product includes technical information
created and made available by CERN.

TEST LVDS
TEST 50ohm

FLAG0a
FLAG1a
FLAG2a
FLAG3a
FLAG0b
FLAG1b
FLAG2b
FLAG3b

LEDs(0)
LEDs(1)
LEDs(2)
LEDs(3)

5A
16A