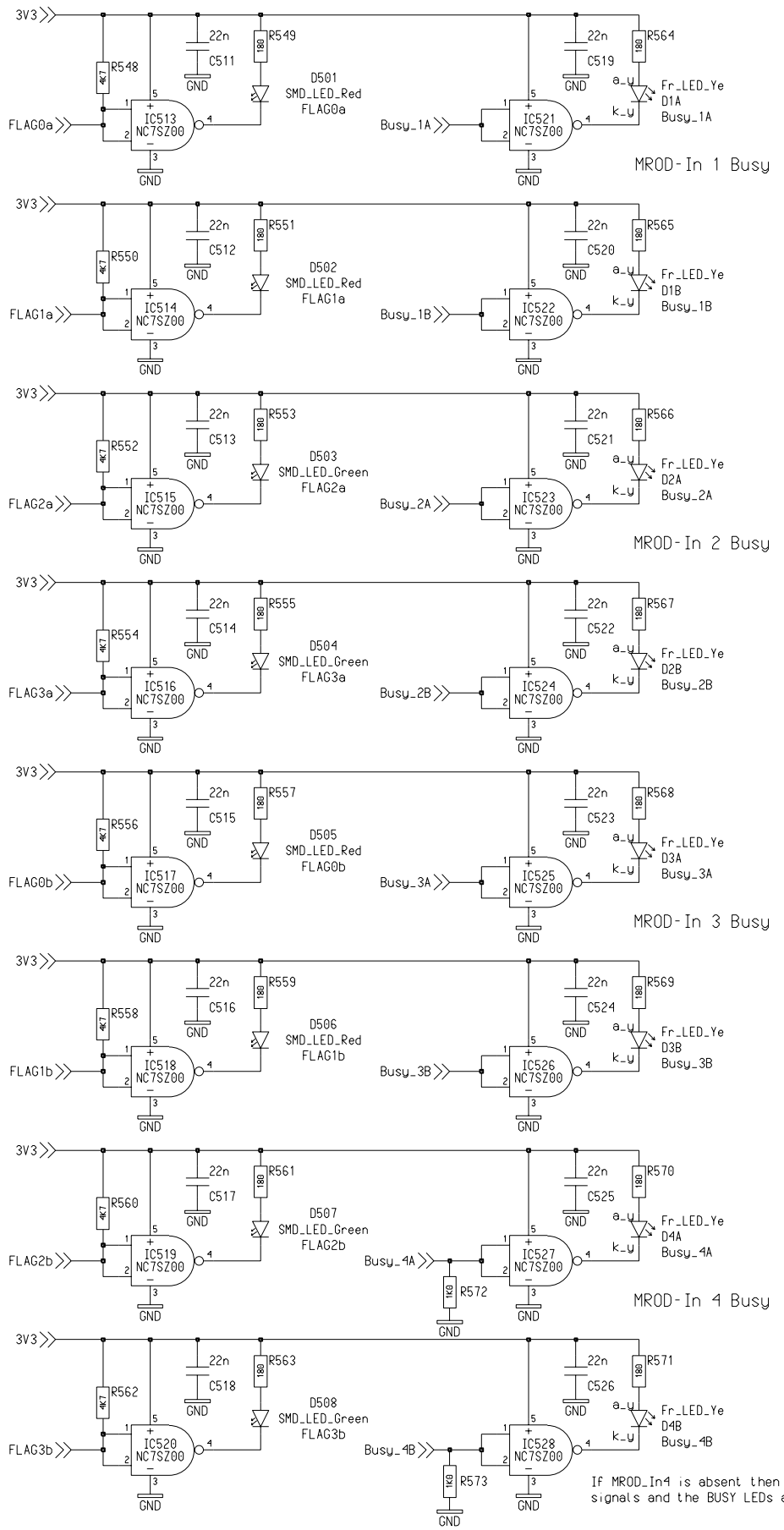
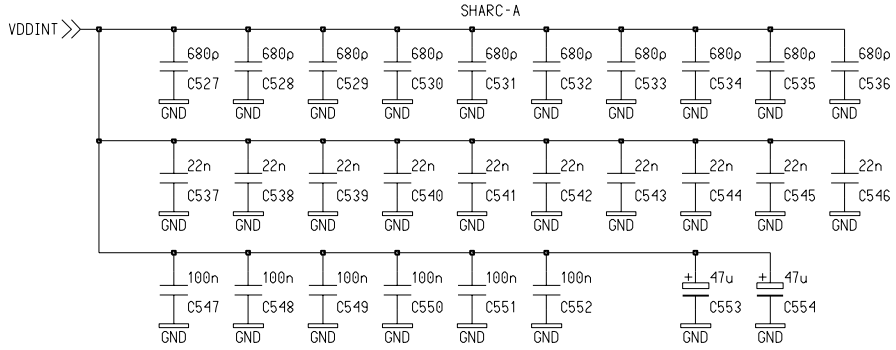
Sharc Flag pin is an Input by default after Power-Up.
This means the LEDs light up during and after Power-Up.

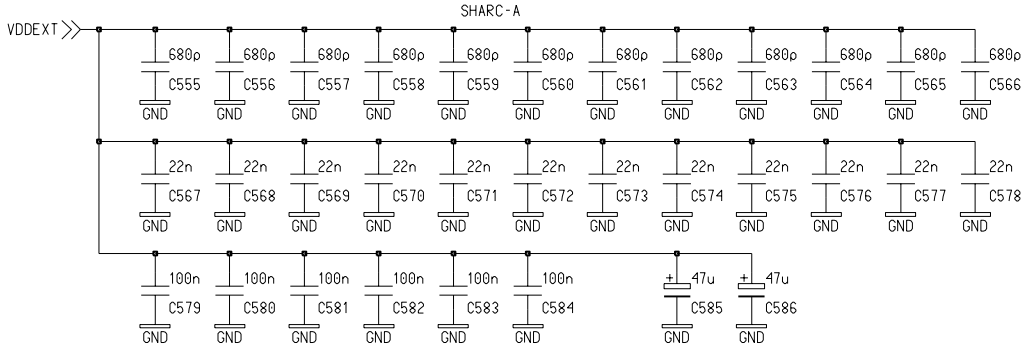


If MROD_In4 is absent then the BUSY signals and the BUSY LEDs are disabled!

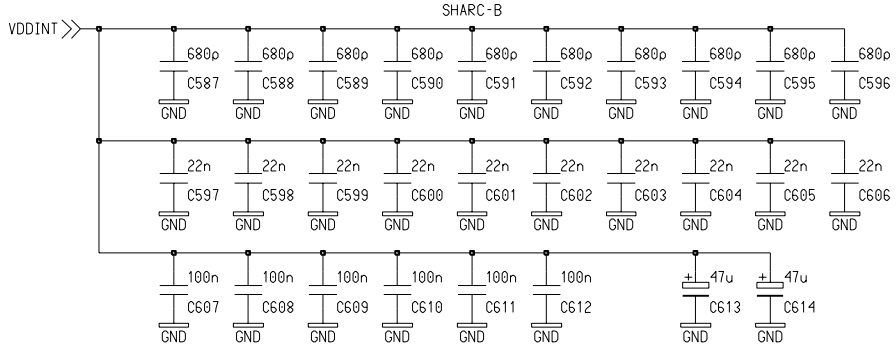
ADSP21160 VDDINT (40 pins) Decoupling Capacitors
SHARC-A



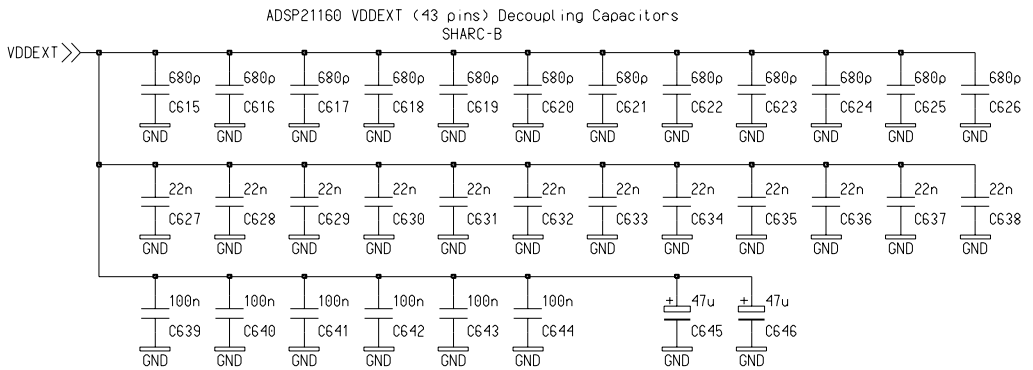
ADSP21160 VDDEXT (43 pins) Decoupling Capacitors
SHARC-A



ADSP21160 VDDINT (40 pins) Decoupling Capacitors
SHARC-B



ADSP21160 VDDEXT (43 pins) Decoupling Capacitors
SHARC-B



MROD-Out		Rev 28
SHARC Power Supply Decoupling and LEDs		Date 26 Jan 2005
Proj: MROD-X	Proj.No:38405	Time 11:59:01 am
Peter Jansweijer	peterj@nikhef.nl	Name peterj
NIKHEF ©ET-Nikhef Amsterdam	NATIONAAL INSTITUUT VOOR KERN-FYSICA EN HOGE ENERGIE-FYSICA KRUISLAAN 409, 020-592 2000 1098 SJ AMSTERDAM NEDERLAND	Size A3 4 1 4 A
		Dim 420 x 297 mm
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