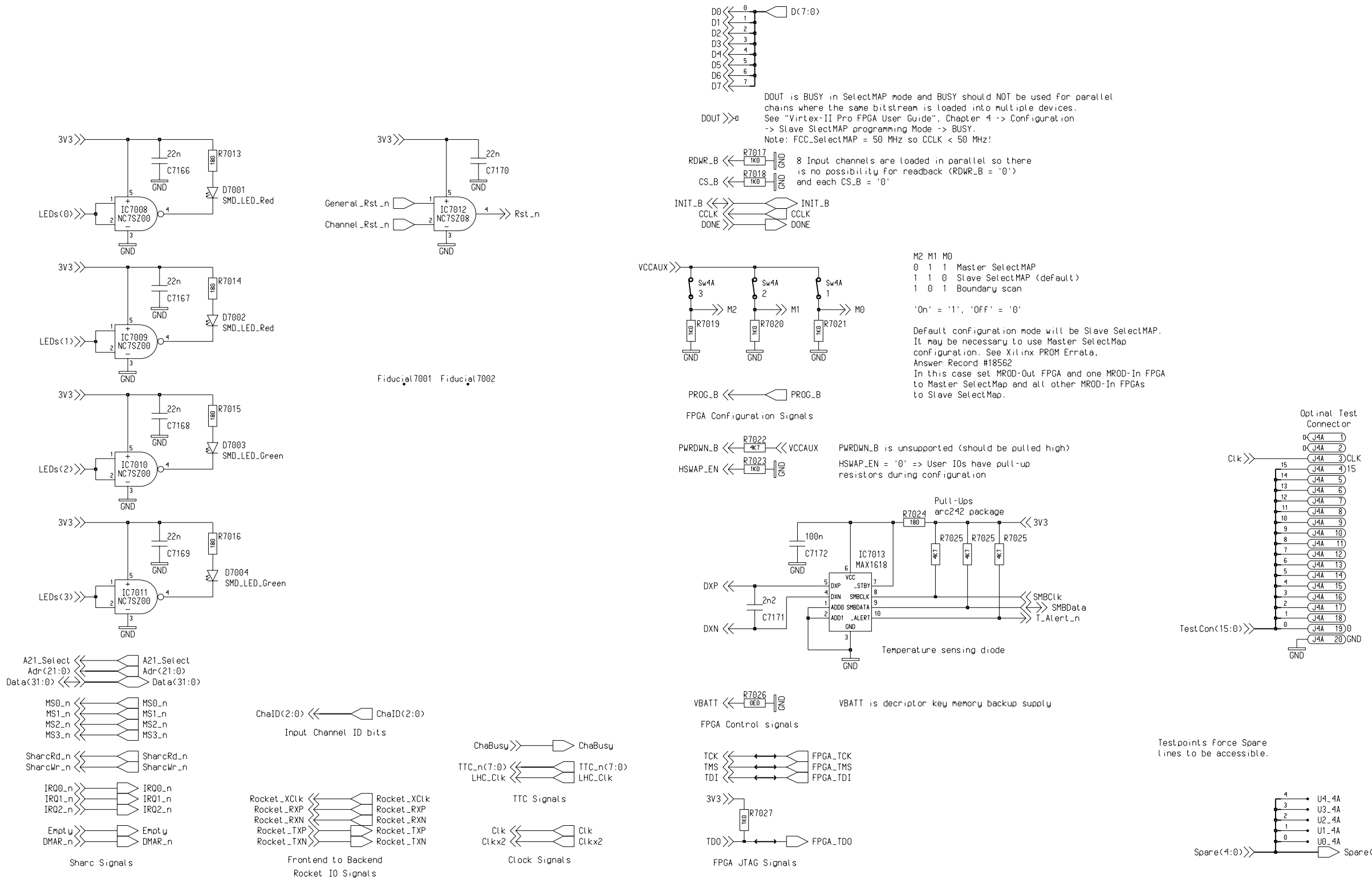

channel_in	Rev 80
Input FPGA Auxiliary Connections	Date 26 Jan 2005
Proj: MROD-X / Proj.No:38405	Time 12:11:01 pm
Peter Jansweijer / peterj@nikhef.nl	Name Peter Jansweijer
NIKHEF ©ET-Nikhef Amsterdam — NATIONAAL INSTITUUT VOOR KERNFYSICA EN HOGE ENERGIE-FYSICA, KRUISLAAN 409, 020-592 2000, 1098 SJ AMSTERDAM NEDERLAND	Size A3 / 4 1 4 A
	Dim 420 x 297 mm
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