



Series Termination for Not (yet) used

Series Termination for Not (yet) used

Series Termination for Shared Addressbus

Series Termination for Shared Addressbus

Series Termination for General purpose

Series Termination for SDRAM Interface

Series Termination for Buffer Memory interface

Series Termination for General purpose

channel_in		Rev	50
Input FPGA MGT Pur Decoupling, Termination		Date	26 Jan 2005
Proj: MKOD-X	Proj: No:38405	Time	12:10:26 pm
Peter Jansweijer	peter.j@nikhef.nl	Name	peter.j

NIKHEF
 NATIONAL INSTITUUT VOOR KERN-
 FYSICA EN HOOG ENERGIE-FYSICA
 KRUISLAAN 409 020-532 2000
 1098 SJ AMSTELDAM NEDERLAND

Size A3 4 1 4 A
 Dim 420 x 297 mm
 Page 4 of 6