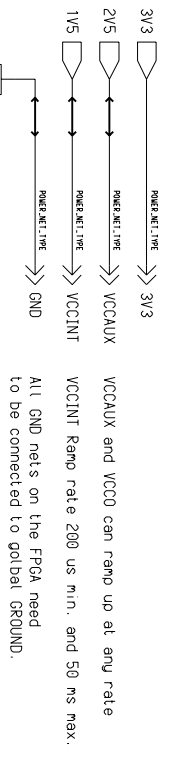


XC2VP1/20 VCCINT Decoupling Capacitors
Min. Power-On Current XC2VP20
IccINT = 600 mA

XC2VP1/20 VCCAUX Decoupling Capacitors
Min. Power-On Current XC2VP20
IccAUX = 250 mA (min)

Power Inputs
For Xilinx Virtex-II Pro, power supplies
can be turned on in any sequence.



channel_in		Rev	45
Input FPGA Power Supply Decoupling		Date	26 Jan 2005
Proj: MR00-X	Proj.No:38405	Time	12:10:10 pm
Peter Jansweijer	petern@nikhef.nl	Name	Peter Jansweijer
NATIONAAL INSTITUUT VOOR KERN- FYSICA EN HOOG ENERGIE-FYSICA KRUISLAAN 409, 020-532 2000 1098 SJ AMSTERDAM NEDERLAND		Size	A3 4 1 4 A
©ET-Nikhef Amsterdam		Dim	420 x 297 mm
		Page	3 of 6