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| channel_in | | Rev | 45 |
| Input FPGA Power Supply Decoupling | | Date | 26 Jan 2005 |
| Proj: MR00-X | Proj.No:38405 | Time | 12:10:10 pm |
| Peter Jansweijer | | Name | Peter Jansweijer |
| petern@nikhef.nl | | Size | A3 |
| NATIONAAL INSTITUUT VOOR KERN- FYSICA EN HOOG ENERGIE-FYSICA KRUISLAAN 409, 020-532 2000 1098 SJ AMSTERDAM NEDERLAND | | Dim | 420 x 297 mm |
| ©ET-Nikhef Amsterdam | | Page | 3 of 6 |

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|---|---|---|---|---|---|---|---|---|---|----|----|----|----|----|----|----|----|----|
| A | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | 13 | 14 | 15 | 16 | 17 | 18 |
| B | | | | | | | | | | | | | | | | | | |
| C | | | | | | | | | | | | | | | | | | |
| D | | | | | | | | | | | | | | | | | | |
| E | | | | | | | | | | | | | | | | | | |
| F | | | | | | | | | | | | | | | | | | |
| G | | | | | | | | | | | | | | | | | | |
| H | | | | | | | | | | | | | | | | | | |
| I | | | | | | | | | | | | | | | | | | |
| J | | | | | | | | | | | | | | | | | | |
| K | | | | | | | | | | | | | | | | | | |
| L | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | 13 | 14 | 15 | 16 | 17 | 18 |