



Power Inputs
For Xilinx Virtex-II Pro, power supplies
can be turned on in any sequence.

VCCAUX and VCC0 can ramp up at any rate
VCCINT Ramp rate 200 us min. and 50 ms max.
All GND nets on the FPGA need
to be connected to global GROUND.

channel_in		Rev	45
Input FPGA Power Supply Decoupling		Date	26 Jan 2005
Proj: MR00-X	Proj.No:38405	Time	12:10:10 pm
Peter Jansweijer	peter.j@nikhef.nl	Name	Peter Jansweijer
Size A3		1 1 4 A	
Dim 420 x 297 mm			
Page 3 of 6			

NIKHEF
NATIONAAL INSTITUUT VOOR KERN-
FYSICA EN HOOG ENERGIE-FYSICA
KRUISLAAN 409, 020-532 2000
1098 SJ AMSTERDAM NEDERLAND
©ET-Nikhef Amsterdam

