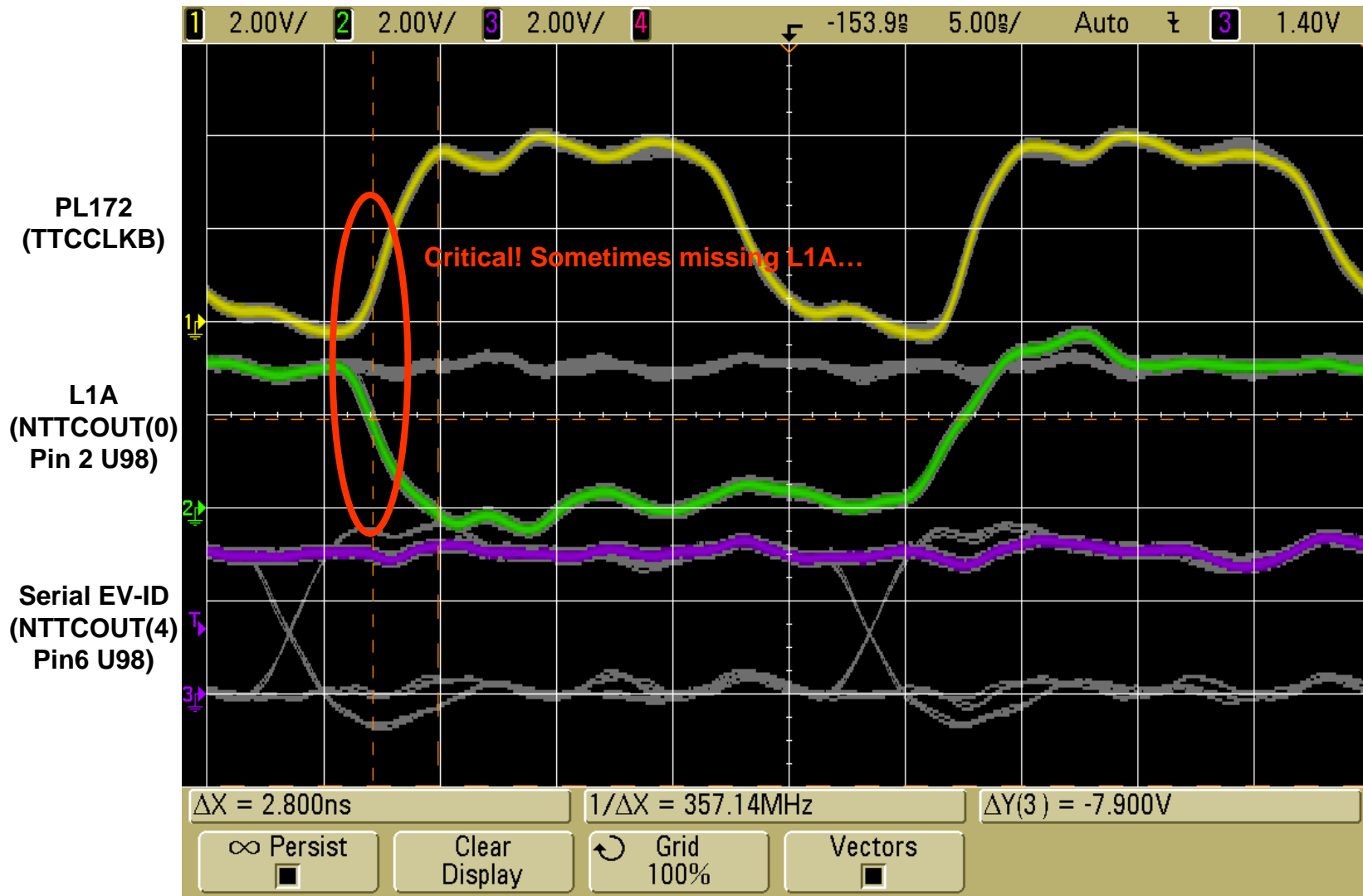


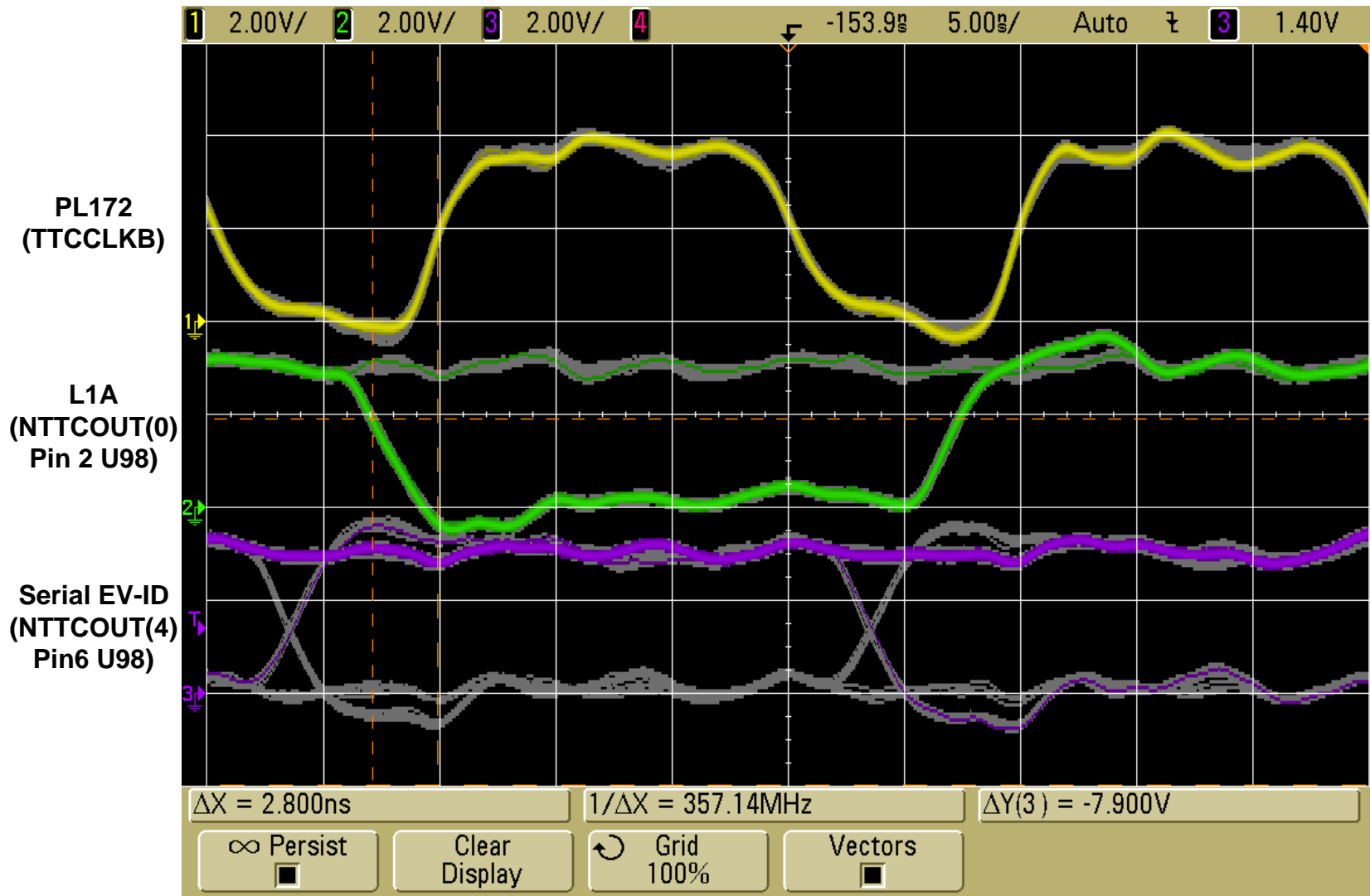
Backplane driver timing in Stand Alone mode

MDT016; SW8[6:1] = 011111 = 31; SW7[6:1] = 010001 = 17



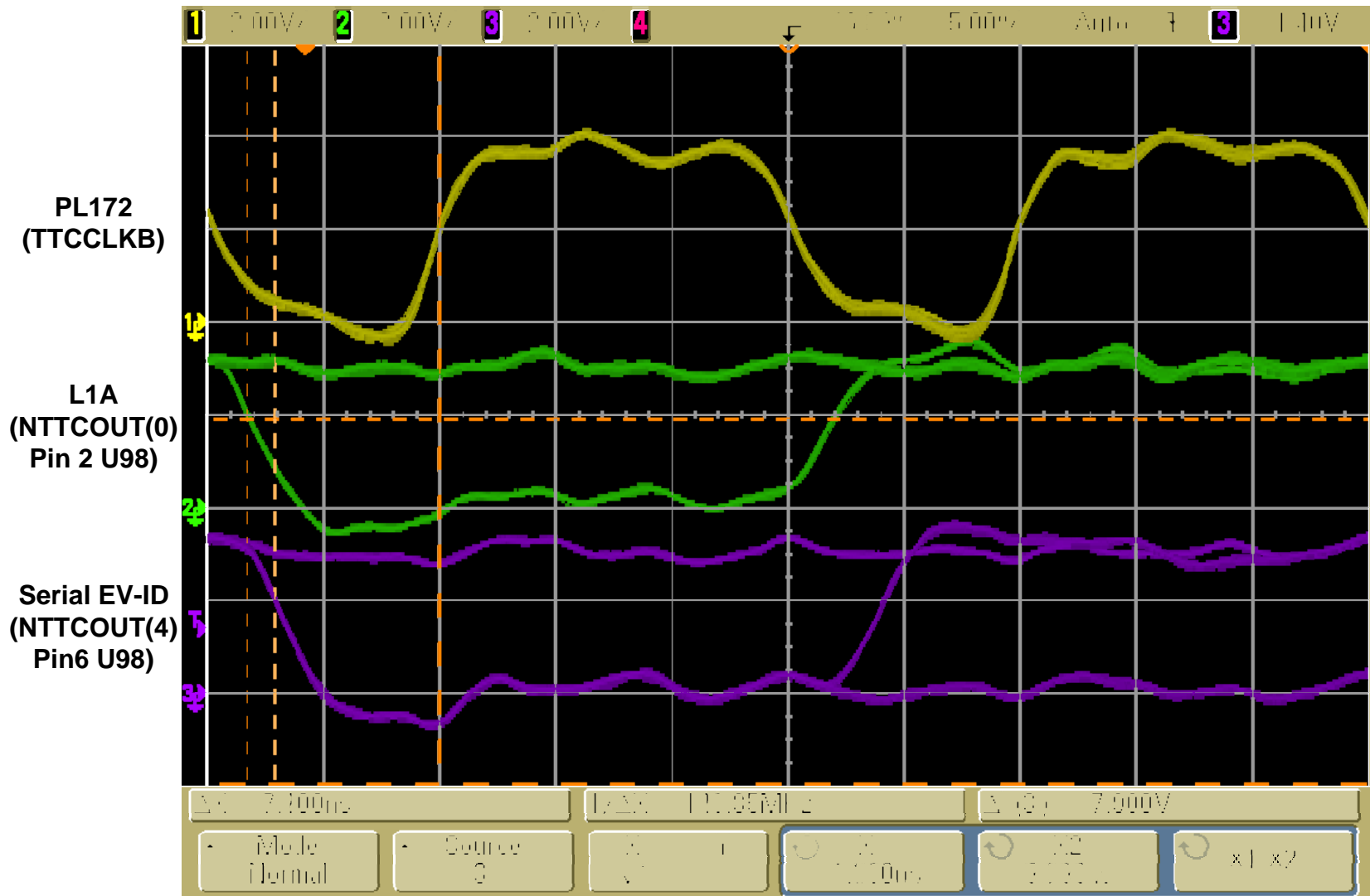
Backplane driver timing in Stand Alone mode

MDT016; SW8[6:1] = 011111 = 31; SW7[6:1] = 011000 = 24



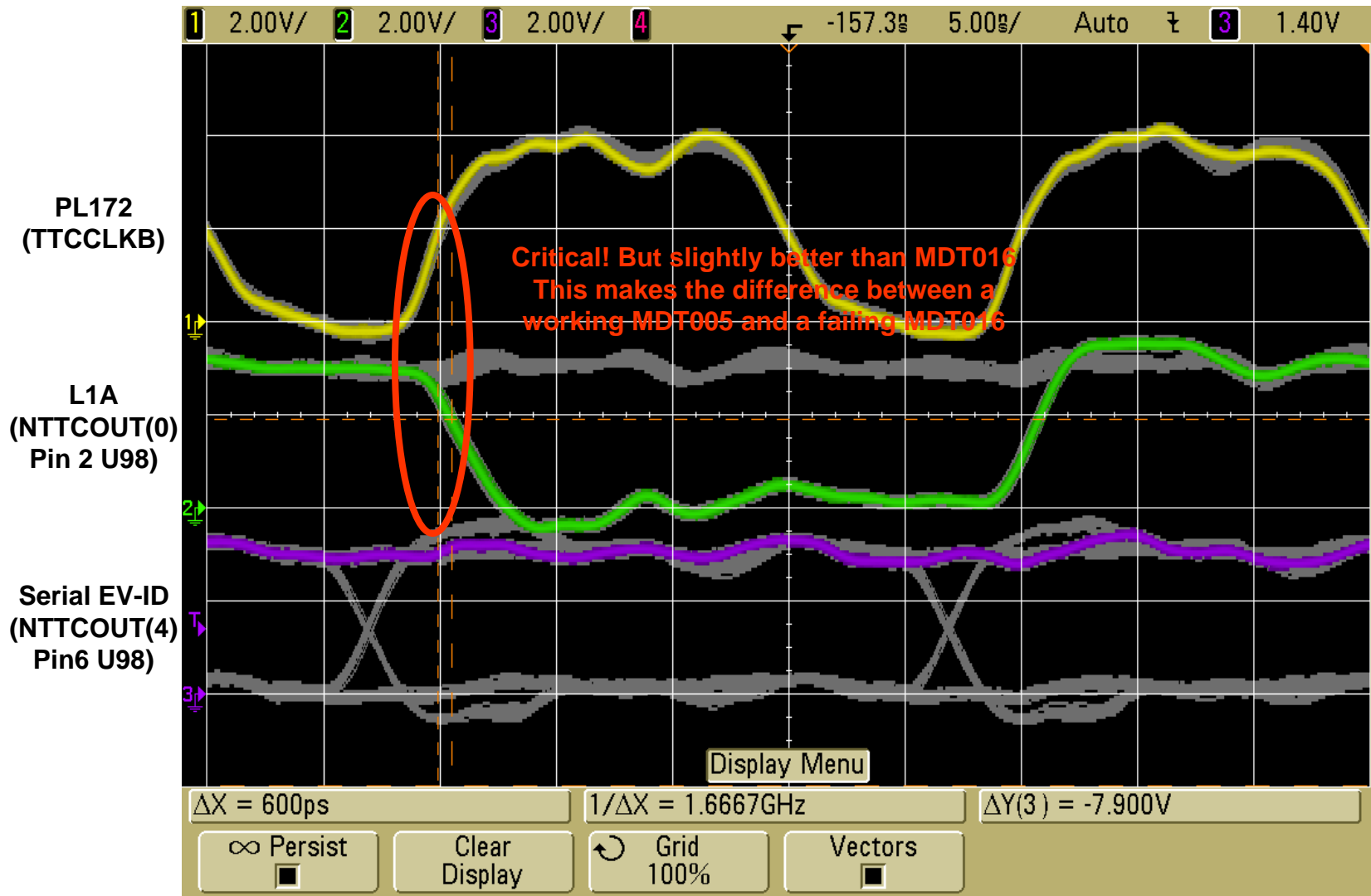
Backplane driver timing in Run mode (TTC)

MDT016; SW8[6:1] = 011111 = 31; SW7[6:1] = 011000 = 24



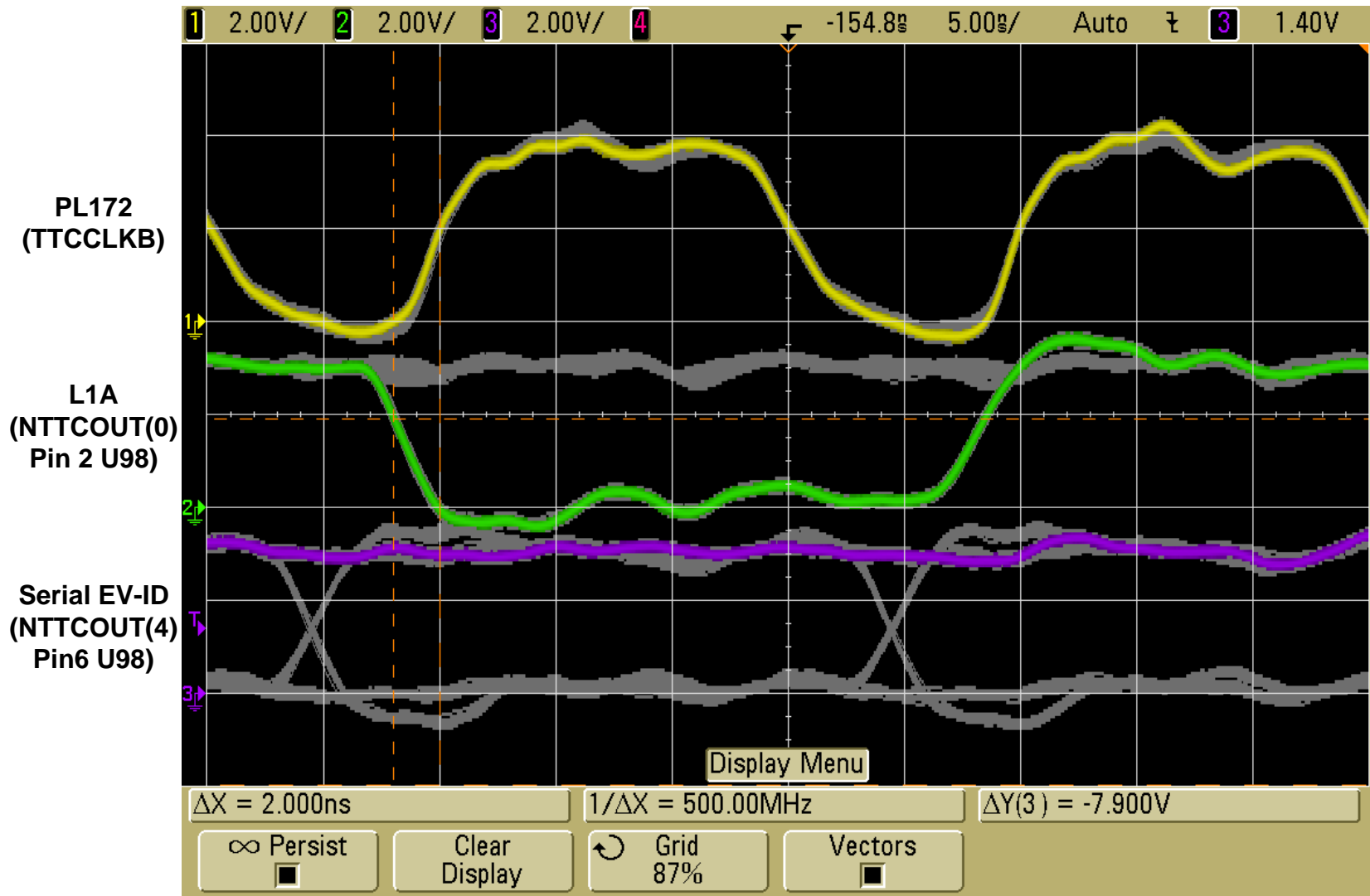
Backplane driver timing in Stand Alone mode

MDT005; SW8[6:1] = 011111 = 31; SW7[6:1] = 010001 = 17



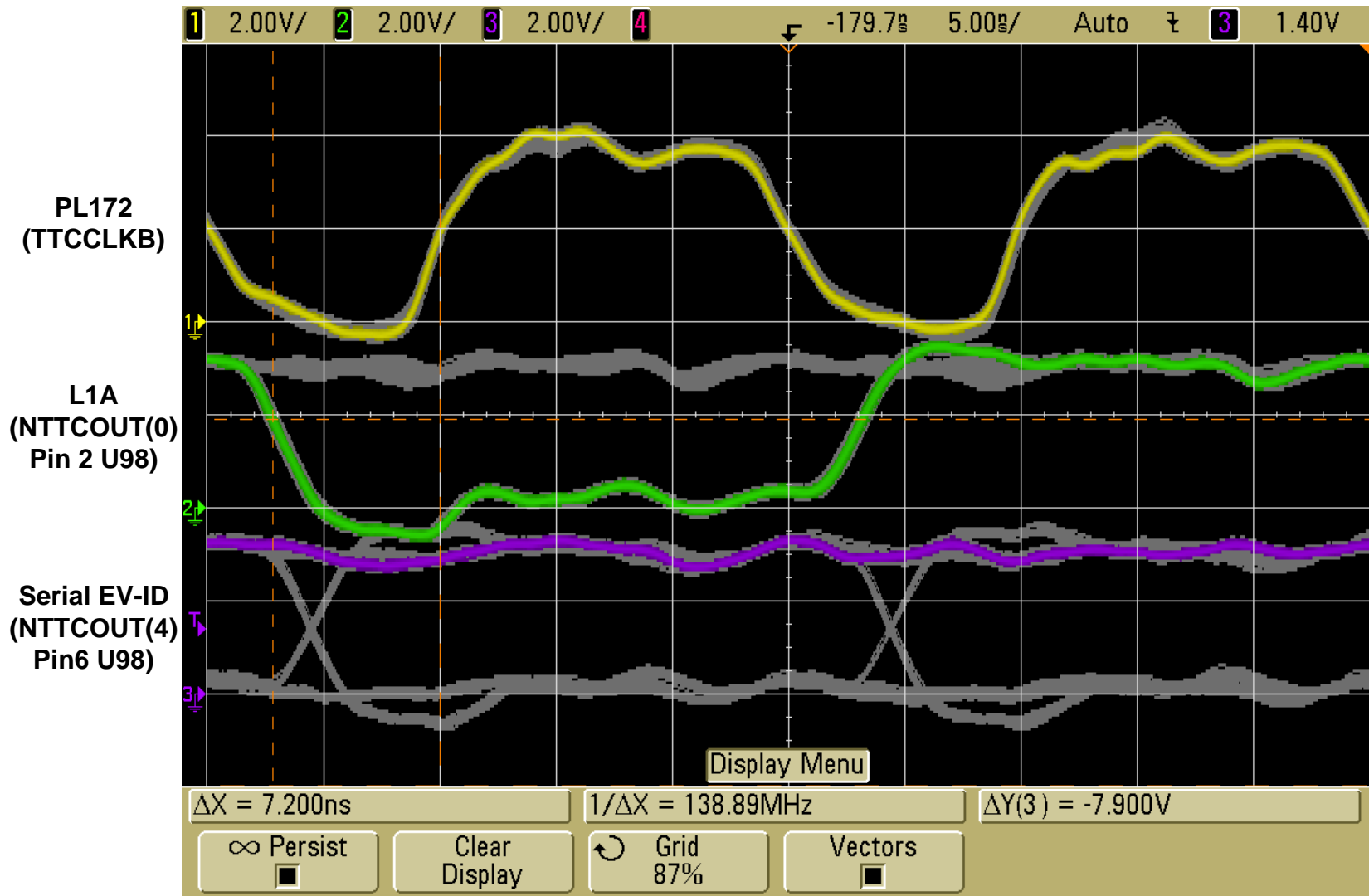
Backplane driver timing in Stand Alone mode

MDT005; SW8[6:1] = 011111 = 31; SW7[6:1] = 011000 = 24



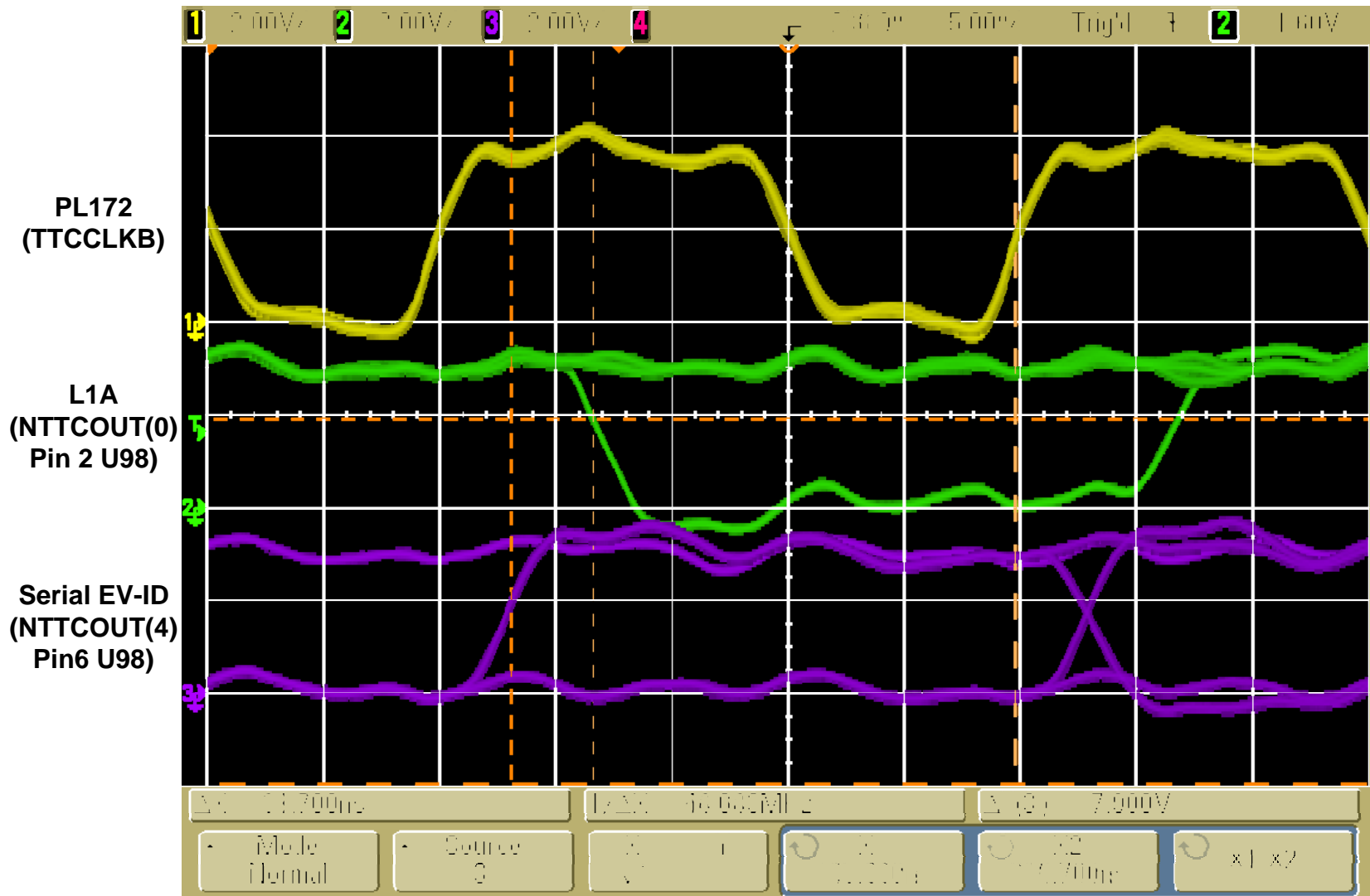
Backplane driver timing in Run mode (TTC)

MDT005; SW8[6:1] = 011111 = 31; SW7[6:1] = 011000 = 24



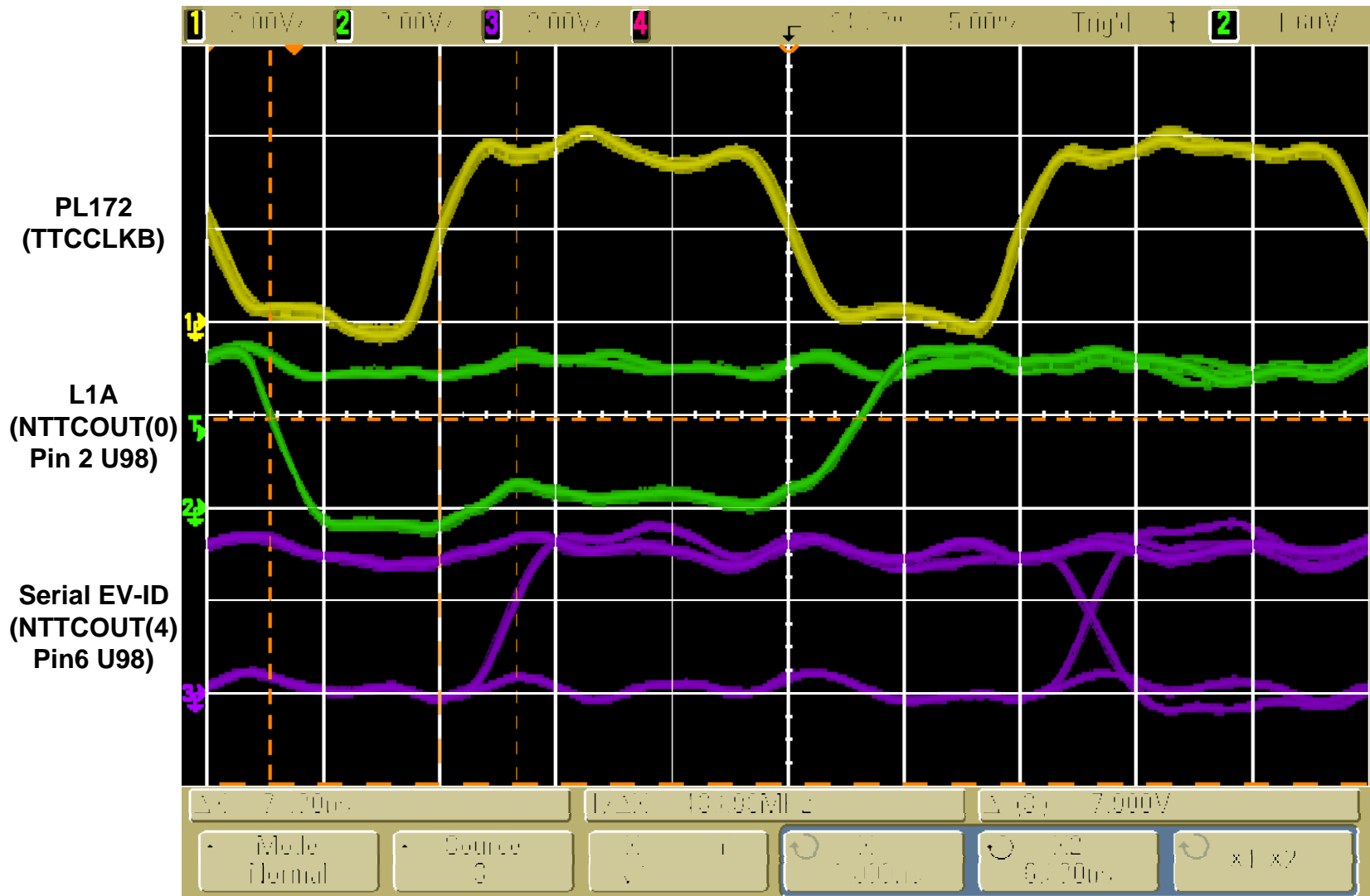
Backplane driver timing in Stand Alone mode

MDT005; SW8[6:1] = 011111 = 31; SW7[6:1] = 000110 = 6



Backplane driver timing in Run mode (TTC)

MDT005; SW8[6:1] = 011111 = 31; SW7[6:1] = 000110 = 6



- mro-tim.sh 20 2 (Mrod slot 20, size 2)
- mri-tim.sh 20 2 (Mrod slot 20, size 2)
- tim-trig-ecr 0 20 10 (600 Khz, Mrod slot 20, ECR/10sec)

OR

- tim-ttc 20 +
- ttcvi -> ttc
Ecr
Ran 5k